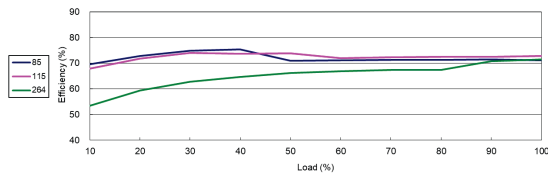


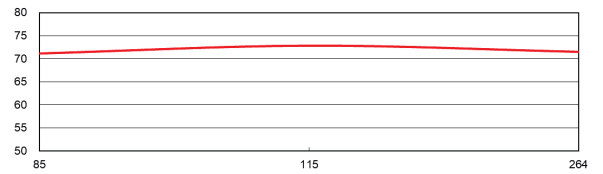
### Characteristic Curves

#### TMPS 03-103

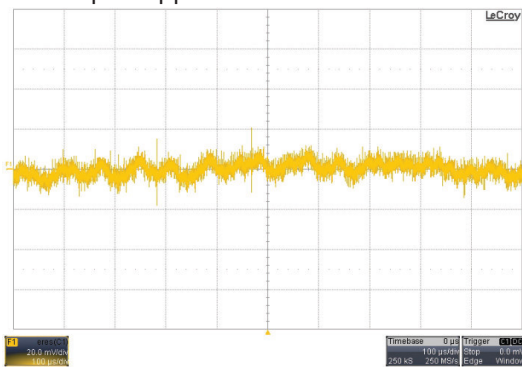
Efficiency vs Output Load



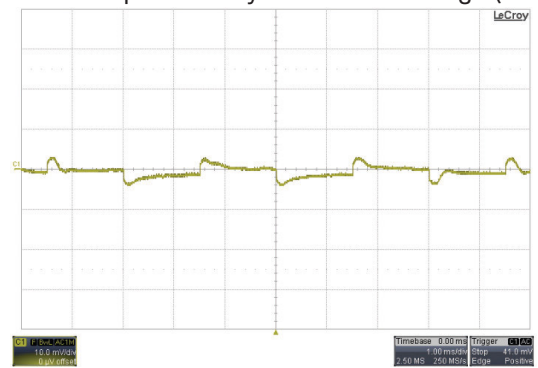
Efficiency vs Input Voltage



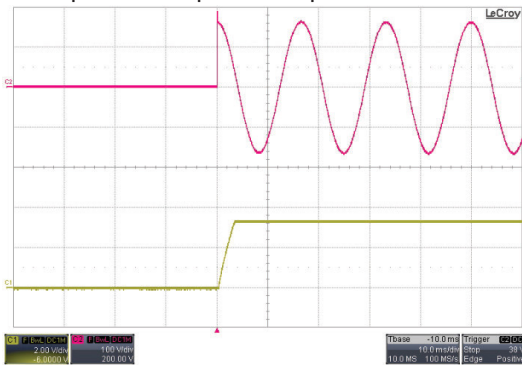
Typical Output Ripple and Noise



Transient Response to Dynamic Load Change (25%)

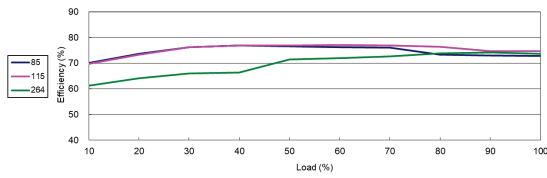


Typical Input Start-Up and Output Rise Characteristic

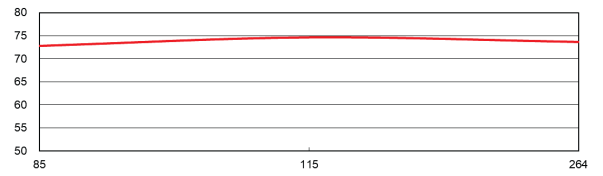


### TMPS 03-105

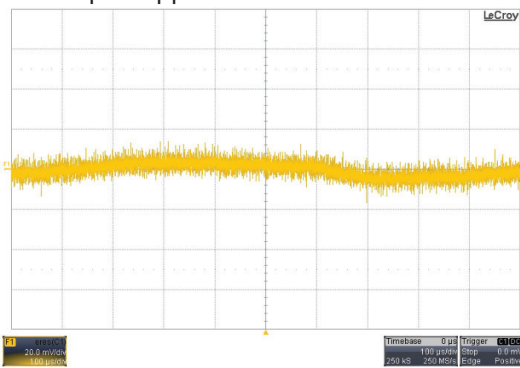
Efficiency vs Output Load



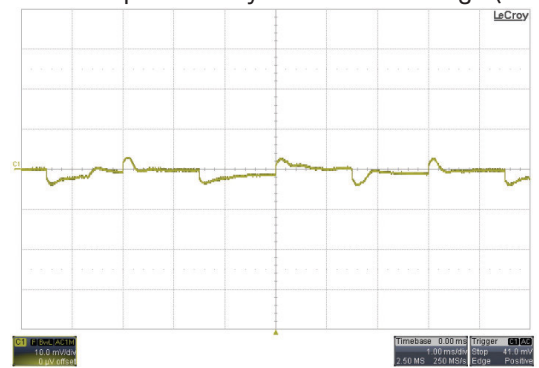
Efficiency vs Input Voltage



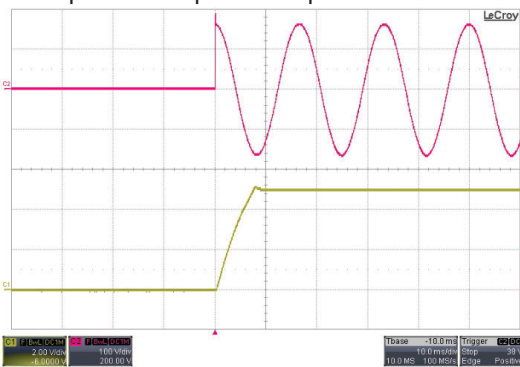
Typical Output Ripple and Noise



Transient Response to Dynamic Load Change (25%)

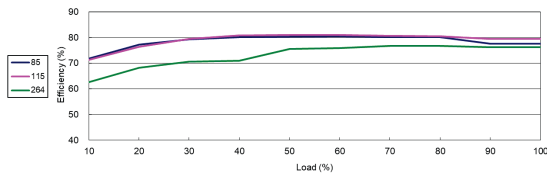


Typical Input Start-Up and Output Rise Characteristic

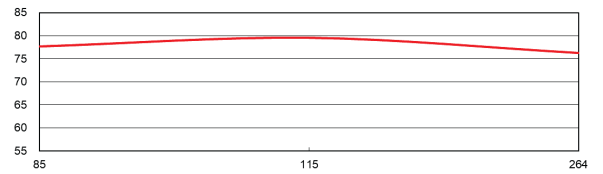


### TMPS 03-109

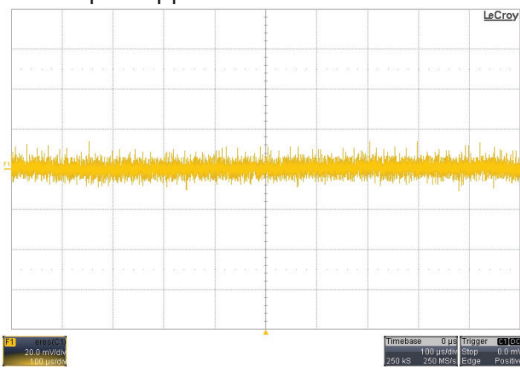
Efficiency vs Output Load



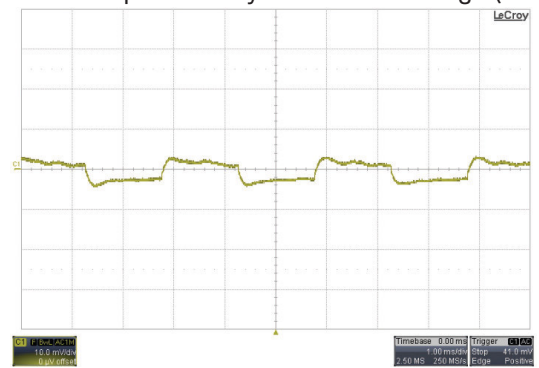
Efficiency vs Input Voltage



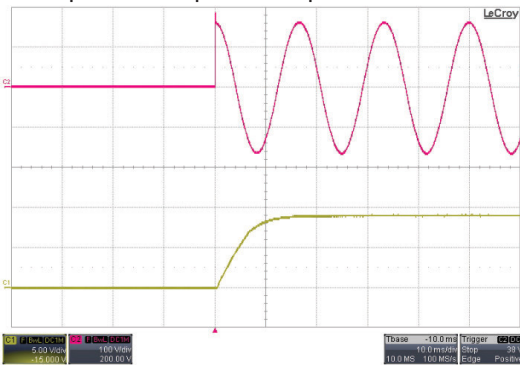
Typical Output Ripple and Noise



Transient Response to Dynamic Load Change (25%)

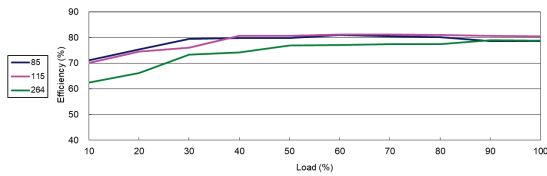


Typical Input Start-Up and Output Rise Characteristic

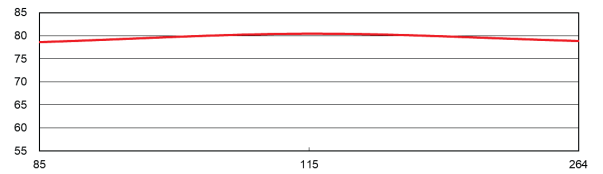


### TMPS 03-112

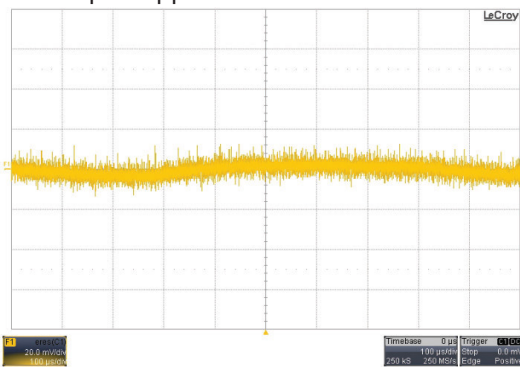
Efficiency vs Output Load



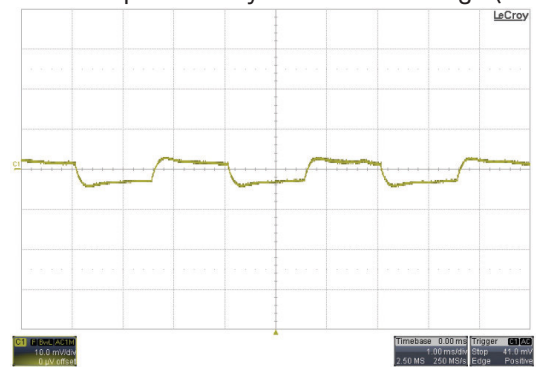
Efficiency vs Input Voltage



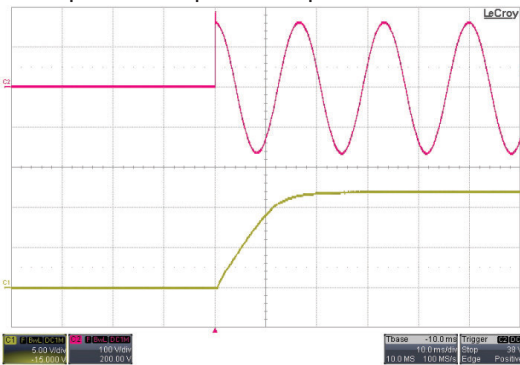
Typical Output Ripple and Noise



Transient Response to Dynamic Load Change (25%)

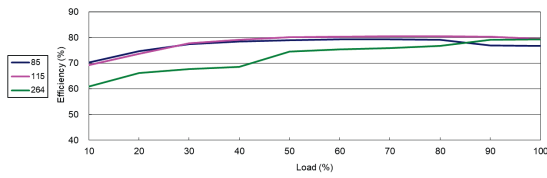


Typical Input Start-Up and Output Rise Characteristic

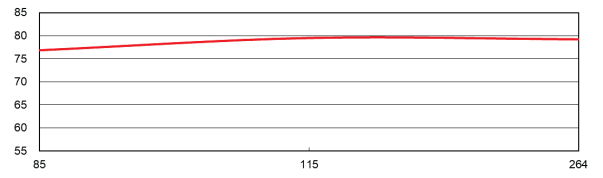


**TMPS 03-115**

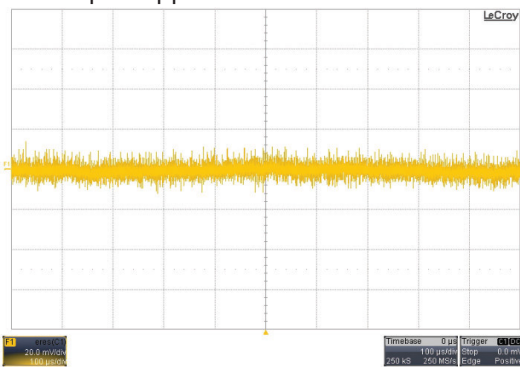
Efficiency vs Output Load



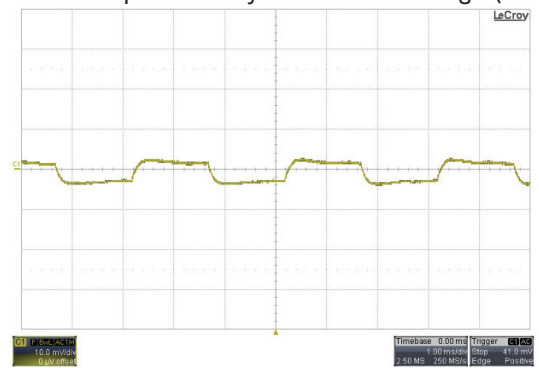
Efficiency vs Input Voltage



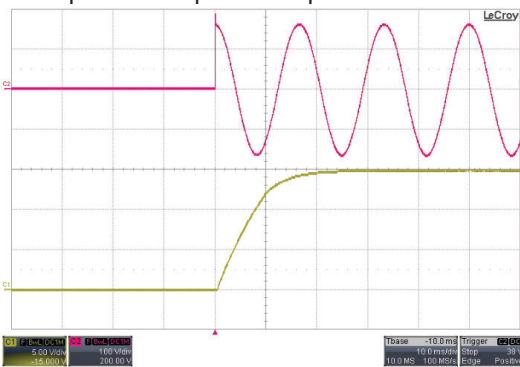
Typical Output Ripple and Noise



Transient Response to Dynamic Load Change (25%)

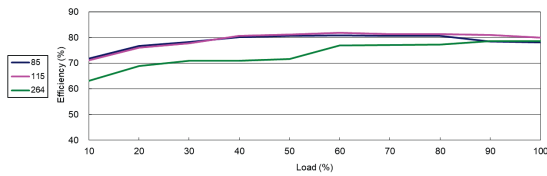


Typical Input Start-Up and Output Rise Characteristic

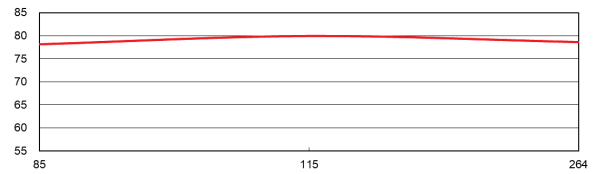


**TMPS 03-124**

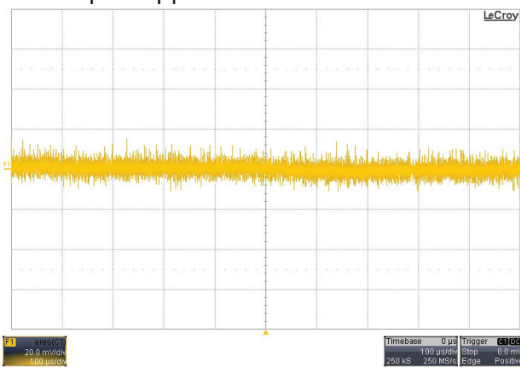
Efficiency vs Output Load



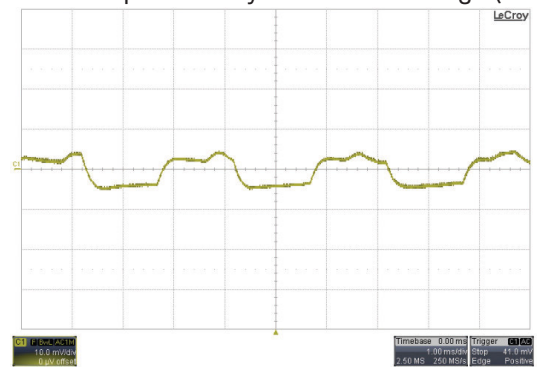
Efficiency vs Input Voltage



Typical Output Ripple and Noise



Transient Response to Dynamic Load Change (25%)



Typical Input Start-Up and Output Rise Characteristic

