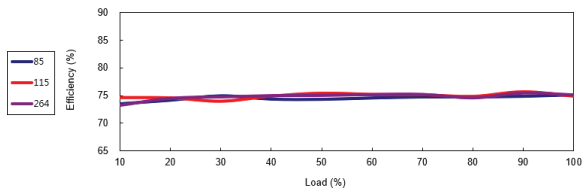


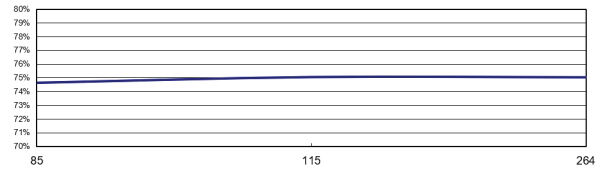
Characteristic Curves

TMP 15105 & TMP 15105C

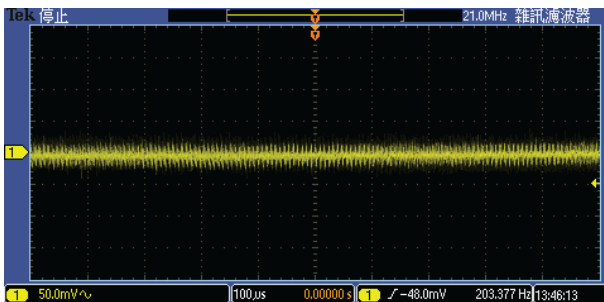
Efficiency vs Output Load



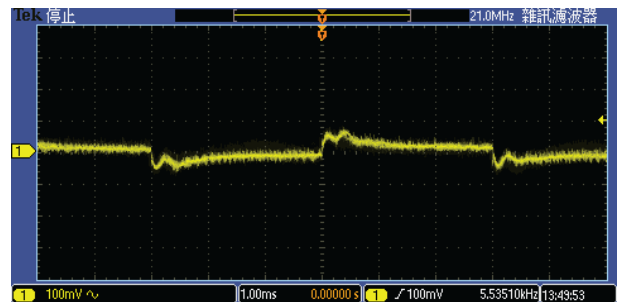
Efficiency vs Input Voltage



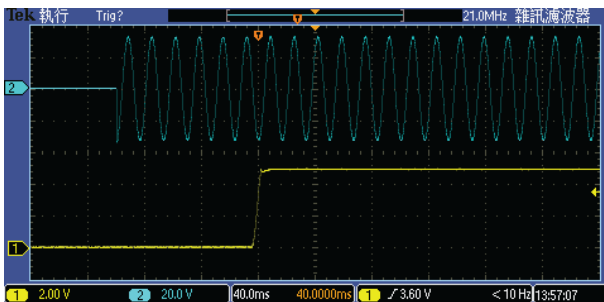
Typical Output Ripple and Noise



Transient Response to Dynamic Load Change (25%)

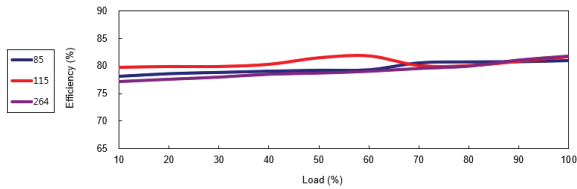


Typical Input Start-Up and Output Rise Characteristic

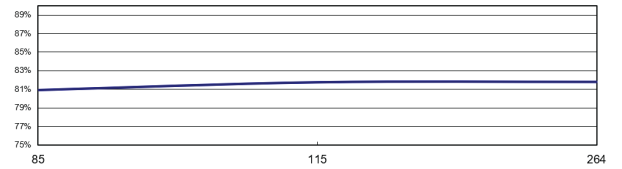


TMP 15112 & TMP 15112C

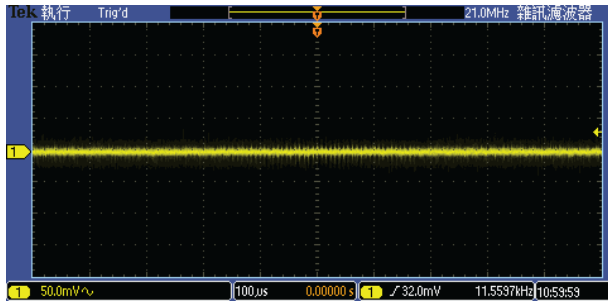
Efficiency vs Output Load



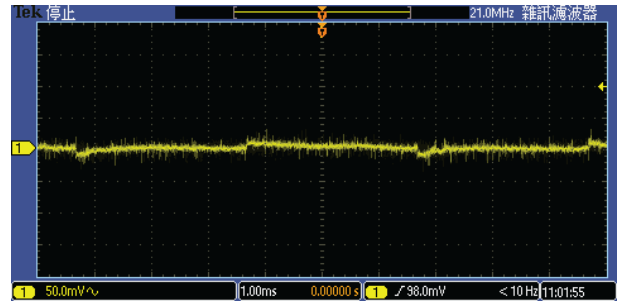
Efficiency vs Input Voltage



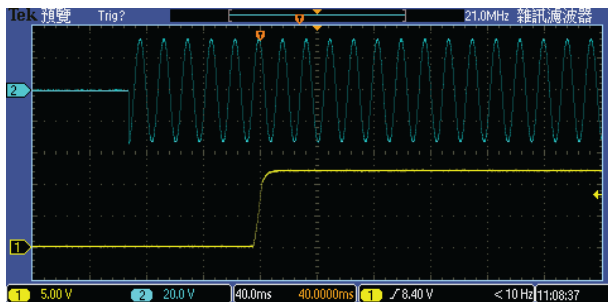
Typical Output Ripple and Noise



Transient Response to Dynamic Load Change (25%)

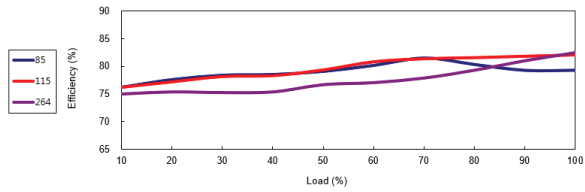


Typical Input Start-Up and Output Rise Characteristic

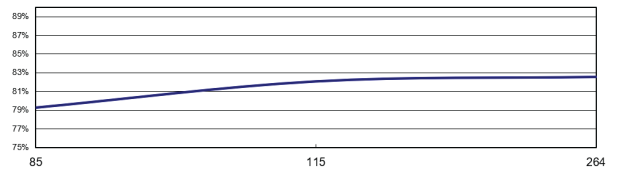


TMP 15115 & TMP 15115C

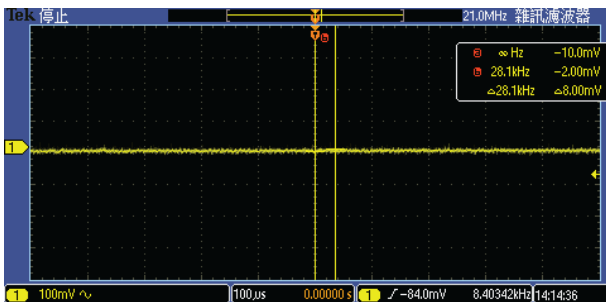
Efficiency vs Output Load



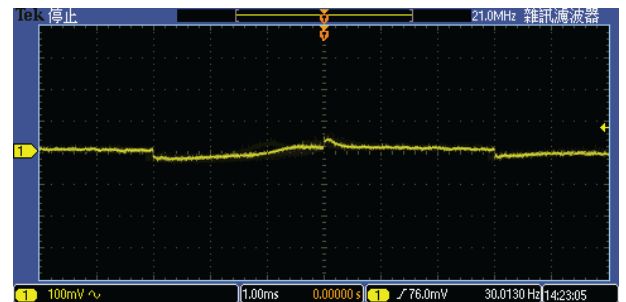
Efficiency vs Input Voltage



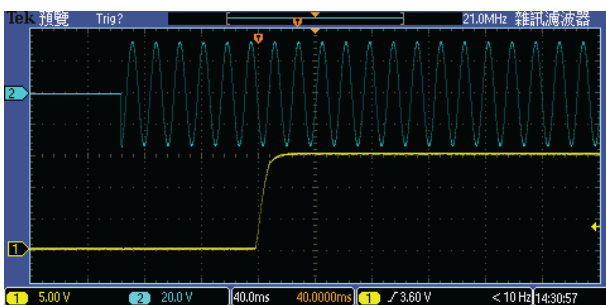
Typical Output Ripple and Noise



Transient Response to Dynamic Load Change (25%)

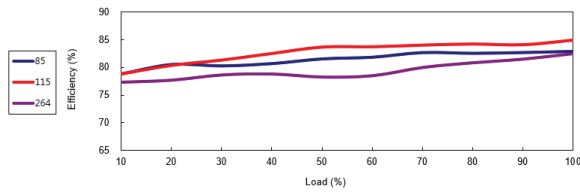


Typical Input Start-Up and Output Rise Characteristic

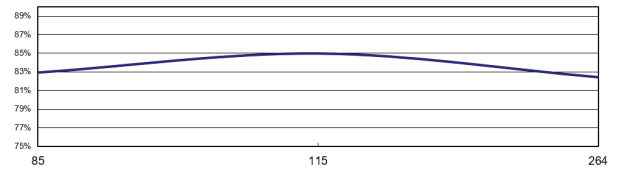


TMP 15124 & TMP 15124C

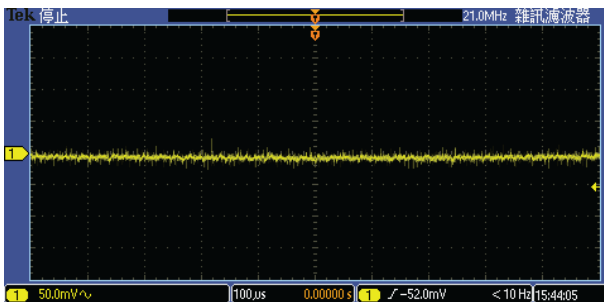
Efficiency vs Output Load



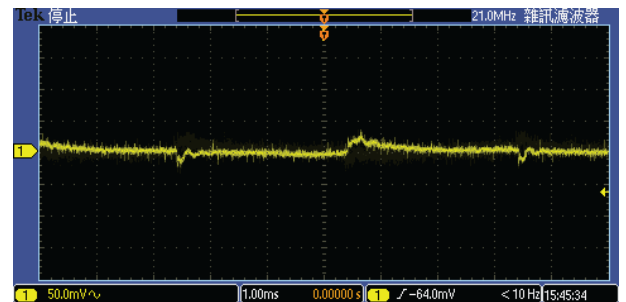
Efficiency vs Input Voltage



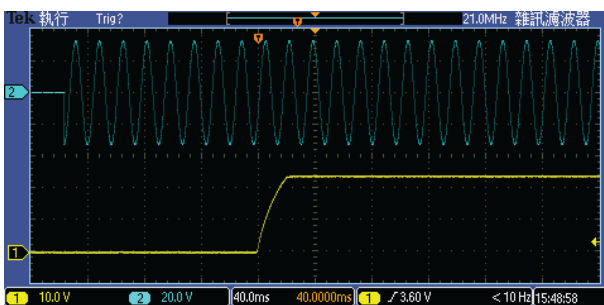
Typical Output Ripple and Noise



Transient Response to Dynamic Load Change (25%)

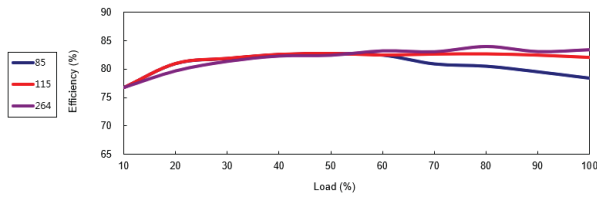


Typical Input Start-Up and Output Rise Characteristic

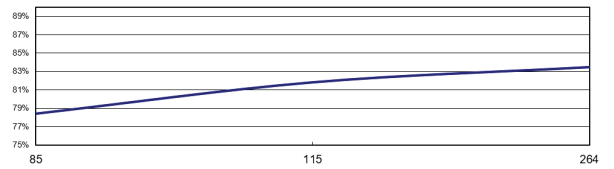


TMP 15148 & TMP 15148C

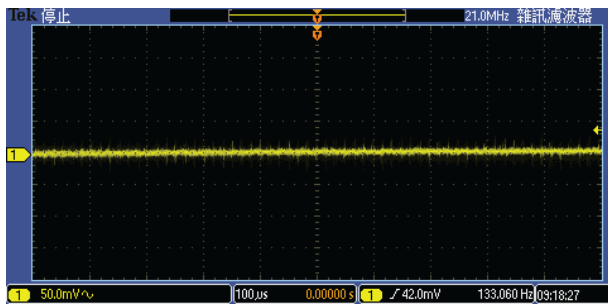
Efficiency vs Output Load



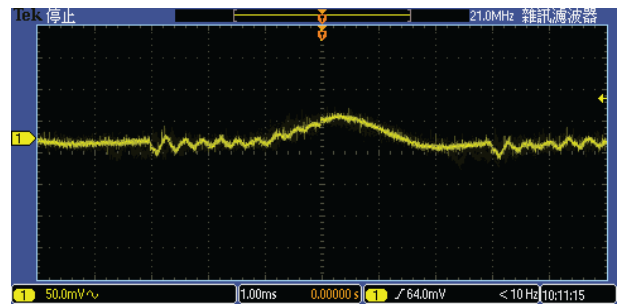
Efficiency vs Input Voltage



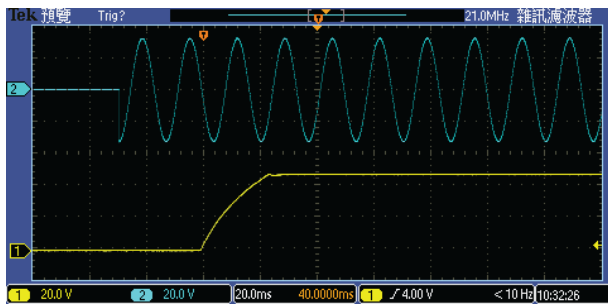
Typical Output Ripple and Noise



Transient Response to Dynamic Load Change (25%)

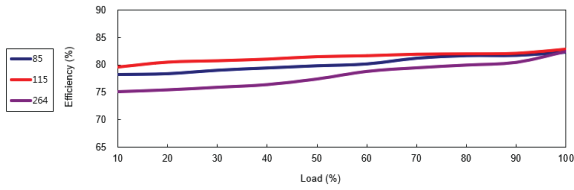


Typical Input Start-Up and Output Rise Characteristic

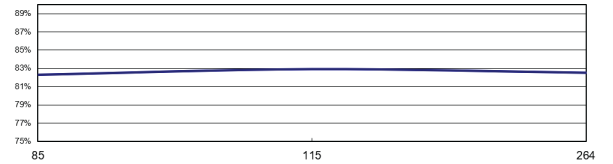


TMP 15212 & TMP 15212C

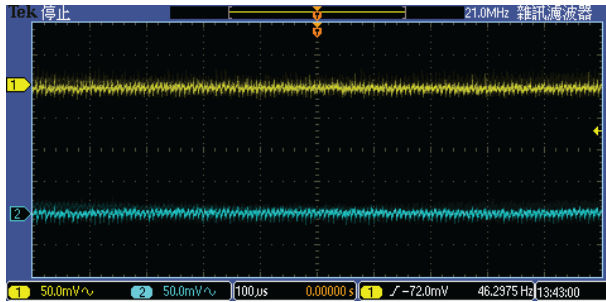
Efficiency vs Output Load



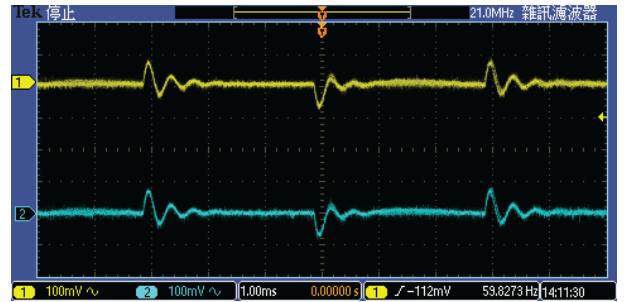
Efficiency vs Input Voltage



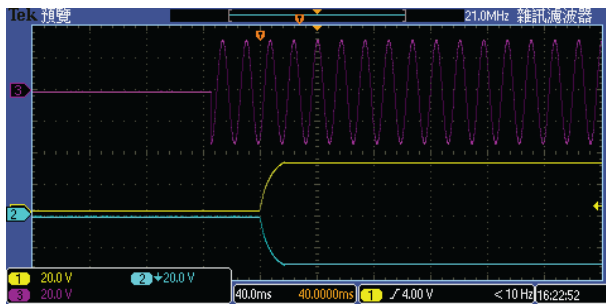
Typical Output Ripple and Noise



Transient Response to Dynamic Load Change (25%)

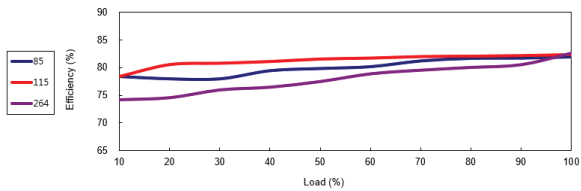


Typical Input Start-Up and Output Rise Characteristic

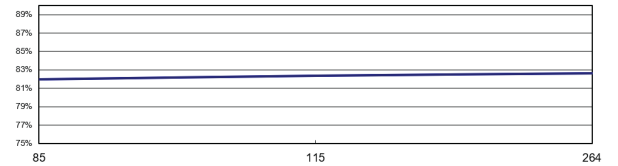


TMP 15215 & TMP 15215C

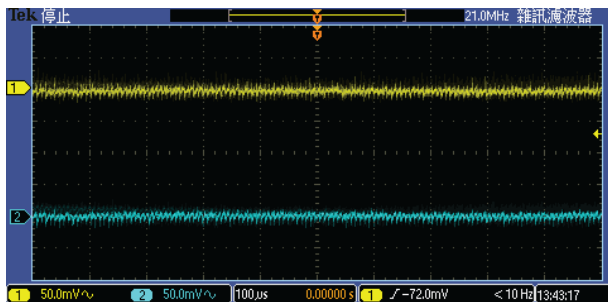
Efficiency vs Output Load



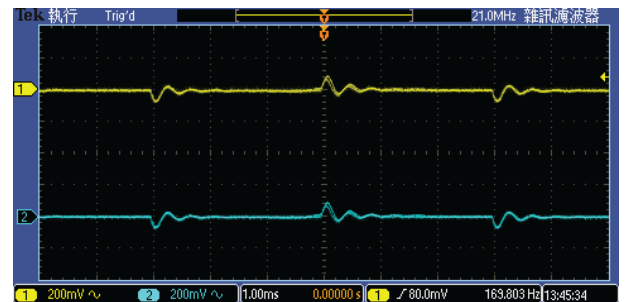
Efficiency vs Input Voltage



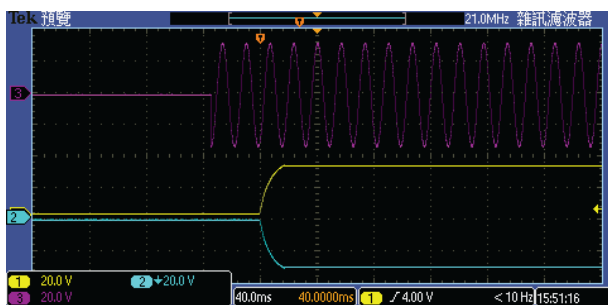
Typical Output Ripple and Noise



Transient Response to Dynamic Load Change (25%)

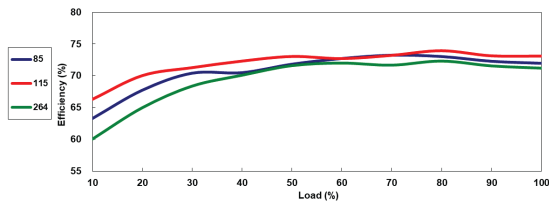


Typical Input Start-Up and Output Rise Characteristic

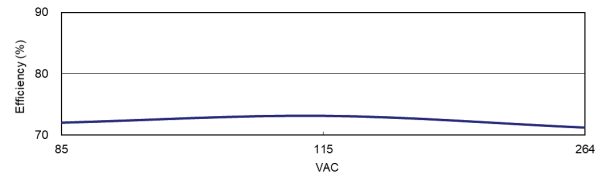


TMP 15252 & TMP 15252C

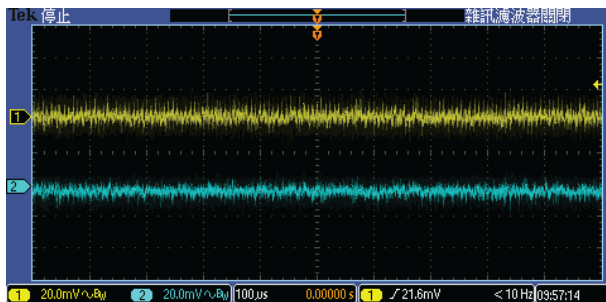
Efficiency vs Output Load



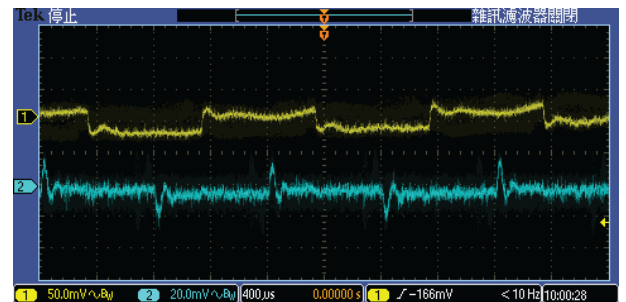
Efficiency vs Input Voltage



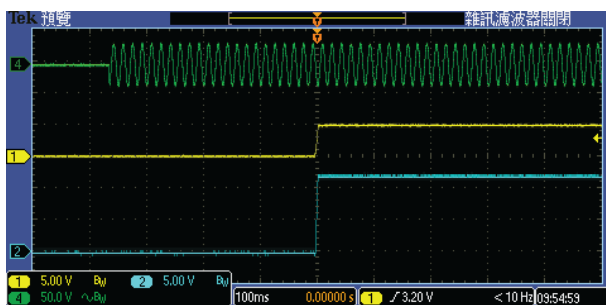
Typical Output Ripple and Noise



Transient Response to Dynamic Load Change (25%)

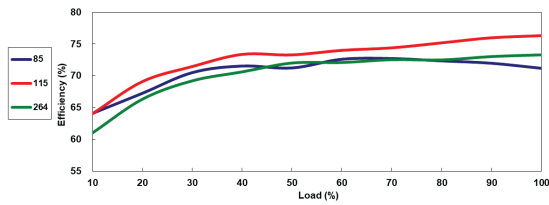


Typical Input Start-Up and Output Rise Characteristic

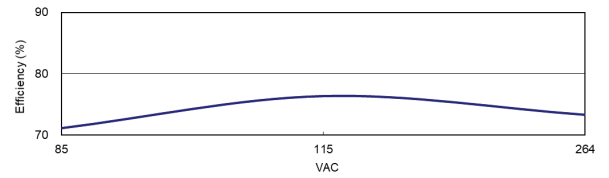


TMP 15512 & TMP 15512C

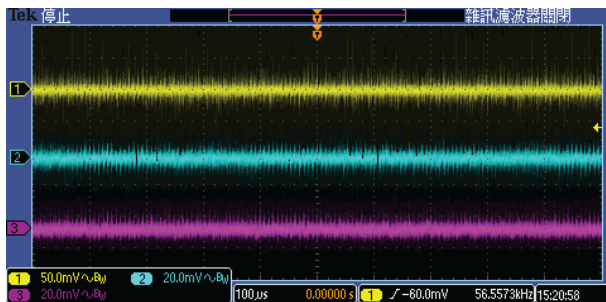
Efficiency vs Output Load



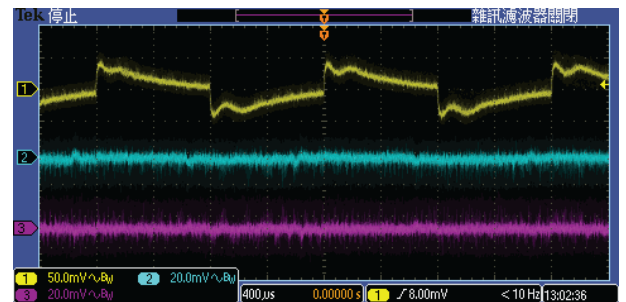
Efficiency vs Input Voltage



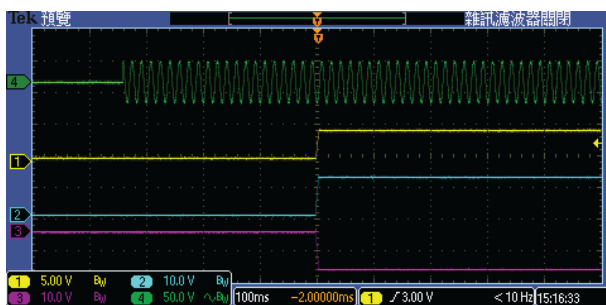
Typical Output Ripple and Noise



Transient Response to Dynamic Load Change (25%)

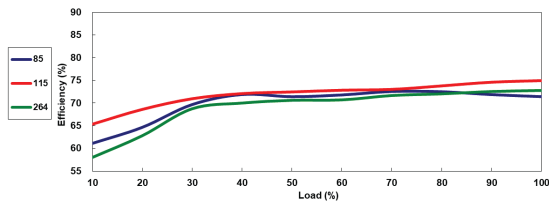


Typical Input Start-Up and Output Rise Characteristic

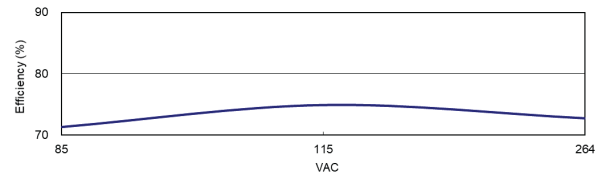


TMP 15515 & TMP 15515C

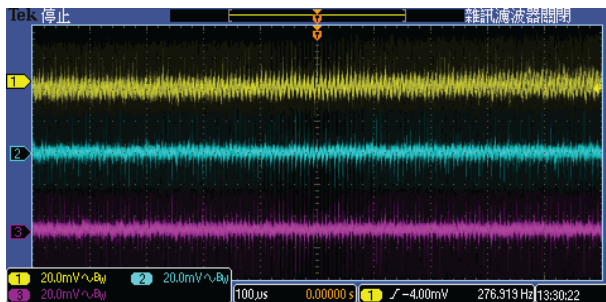
Efficiency vs Output Load



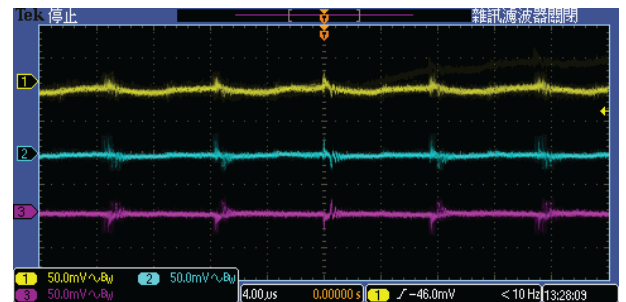
Efficiency vs Input Voltage



Typical Output Ripple and Noise



Transient Response to Dynamic Load Change (25%)



Typical Input Start-Up and Output Rise Characteristic

