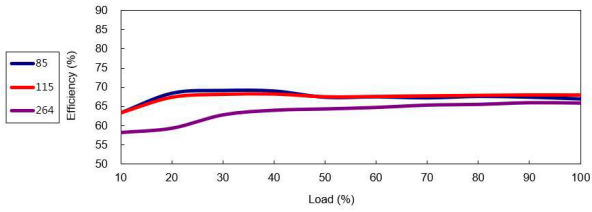


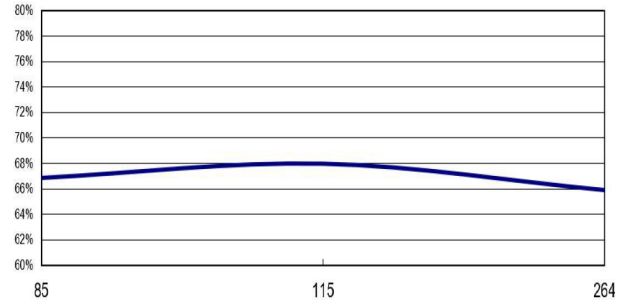
Characteristic Curves

TMP 07103

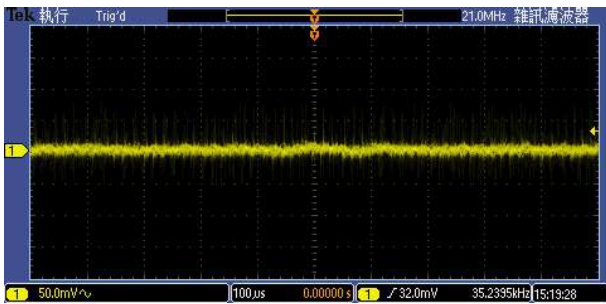
Efficiency vs Output Load



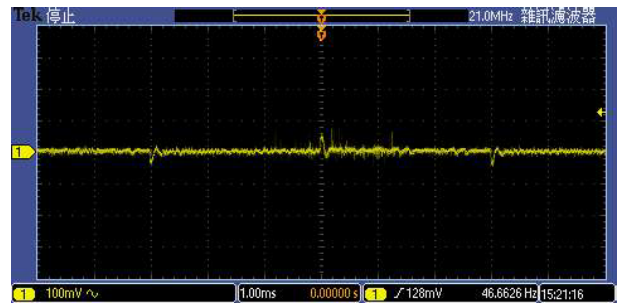
Efficiency vs Input Voltage



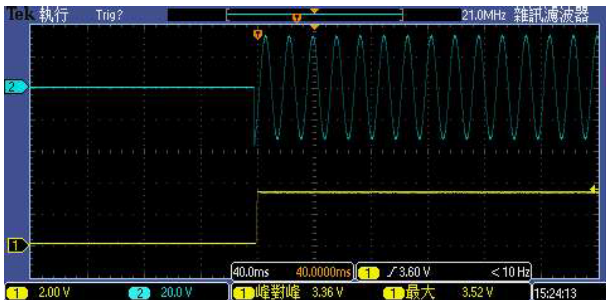
Typical Output Ripple and Noise



Transient Response to Dynamic Load Change (25%)

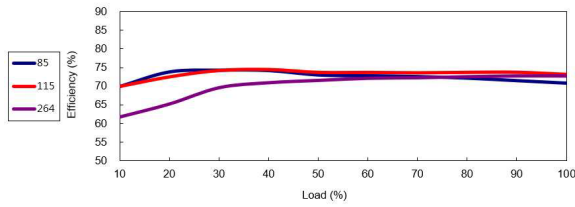


Typical Input Start-Up and Output Rise Characteristic

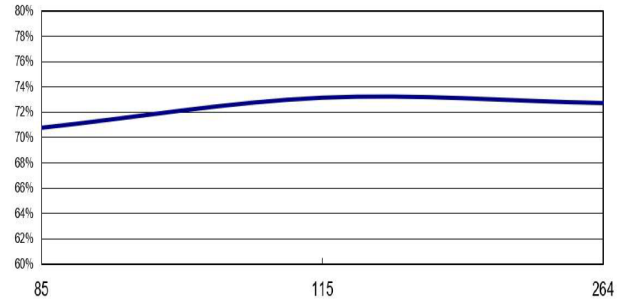


TMP 07105

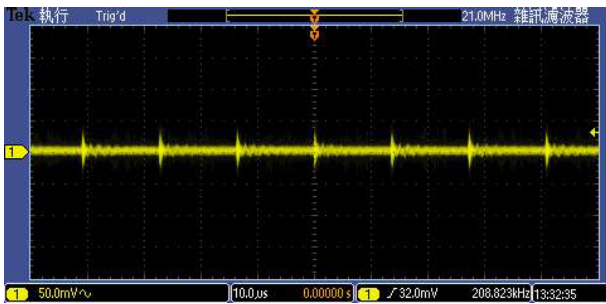
Efficiency vs Output Load



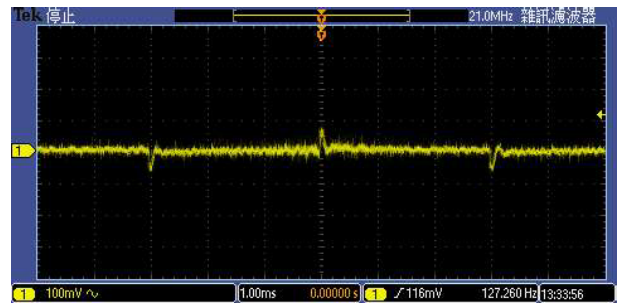
Efficiency vs Input Voltage



Typical Output Ripple and Noise



Transient Response to Dynamic Load Change (25%)

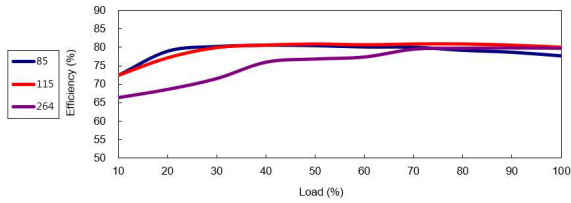


Typical Input Start-Up and Output Rise Characteristic

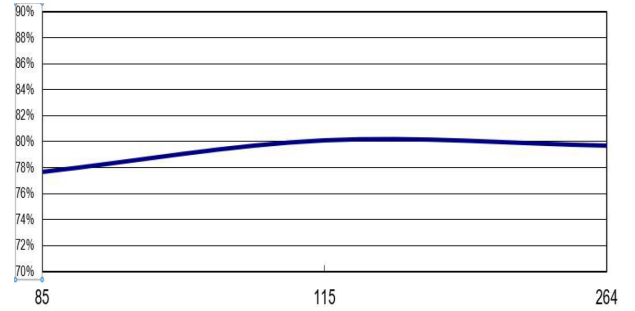


TMP 07112

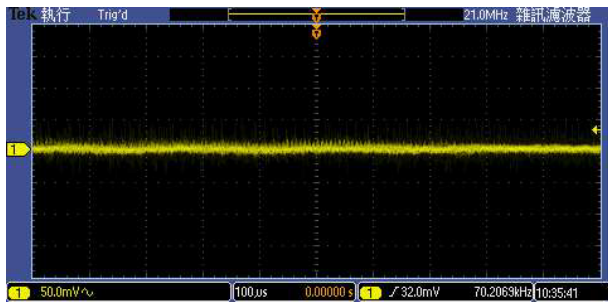
Efficiency vs Output Load



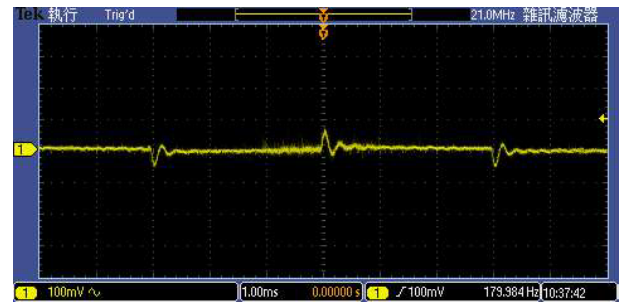
Efficiency vs Input Voltage



Typical Output Ripple and Noise



Transient Response to Dynamic Load Change (25%)

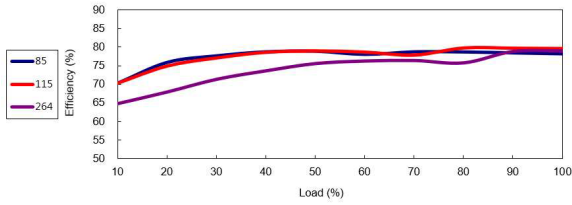


Typical Input Start-Up and Output Rise Characteristic

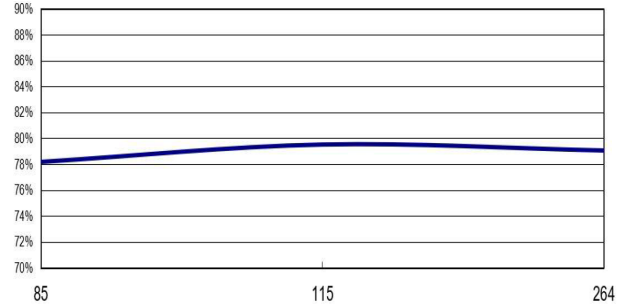


TMP 07115

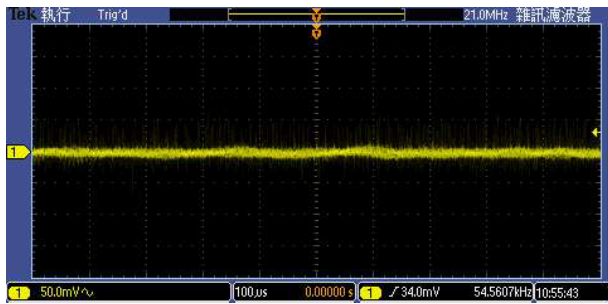
Efficiency vs Output Load



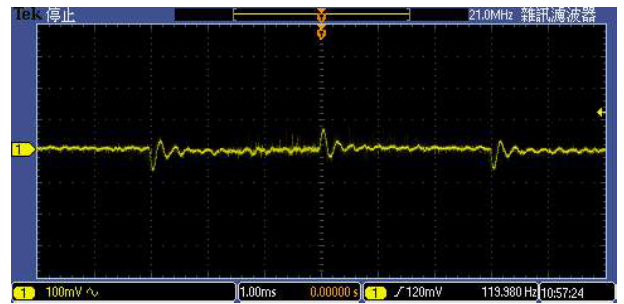
Efficiency vs Input Voltage



Typical Output Ripple and Noise



Transient Response to Dynamic Load Change (25%)

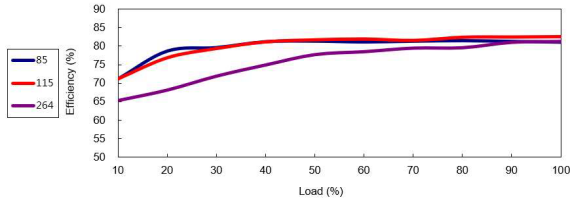


Typical Input Start-Up and Output Rise Characteristic

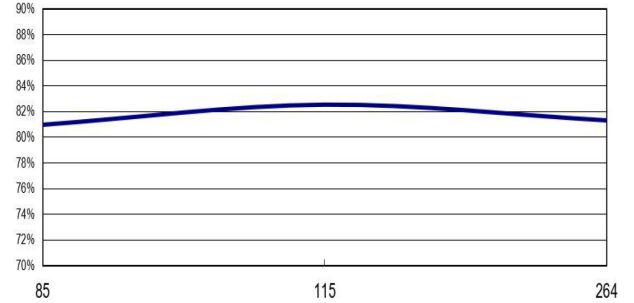


TMP 07124

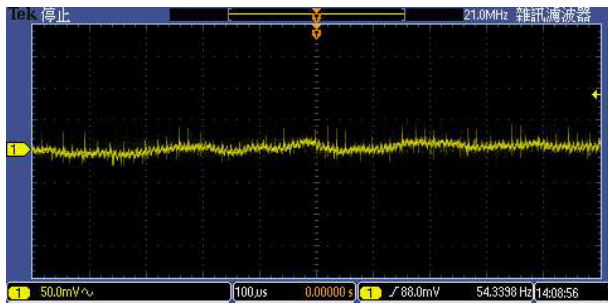
Efficiency vs Output Load



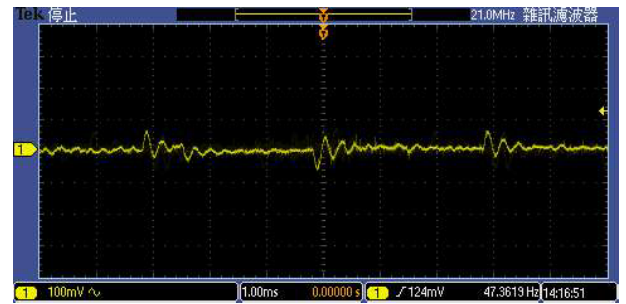
Efficiency vs Input Voltage



Typical Output Ripple and Noise



Transient Response to Dynamic Load Change (25%)



Typical Input Start-Up and Output Rise Characteristic

