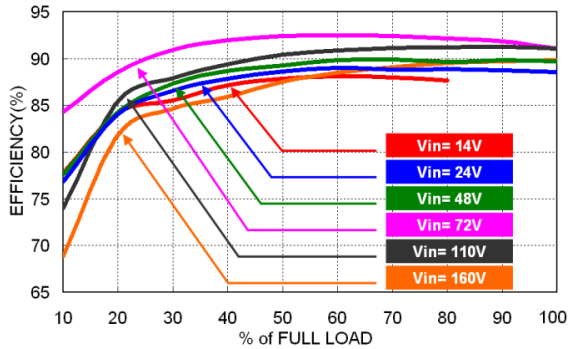


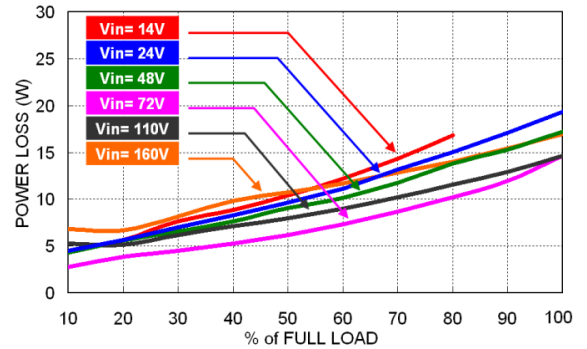
Characteristic Curves

TEP 150-7211UIR

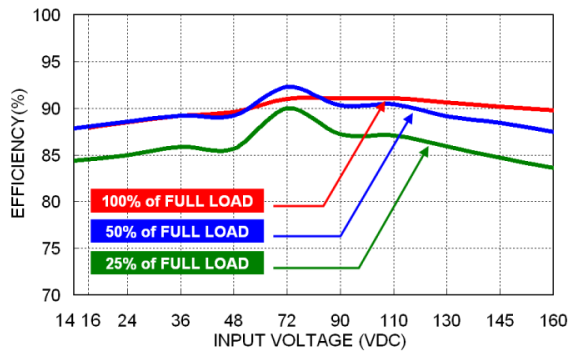
Efficiency versus Output Load



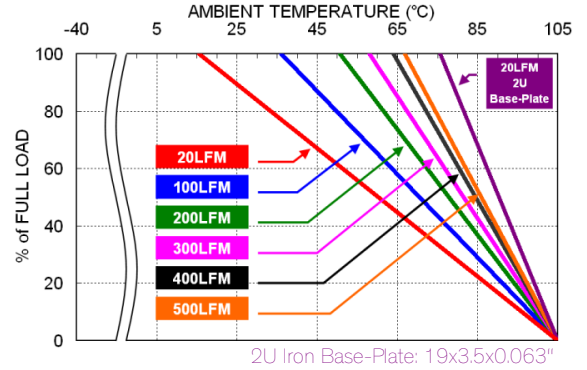
Power Dissipation versus Output Load



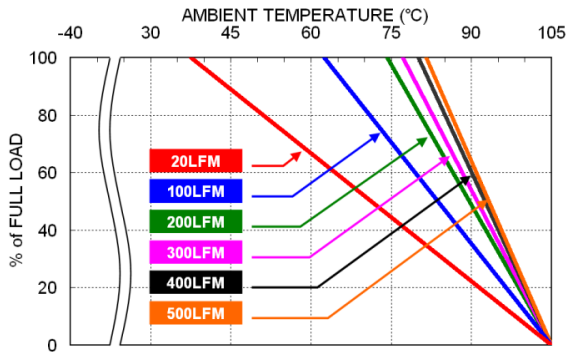
Efficiency versus Input Voltage



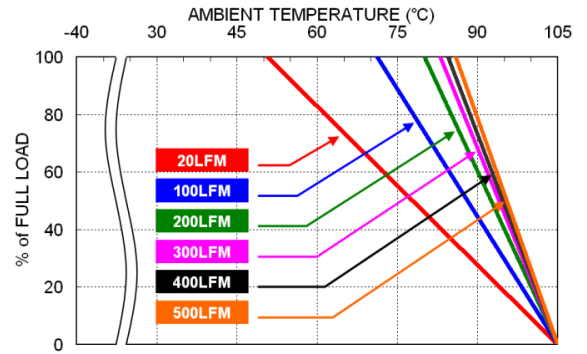
Derating Output Load versus Ambient Temperature without Heatsink



Derating Output Load versus Ambient Temperature with Heatsink TEP-HS6

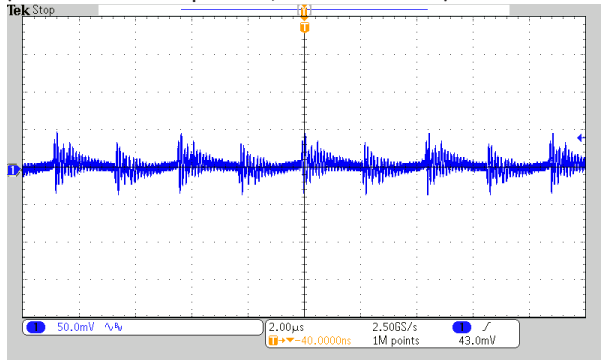


Derating Output Load versus Ambient Temperature with Heatsink TEP-HS7

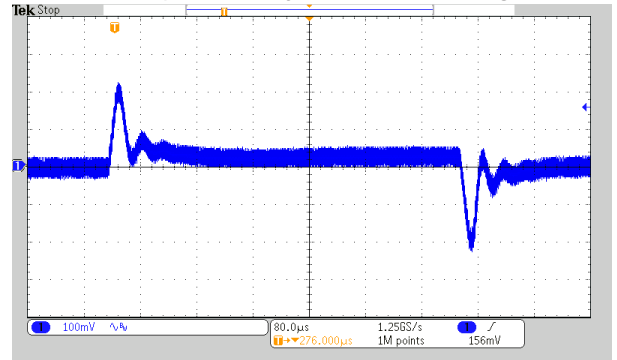


TEP 150-7211UIR

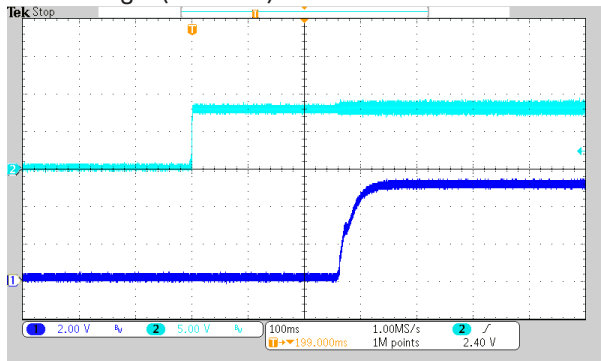
Typical Output Ripple and Noise at 115 Vin
(with external capacitor; see datasheet)



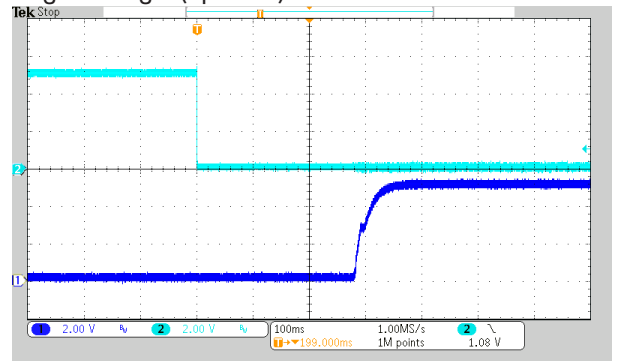
Transient Response to Dynamic Load Change (25%)



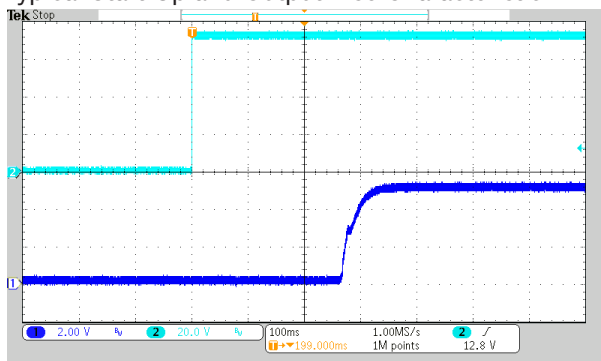
Remote on/off Voltage Start-Up Characteristic
Positive logic (standard)



Remote on/off Voltage Start-Up Characteristic
Negative logic (optional)

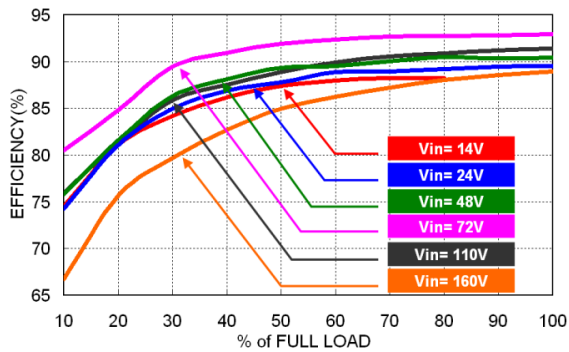


Typical Start-Up and Output Rise Characteristic

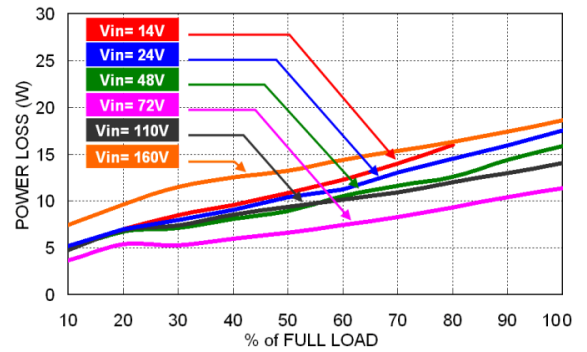


TEP 150-7212UIR

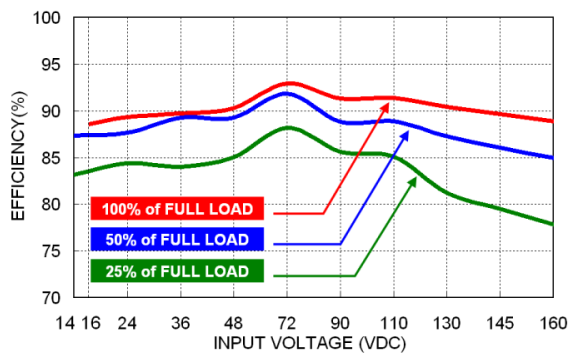
Efficiency versus Output Load



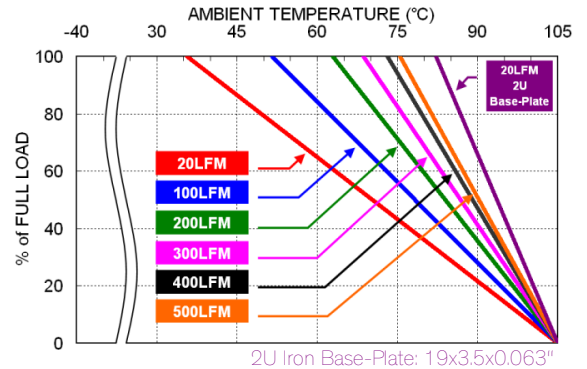
Power Dissipation versus Output Load



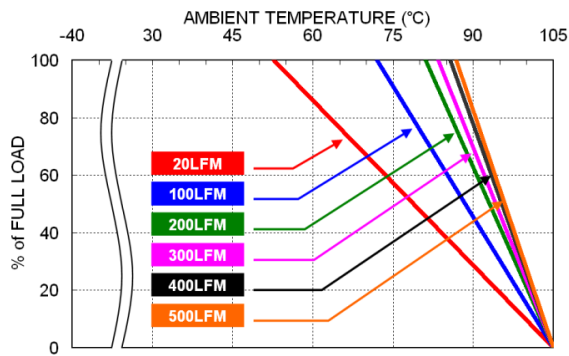
Efficiency versus Input Voltage



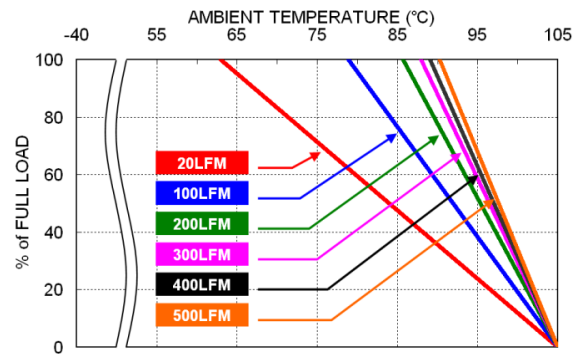
Derating Output Load versus Ambient Temperature without Heatsink



Derating Output Load versus Ambient Temperature with Heatsink TEP-HS6

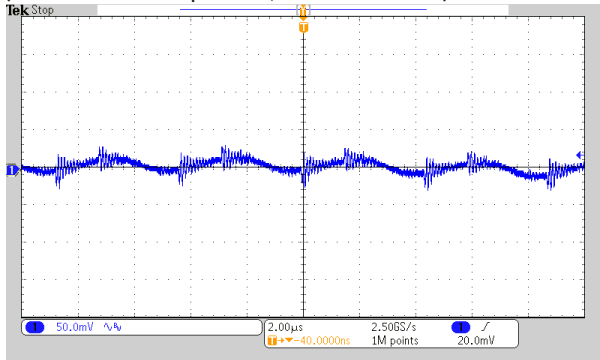


Derating Output Load versus Ambient Temperature with Heatsink TEP-HS7

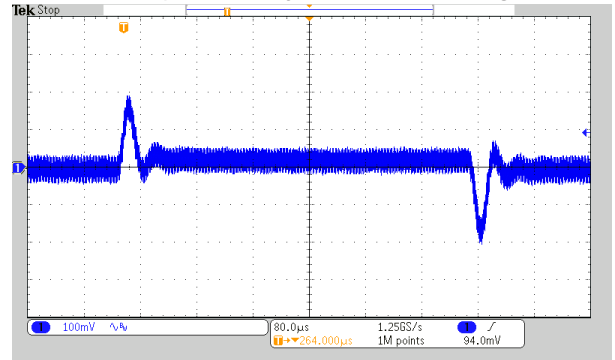


TEP 150-7212UIR

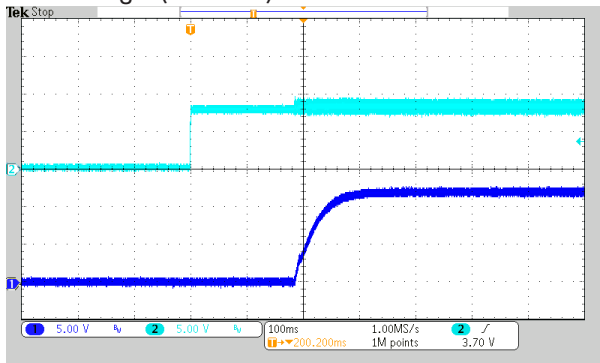
Typical Output Ripple and Noise at 115 Vin
(with external capacitor; see datasheet)



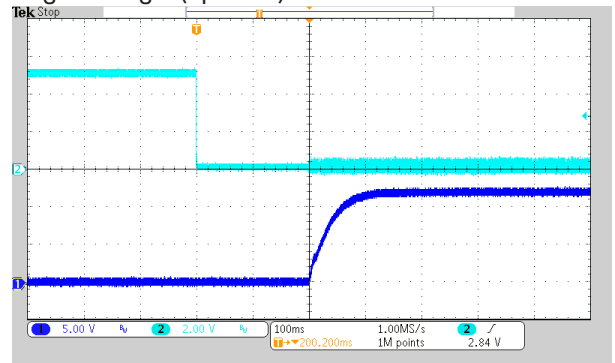
Transient Response to Dynamic Load Change (25%)



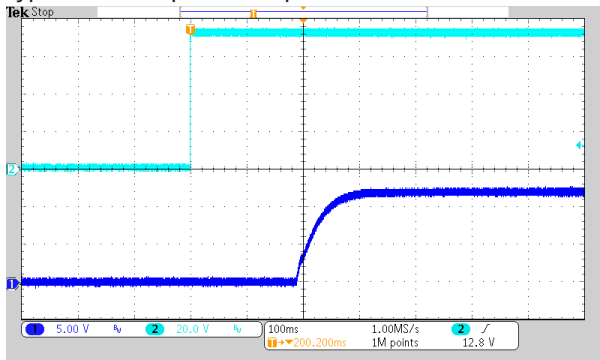
Remote on/off Voltage Start-Up Characteristic
Positive logic (standard)



Remote on/off Voltage Start-Up Characteristic
Negative logic (optional)

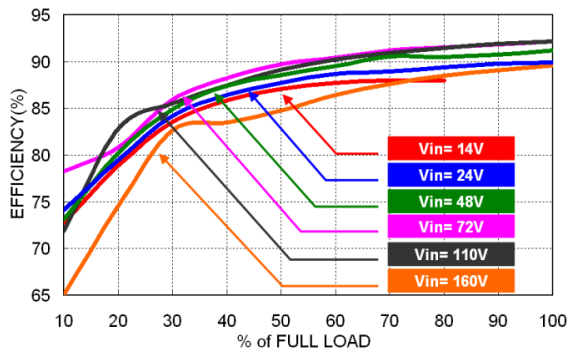


Typical Start-Up and Output Rise Characteristic

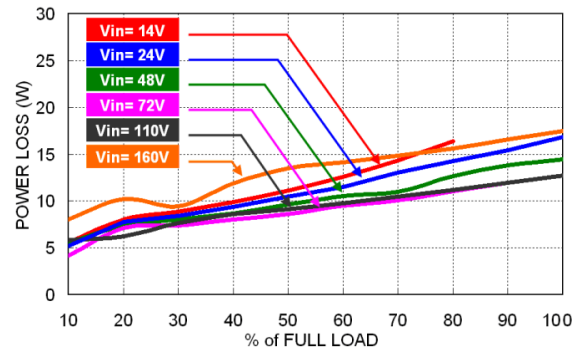


TEP 150-7213UIR

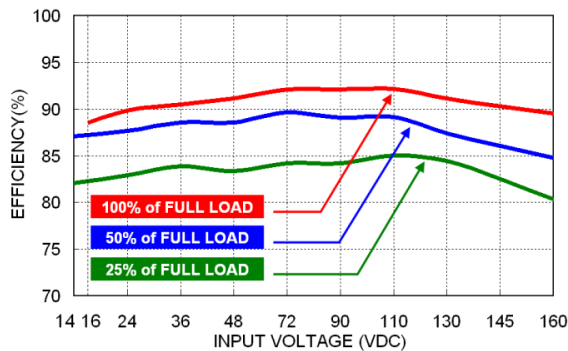
Efficiency versus Output Load



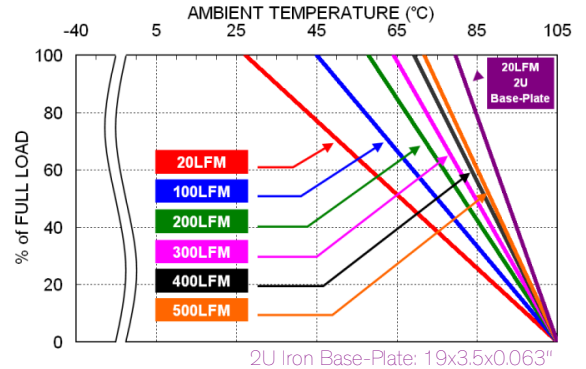
Power Dissipation versus Output Load



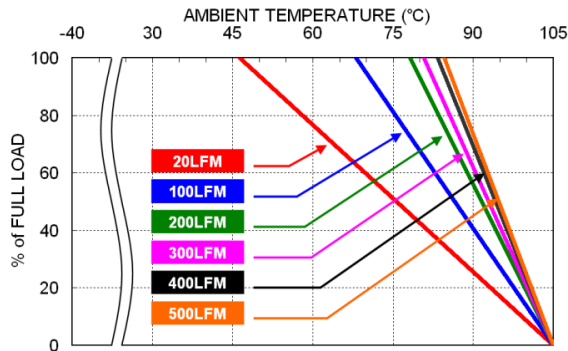
Efficiency versus Input Voltage



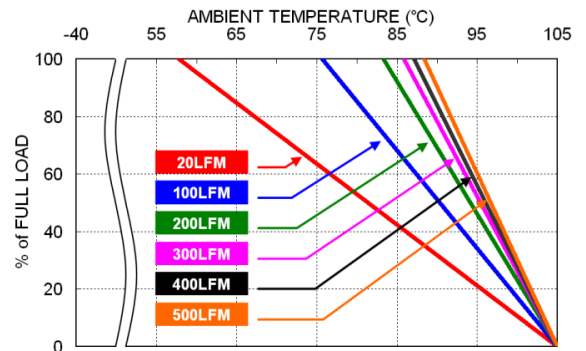
Derating Output Load versus Ambient Temperature without Heatsink



Derating Output Load versus Ambient Temperature with Heatsink TEP-HS6

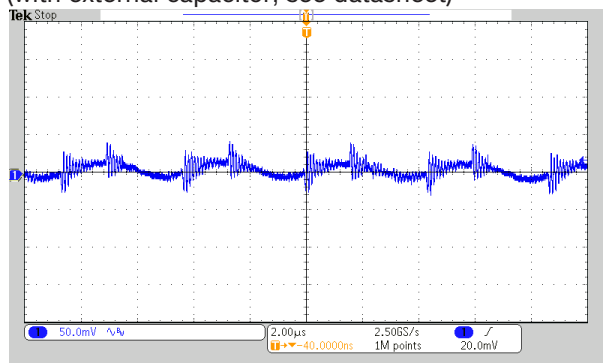


Derating Output Load versus Ambient Temperature with Heatsink TEP-HS7

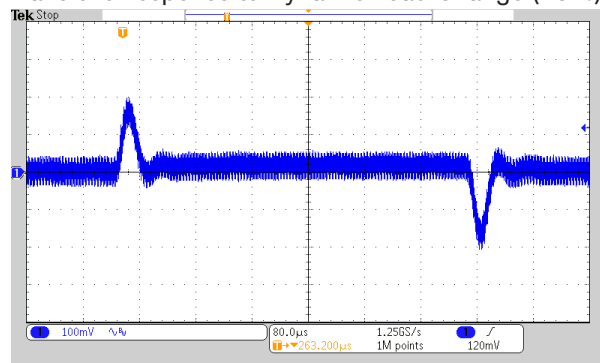


TEP 150-7213UIR

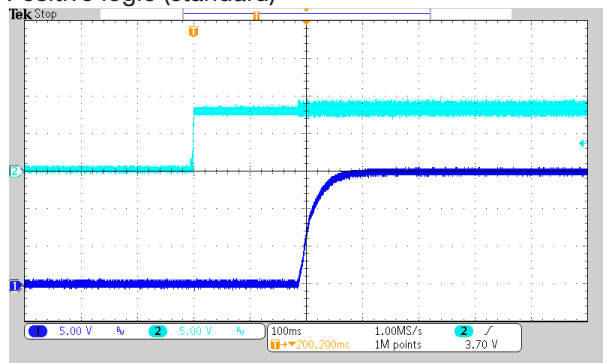
Typical Output Ripple and Noise at 115 Vin
(with external capacitor; see datasheet)



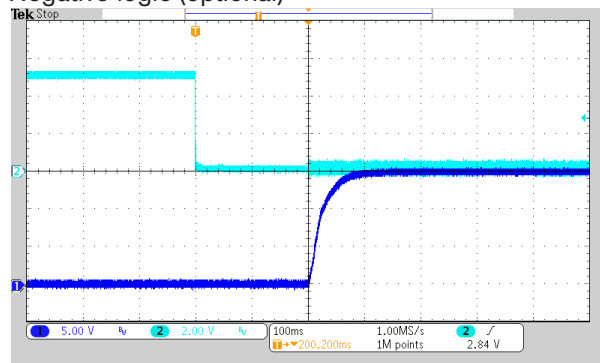
Transient Response to Dynamic Load Change (25%)



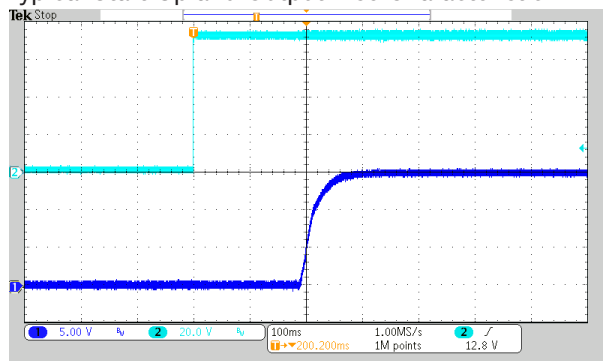
Remote on/off Voltage Start-Up Characteristic
Positive logic (standard)



Remote on/off Voltage Start-Up Characteristic
Negative logic (optional)

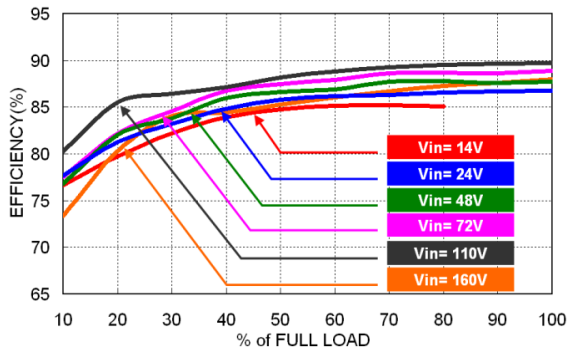


Typical Start-Up and Output Rise Characteristic

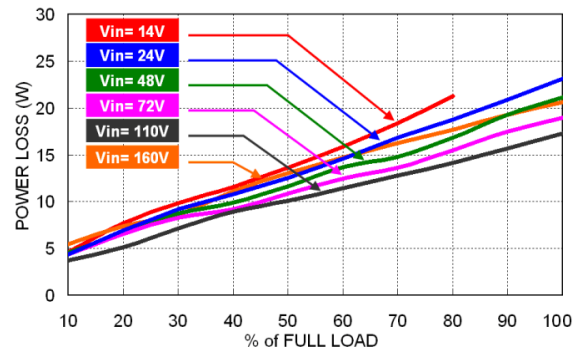


TEP 150-7215UIR

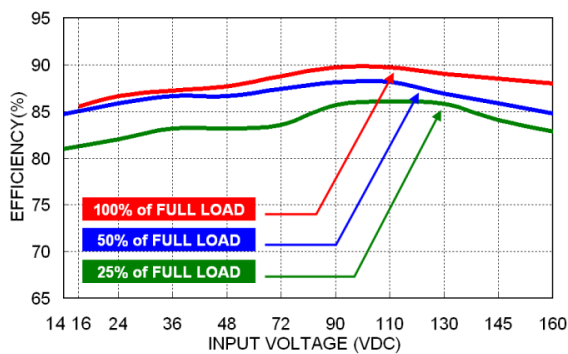
Efficiency versus Output Load



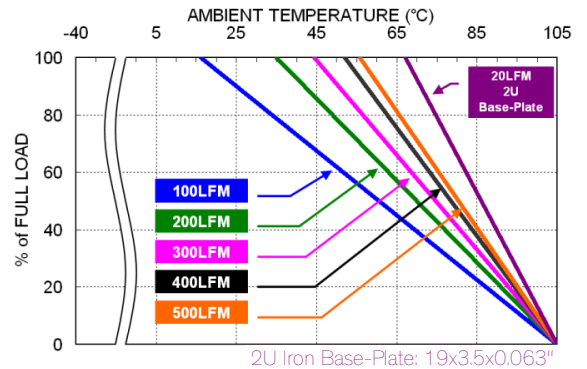
Power Dissipation versus Output Load



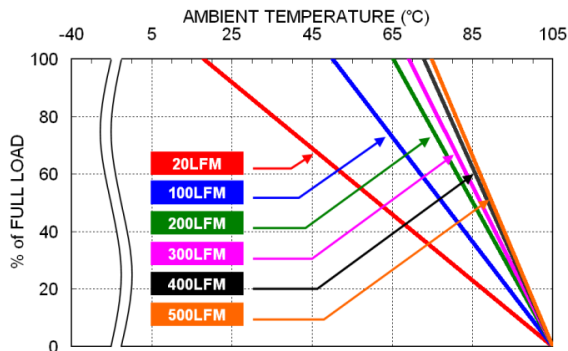
Efficiency versus Input Voltage



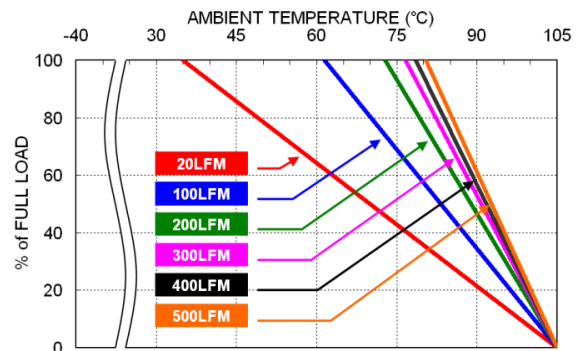
Derating Output Load versus Ambient Temperature without Heatsink



Derating Output Load versus Ambient Temperature with Heatsink TEP-HS6

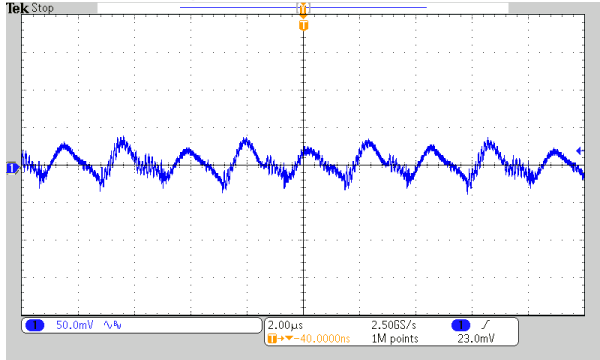


Derating Output Load versus Ambient Temperature with Heatsink TEP-HS7

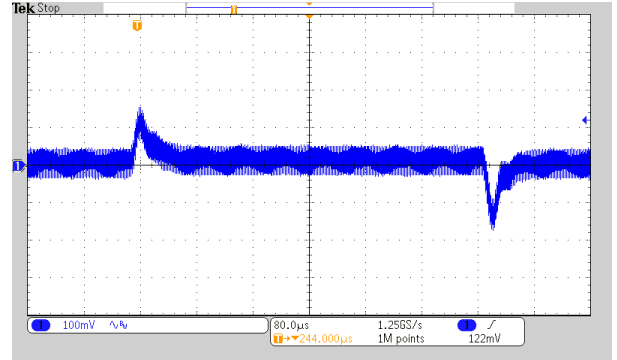


TEP 150-7215UIR

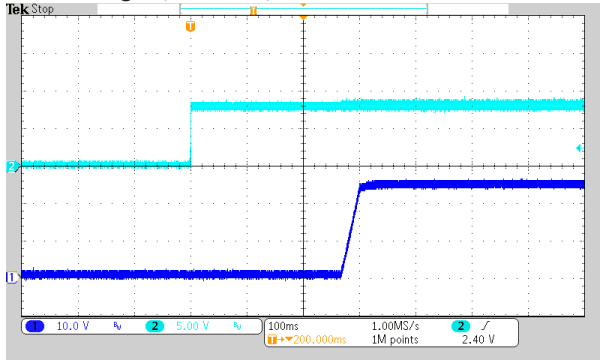
Typical Output Ripple and Noise at 115 Vin
(with external capacitor; see datasheet)



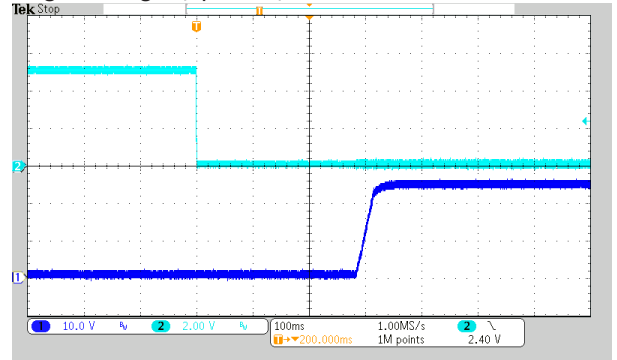
Transient Response to Dynamic Load Change (25%)



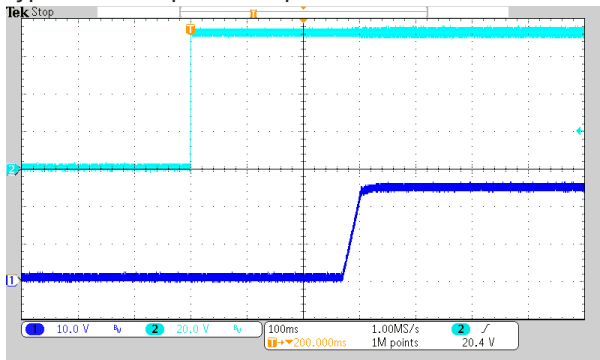
Remote on/off Voltage Start-Up Characteristic
Positive logic (standard)



Remote on/off Voltage Start-Up Characteristic
Negative logic (optional)

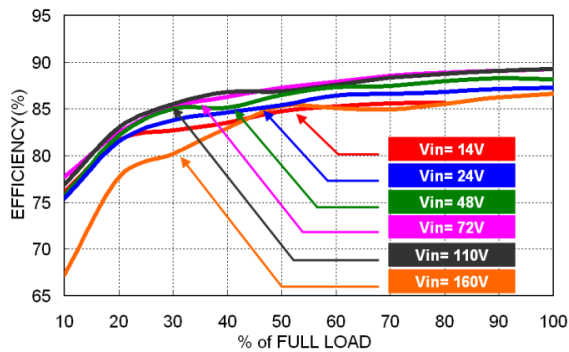


Typical Start-Up and Output Rise Characteristic

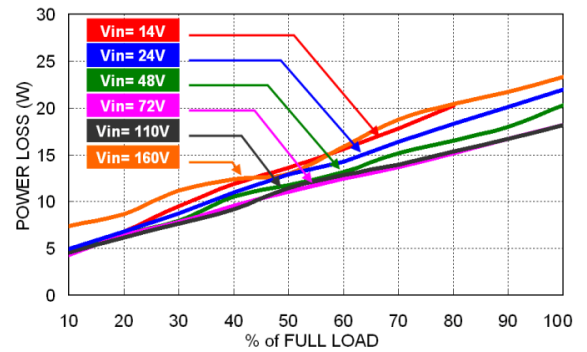


optional model with 28 Vout

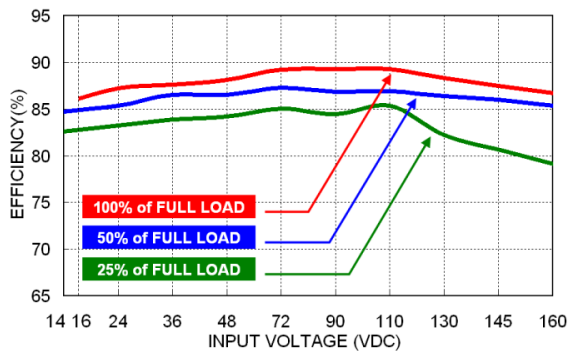
Efficiency versus Output Load



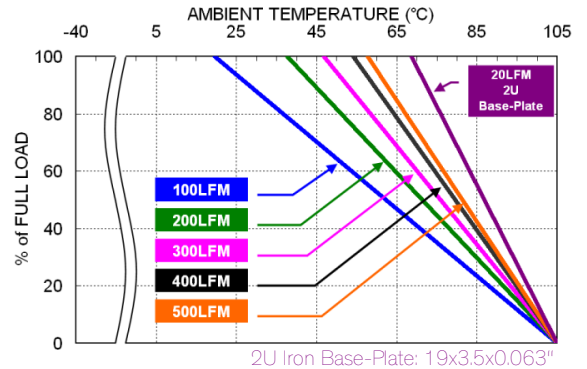
Power Dissipation versus Output Load



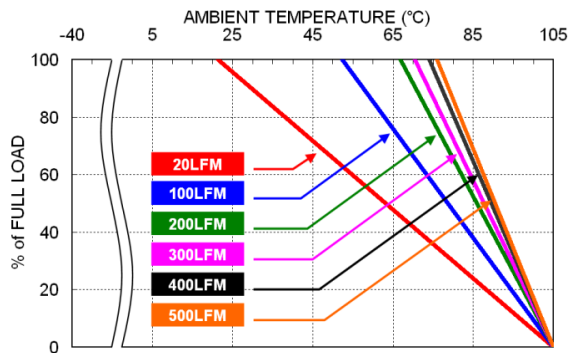
Efficiency versus Input Voltage



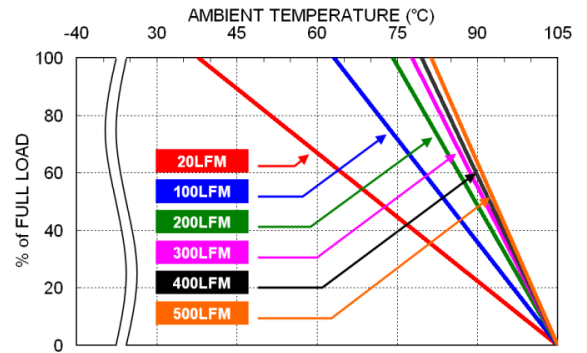
Derating Output Load versus Ambient Temperature without Heatsink



Derating Output Load versus Ambient Temperature with Heatsink TEP-HS6

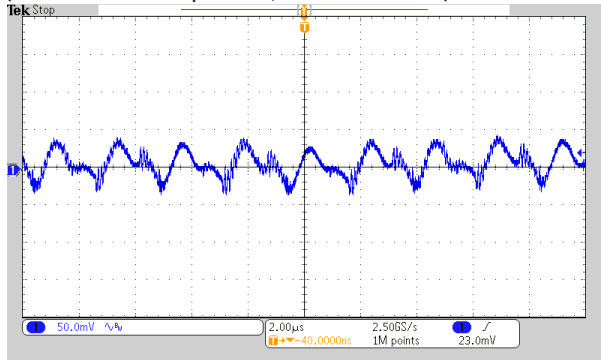


Derating Output Load versus Ambient Temperature with Heatsink TEP-HS7

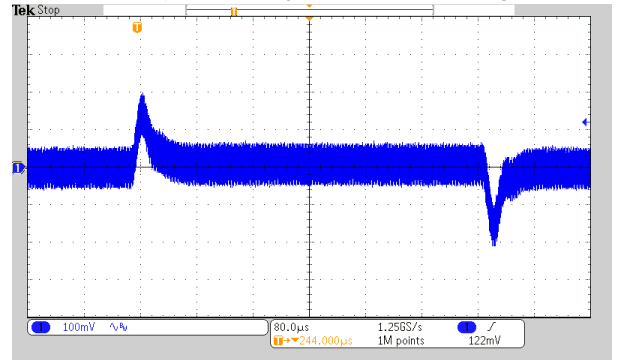


optional model with 28 Vout

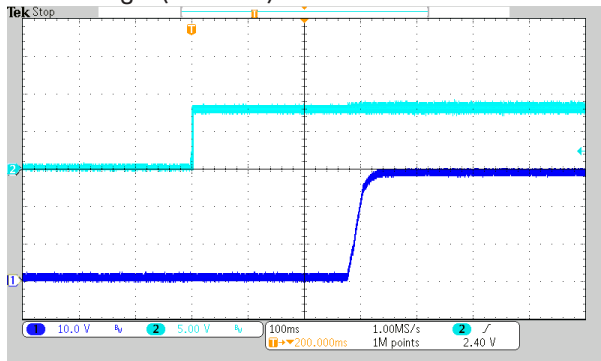
Typical Output Ripple and Noise at 115 Vin
(with external capacitor; see datasheet)



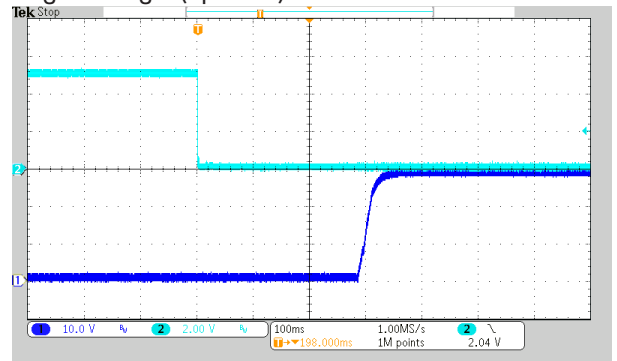
Transient Response to Dynamic Load Change (25%)



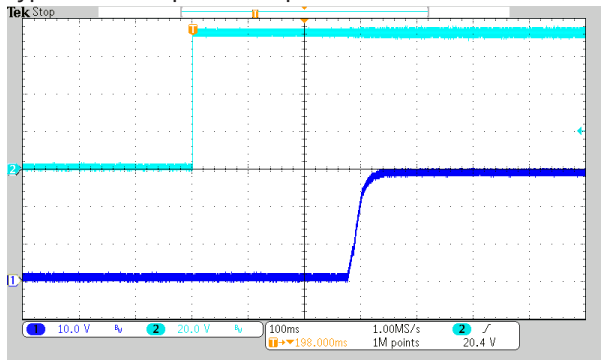
Remote on/off Voltage Start-Up Characteristic
Positive logic (standard)



Remote on/off Voltage Start-Up Characteristic
Negative logic (optional)

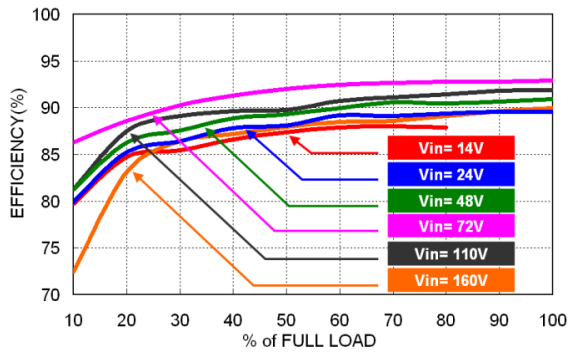


Typical Start-Up and Output Rise Characteristic

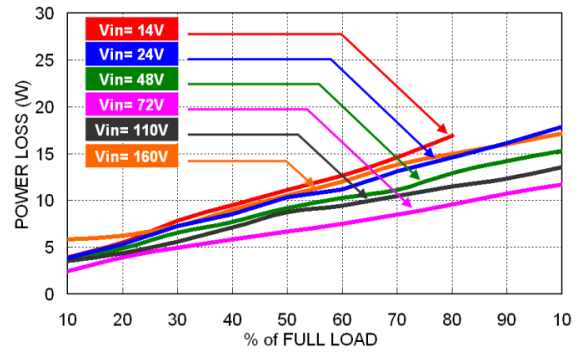


TEP 150-7218UIR

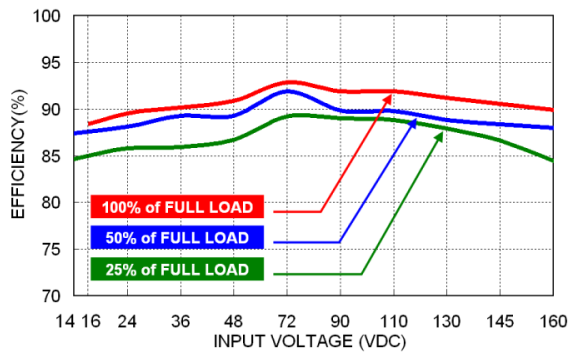
Efficiency versus Output Load



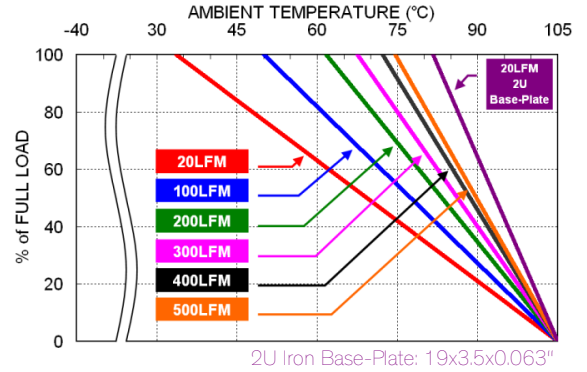
Power Dissipation versus Output Load



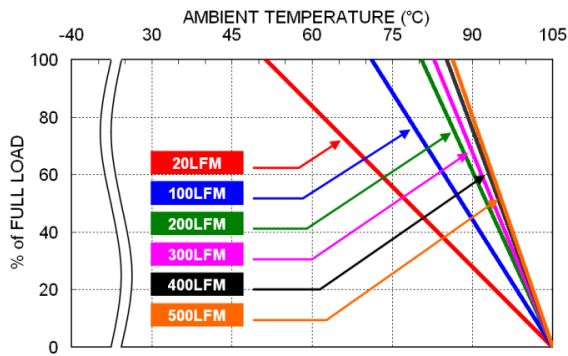
Efficiency versus Input Voltage



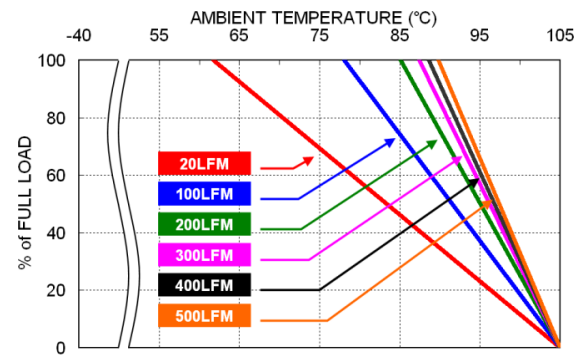
Derating Output Load versus Ambient Temperature without Heatsink



Derating Output Load versus Ambient Temperature with Heatsink TEP-HS6

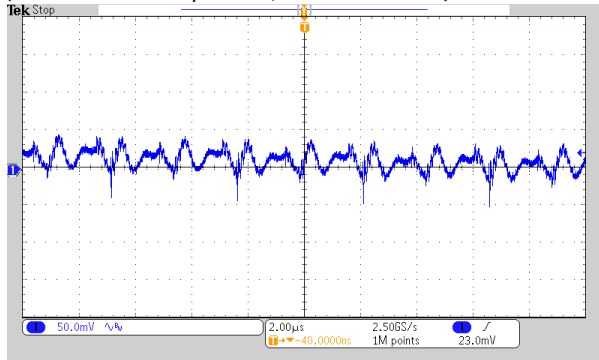


Derating Output Load versus Ambient Temperature with Heatsink TEP-HS7

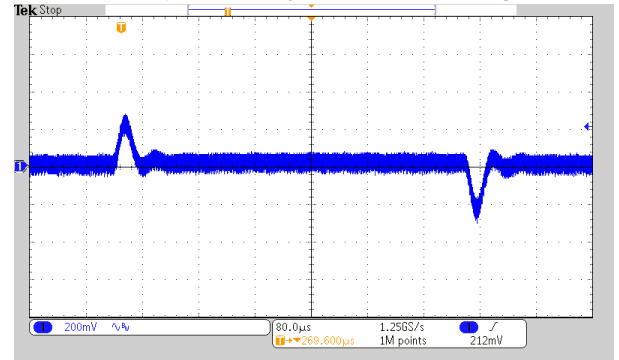


TEP 150-7218UIR

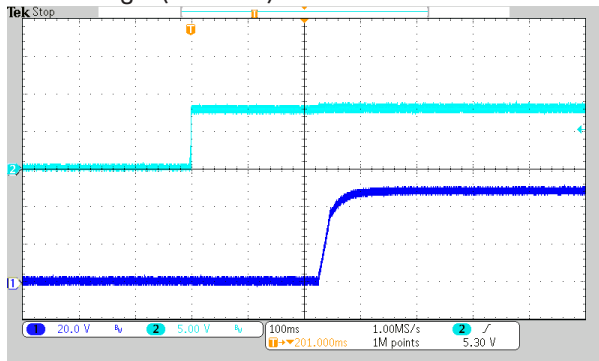
Typical Output Ripple and Noise at 115 Vin
(with external capacitor; see datasheet)



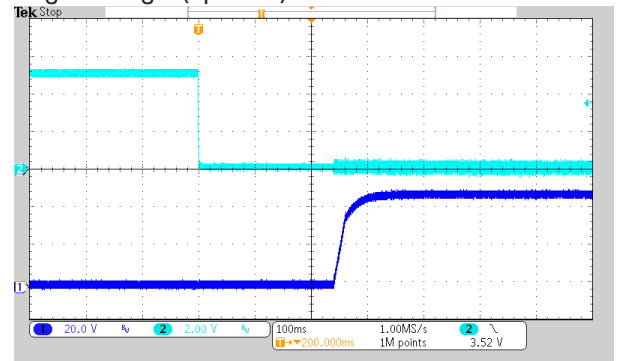
Transient Response to Dynamic Load Change (25%)



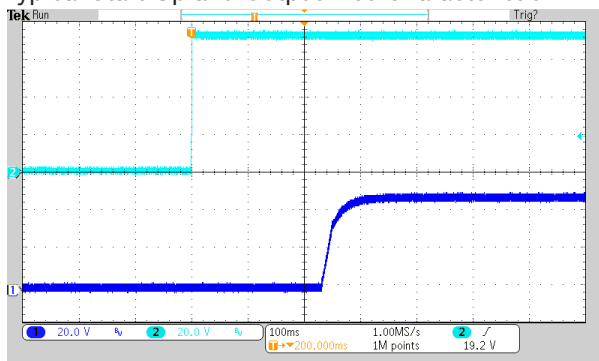
Remote on/off Voltage Start-Up Characteristic
Positive logic (standard)



Remote on/off Voltage Start-Up Characteristic
Negative logic (optional)

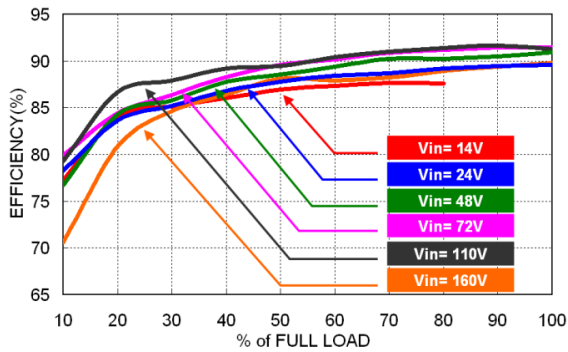


Typical Start-Up and Output Rise Characteristic

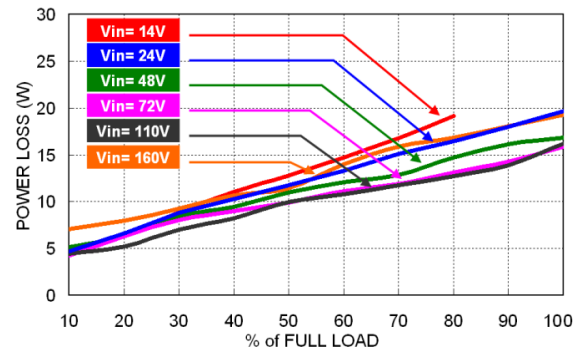


optional model with 53 Vout

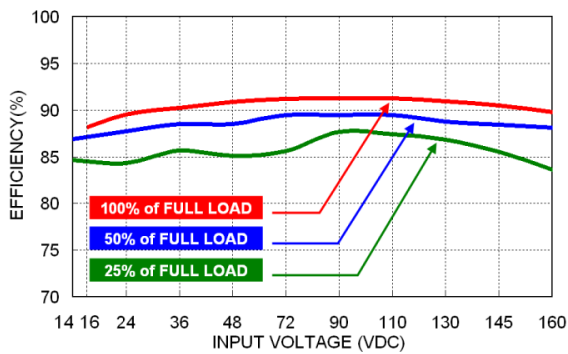
Efficiency versus Output Load



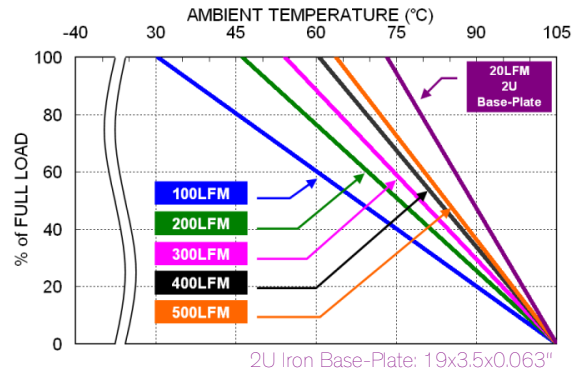
Power Dissipation versus Output Load



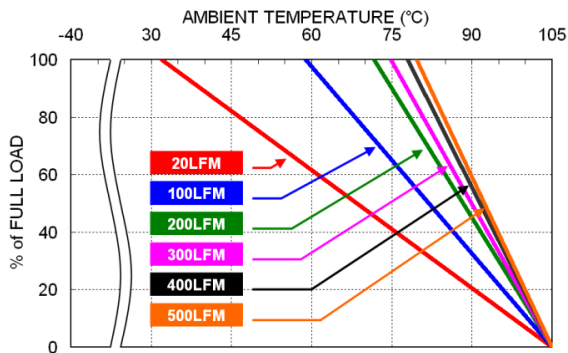
Efficiency versus Input Voltage



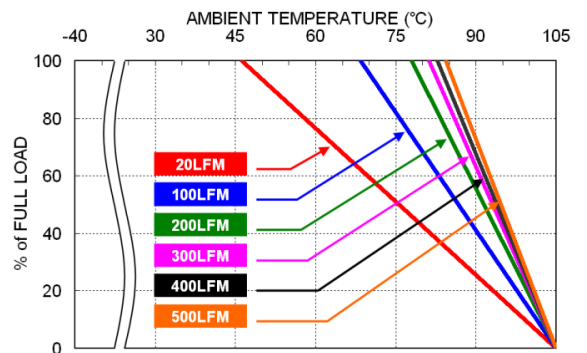
Derating Output Load versus Ambient Temperature without Heatsink



Derating Output Load versus Ambient Temperature with Heatsink TEP-HS6

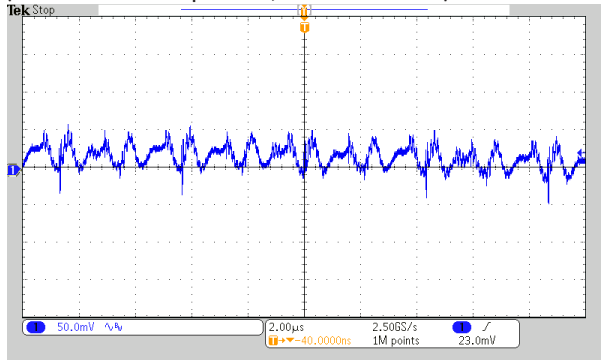


Derating Output Load versus Ambient Temperature with Heatsink TEP-HS7

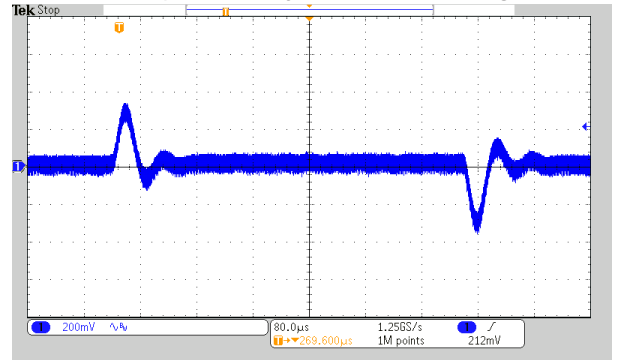


optional model with 53 Vout

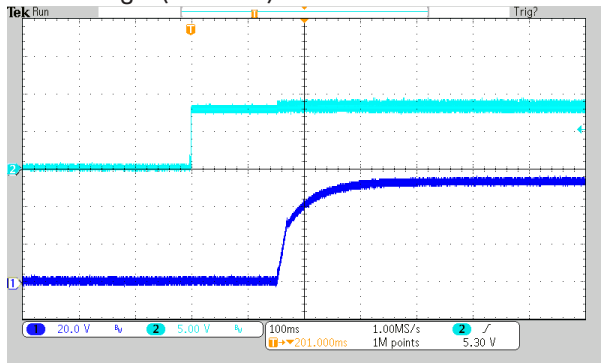
Typical Output Ripple and Noise at 115 Vin
(with external capacitor; see datasheet)



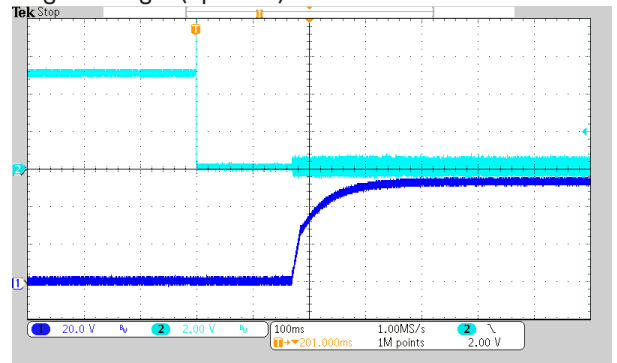
Transient Response to Dynamic Load Change (25%)



Remote on/off Voltage Start-Up Characteristic
Positive logic (standard)



Remote on/off Voltage Start-Up Characteristic
Negative logic (optional)



Typical Start-Up and Output Rise Characteristic

