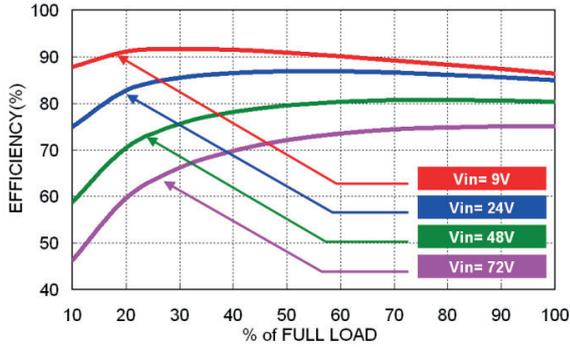


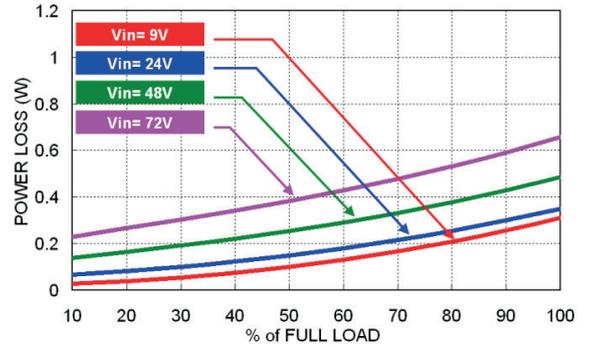
Characteristic Curves

TSR 0.6-4833WI

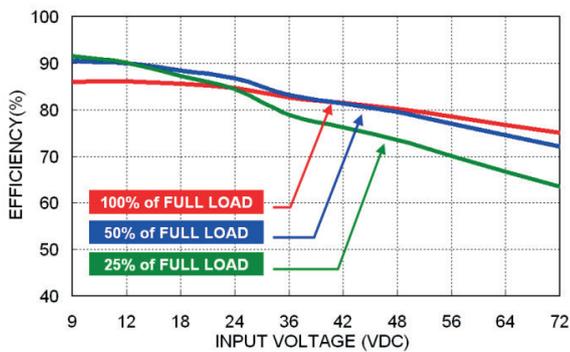
Efficiency vs Output Load



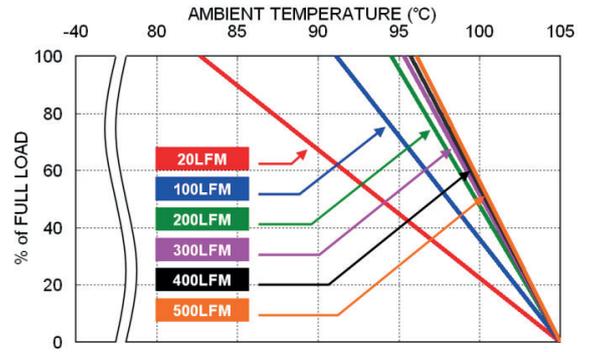
Power Dissipation vs Output Load



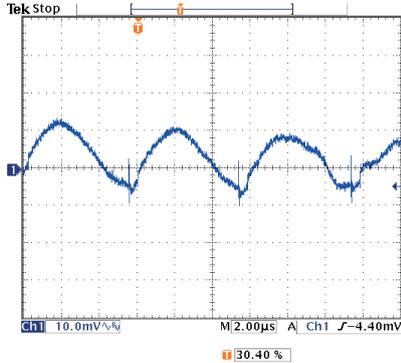
Efficiency vs Input Voltage



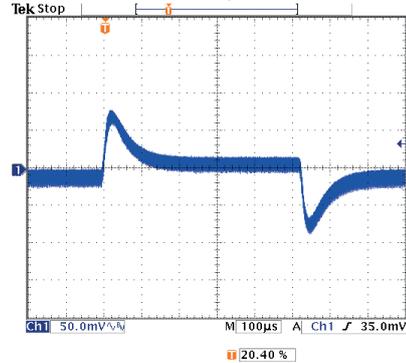
Derating Output Load versus Ambient Temperature



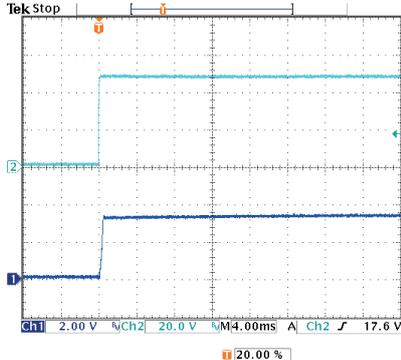
Typical Output Ripple and Noise



Transient Response to Dynamic Load Change (25%)

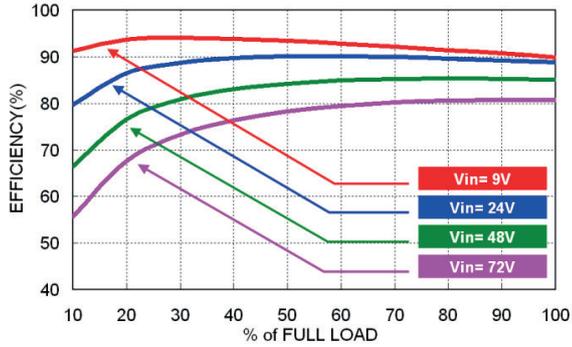


Typical Input Start-Up and Output Rise Characteristic

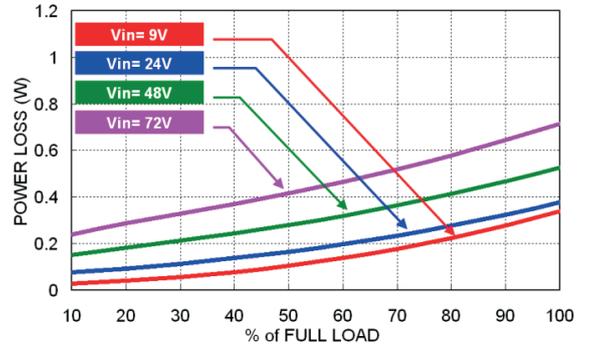


TSR 0.6-4850WI

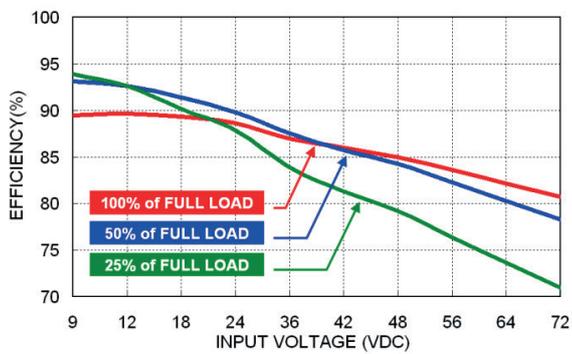
Efficiency vs Output Load



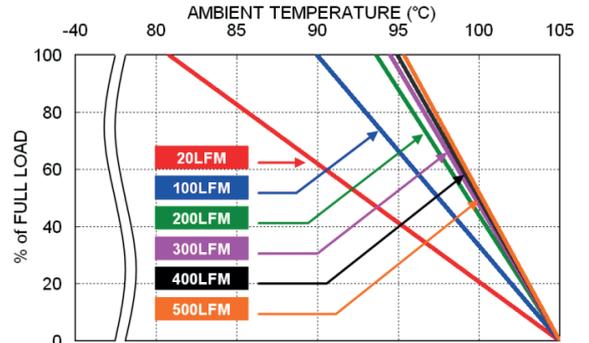
Power Dissipation vs Output Load



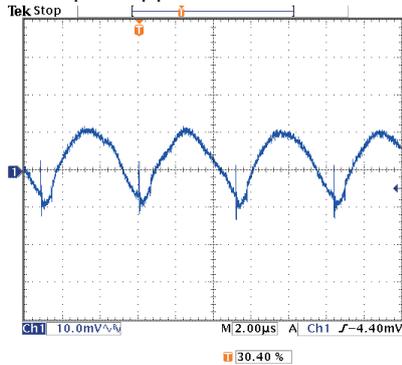
Efficiency vs Input Voltage



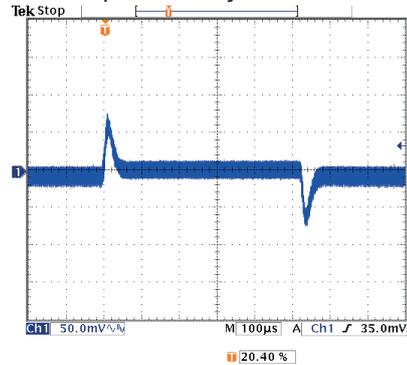
Derating Output Load versus Ambient Temperature



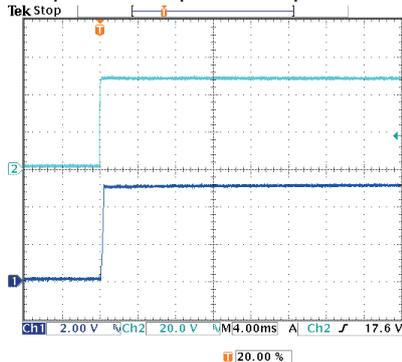
Typical Output Ripple and Noise



Transient Response to Dynamic Load Change (25%)

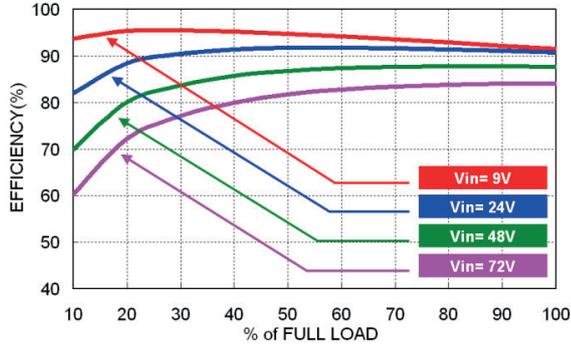


Typical Input Start-Up and Output Rise Characteristic

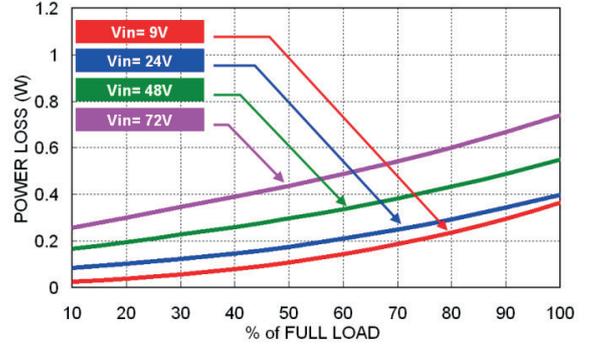


TSR 0.6-4865WI

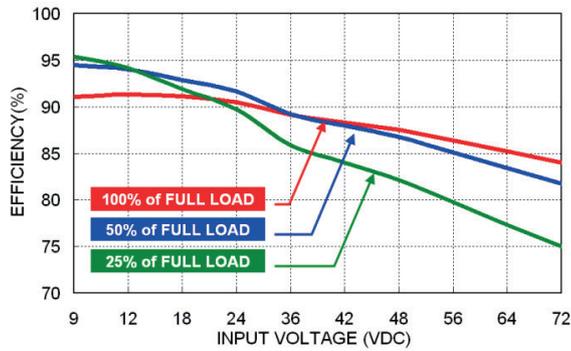
Efficiency vs Output Load



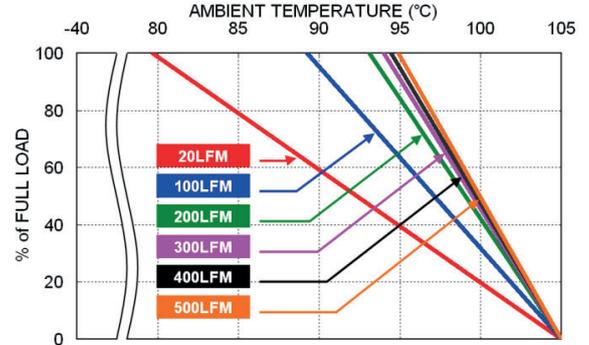
Power Dissipation vs Output Load



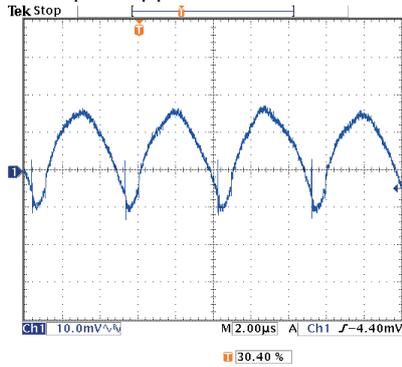
Efficiency vs Input Voltage



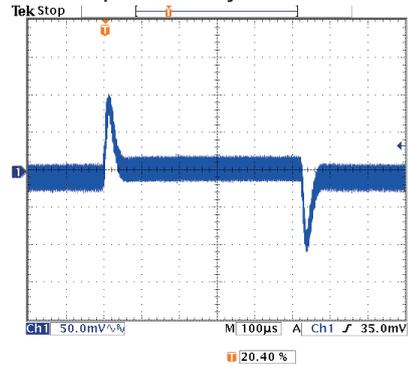
Derating Output Load versus Ambient Temperature



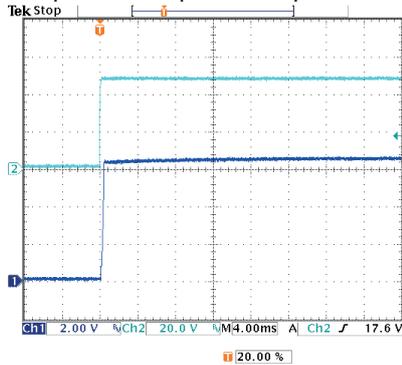
Typical Output Ripple and Noise



Transient Response to Dynamic Load Change (25%)

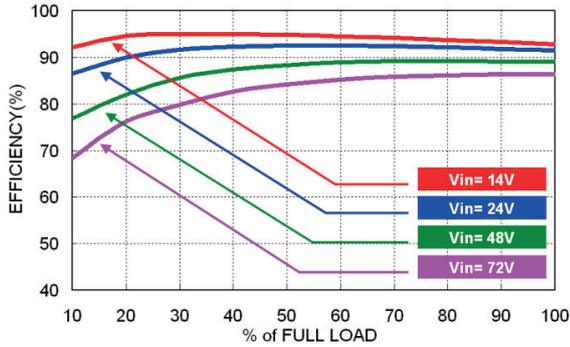


Typical Input Start-Up and Output Rise Characteristic

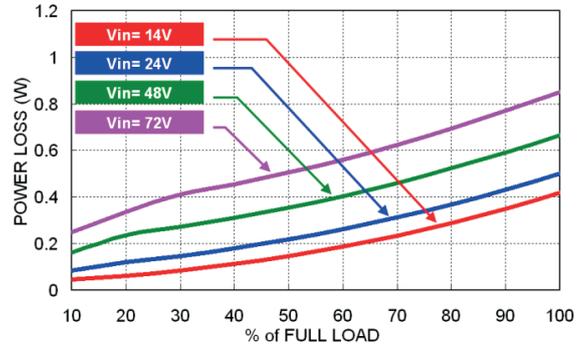


TSR 0.6-4890WI

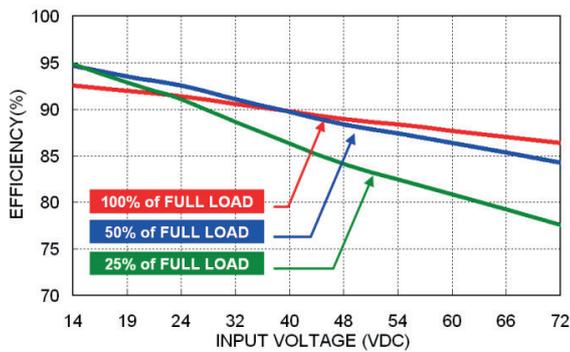
Efficiency vs Output Load



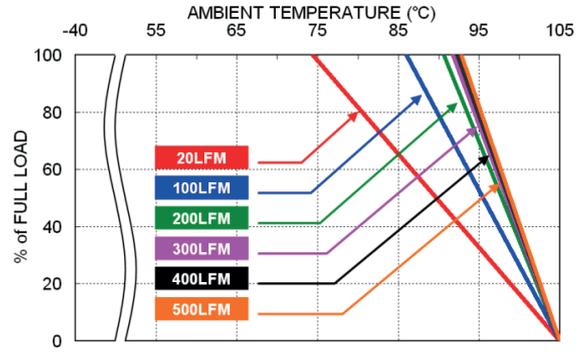
Power Dissipation vs Output Load



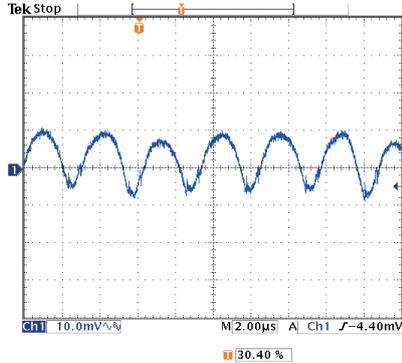
Efficiency vs Input Voltage



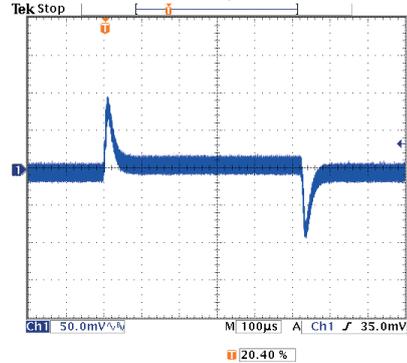
Derating Output Load versus Ambient Temperature



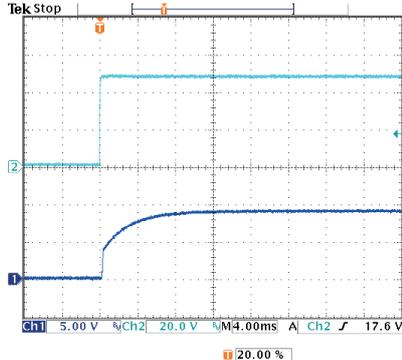
Typical Output Ripple and Noise



Transient Response to Dynamic Load Change (25%)

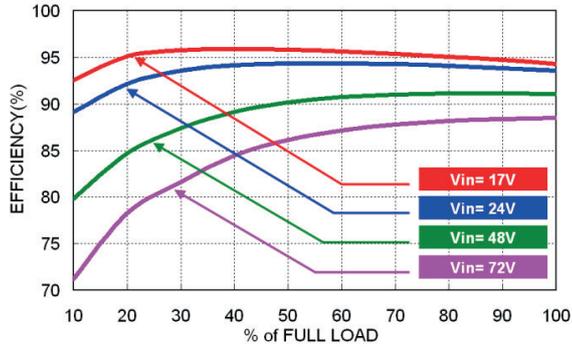


Typical Input Start-Up and Output Rise Characteristic

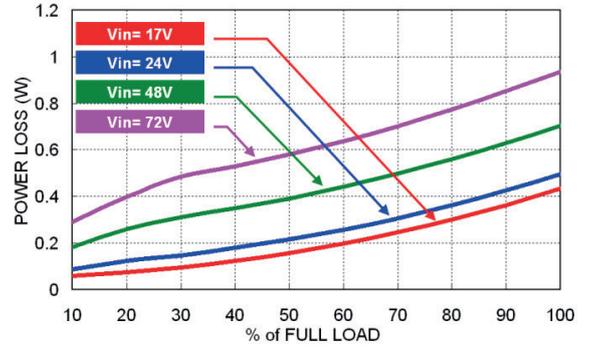


TSR 0.6-48120WI

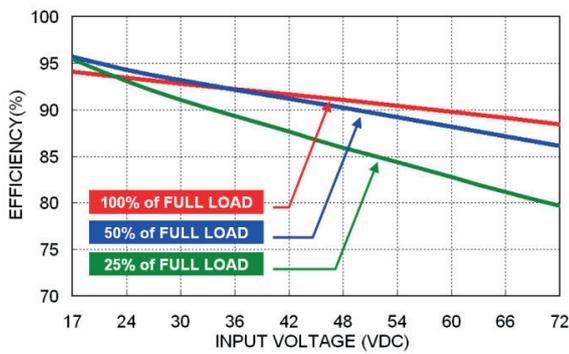
Efficiency vs Output Load



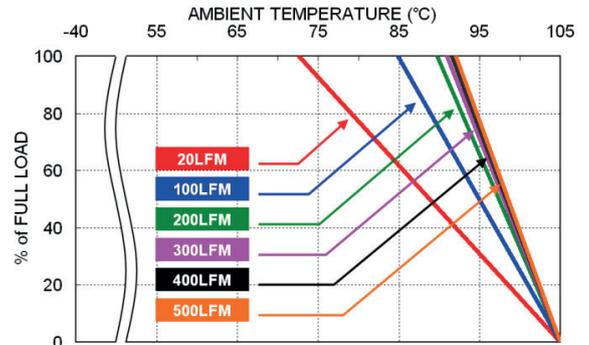
Power Dissipation vs Output Load



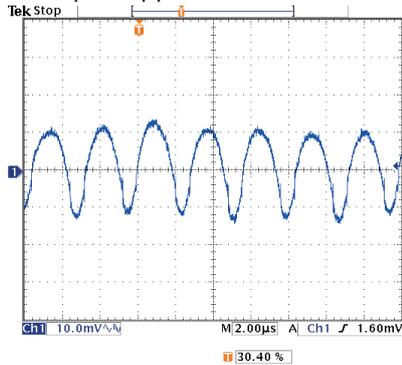
Efficiency vs Input Voltage



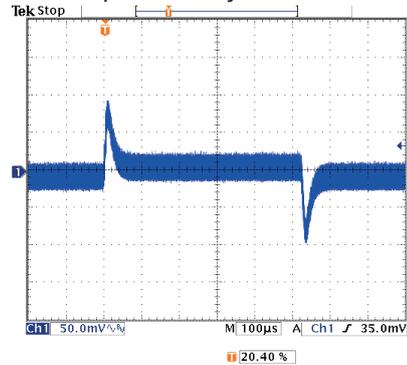
Derating Output Load versus Ambient Temperature



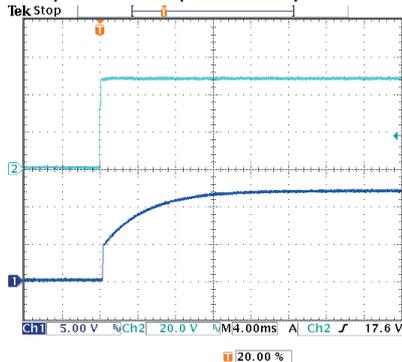
Typical Output Ripple and Noise



Transient Response to Dynamic Load Change (25%)

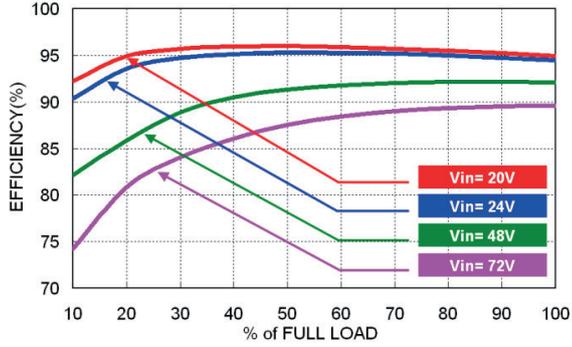


Typical Input Start-Up and Output Rise Characteristic

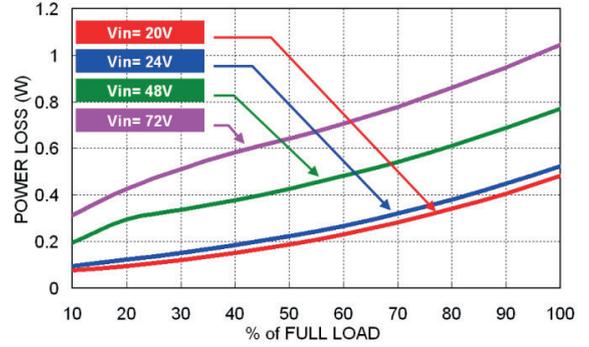


TSR 0.6-48150WI

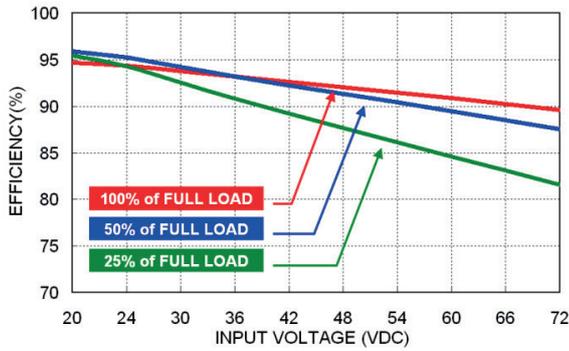
Efficiency vs Output Load



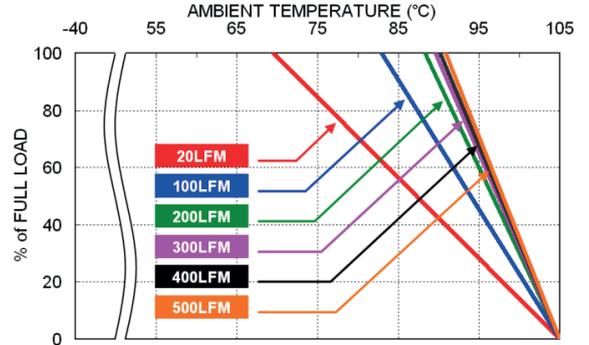
Power Dissipation vs Output Load



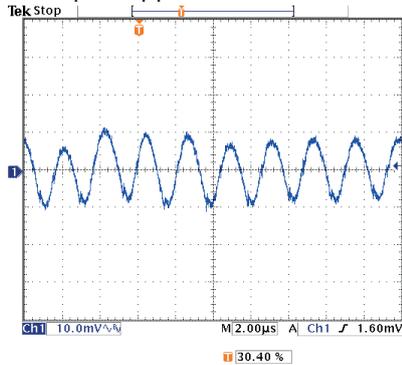
Efficiency vs Input Voltage



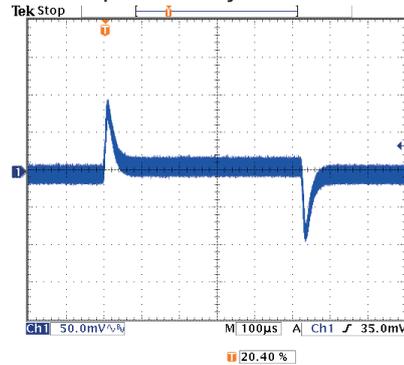
Derating Output Load versus Ambient Temperature



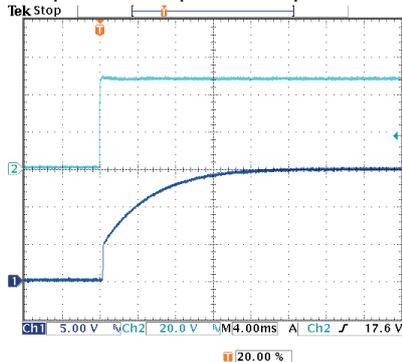
Typical Output Ripple and Noise



Transient Response to Dynamic Load Change (25%)

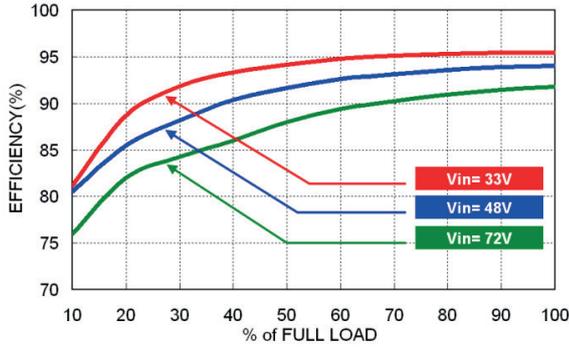


Typical Input Start-Up and Output Rise Characteristic

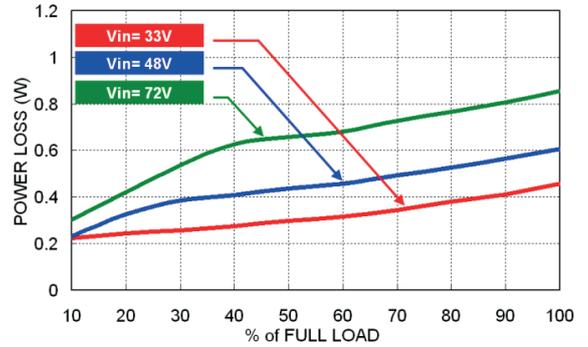


TSR 0.6-48240WI

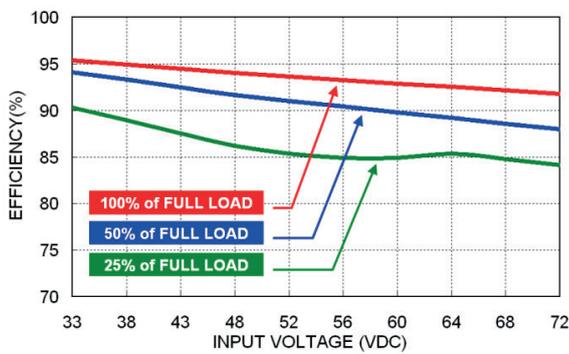
Efficiency vs Output Load



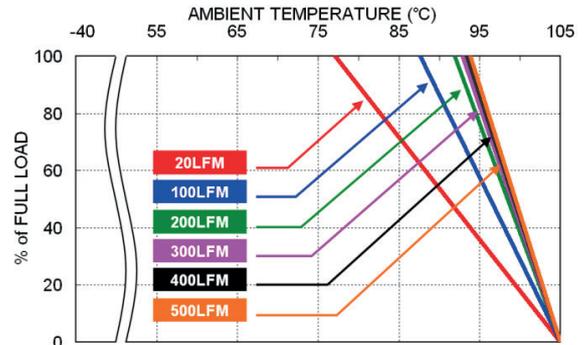
Power Dissipation vs Output Load



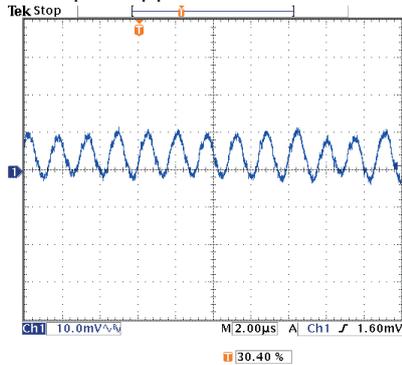
Efficiency vs Input Voltage



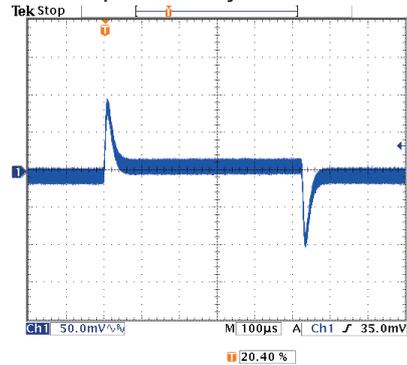
Derating Output Load versus Ambient Temperature



Typical Output Ripple and Noise



Transient Response to Dynamic Load Change (25%)



Typical Input Start-Up and Output Rise Characteristic

