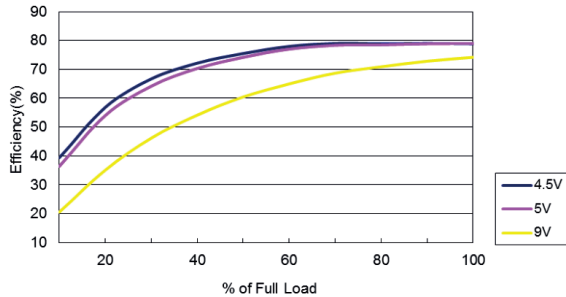


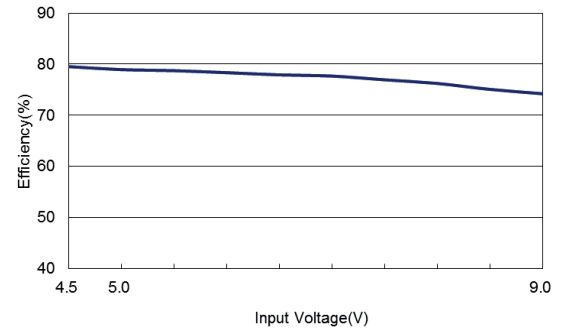
Characteristic Curves

TMR 1-0511

Efficiency vs Output Load



Efficiency vs Input Voltage



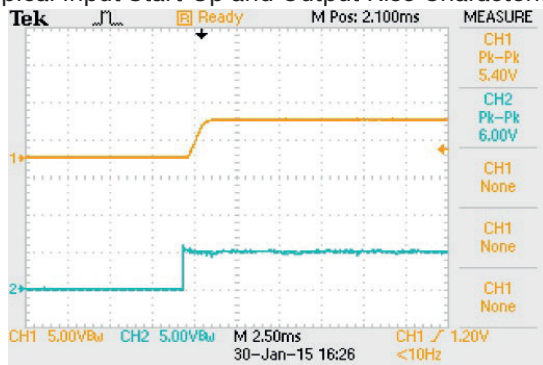
Typical Output Ripple and Noise



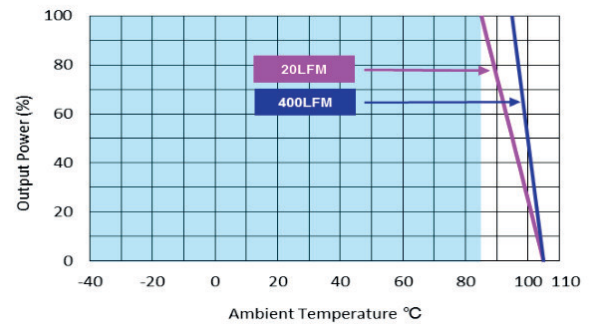
Transient Response to Dynamic Load Change (25%)



Typical Input Start-Up and Output Rise Characteristic

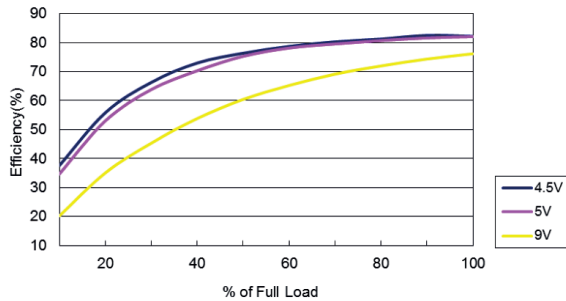


Derating Output Load versus Ambient Temperature

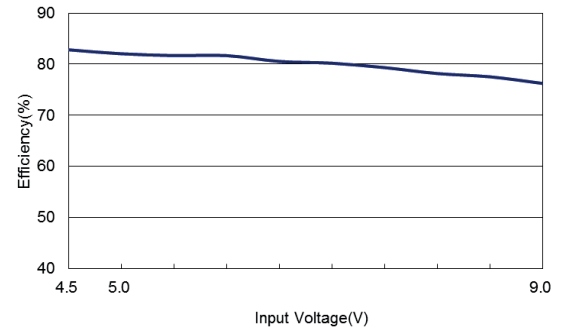


TMR 1-0512

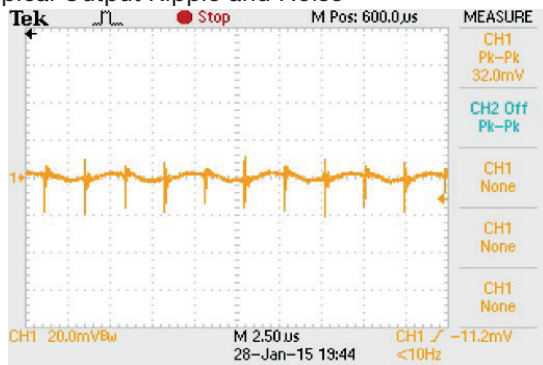
Efficiency vs Output Load



Efficiency vs Input Voltage



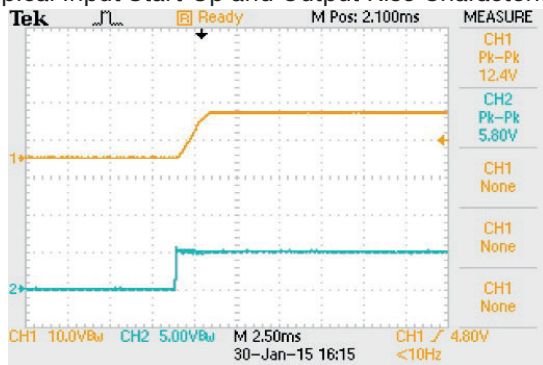
Typical Output Ripple and Noise



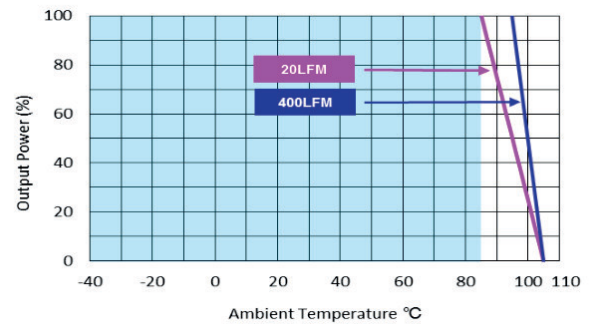
Transient Response to Dynamic Load Change (25%)



Typical Input Start-Up and Output Rise Characteristic

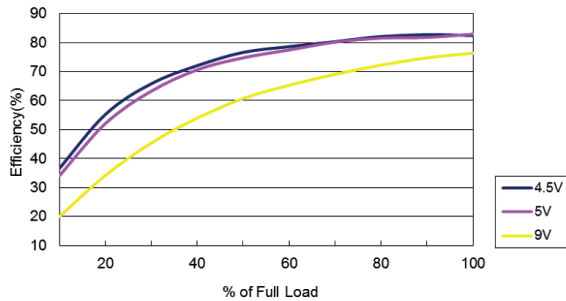


Derating Output Load versus Ambient Temperature

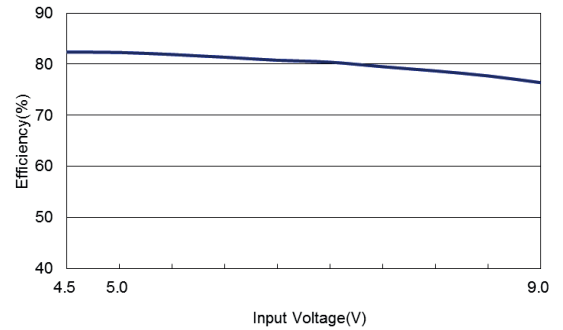


TMR 1-0513

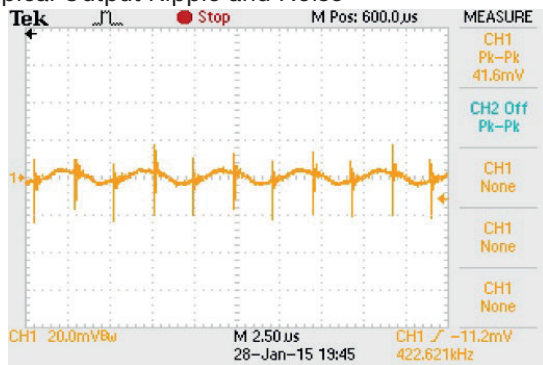
Efficiency vs Output Load



Efficiency vs Input Voltage



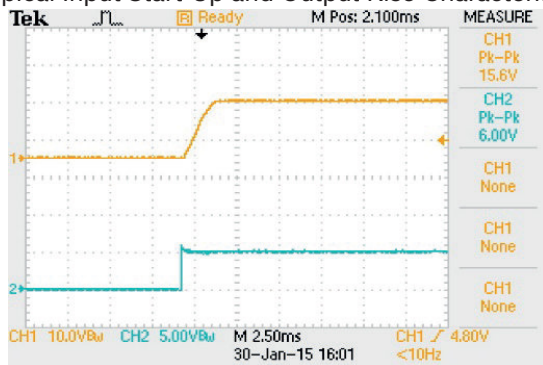
Typical Output Ripple and Noise



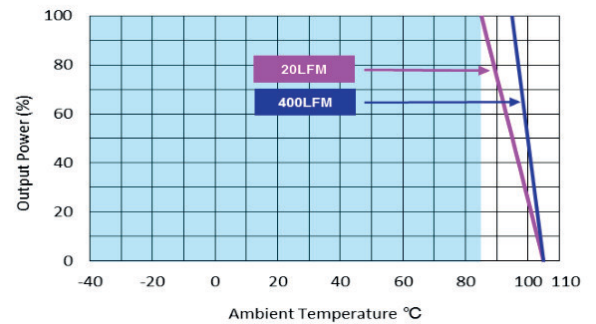
Transient Response to Dynamic Load Change (25%)



Typical Input Start-Up and Output Rise Characteristic

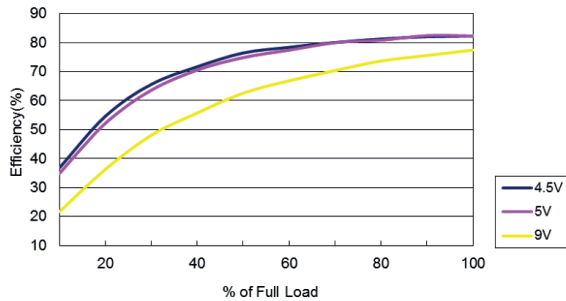


Derating Output Load versus Ambient Temperature

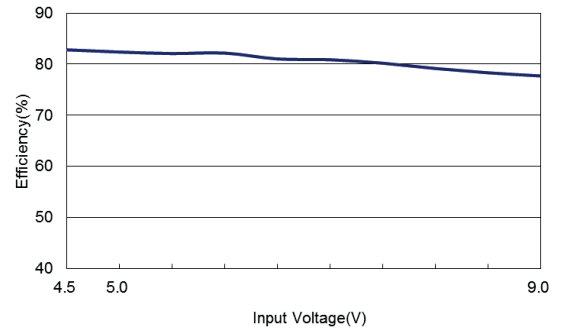


TMR 1-0515

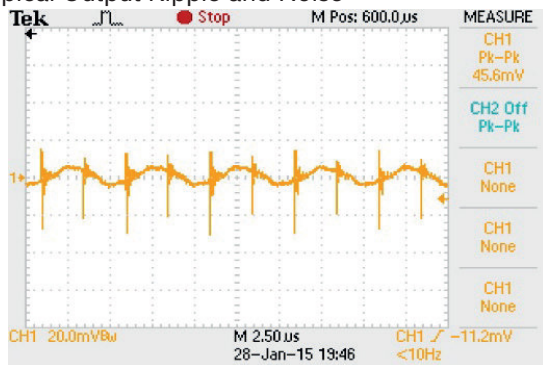
Efficiency vs Output Load



Efficiency vs Input Voltage



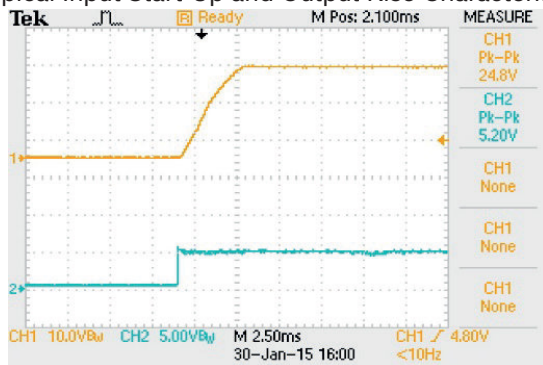
Typical Output Ripple and Noise



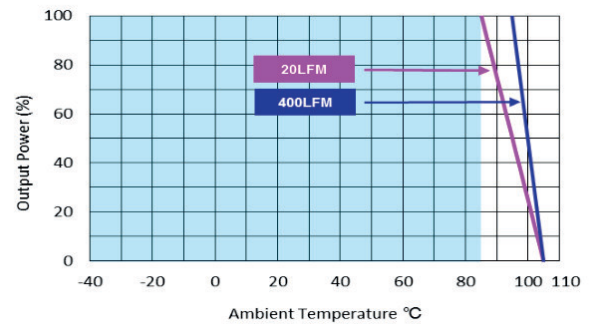
Transient Response to Dynamic Load Change (25%)



Typical Input Start-Up and Output Rise Characteristic

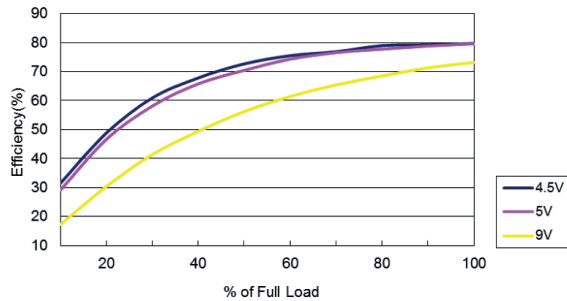


Derating Output Load versus Ambient Temperature

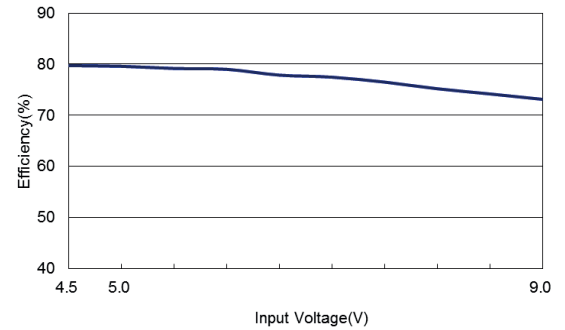


TMR 1-0522

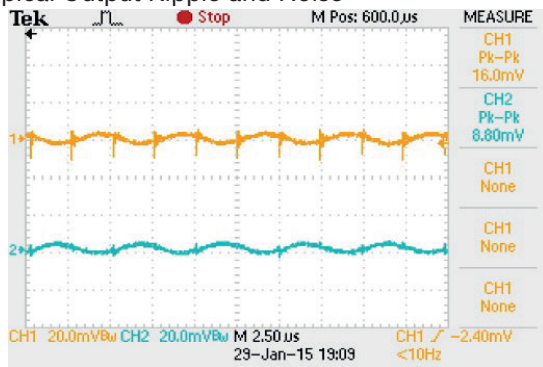
Efficiency vs Output Load



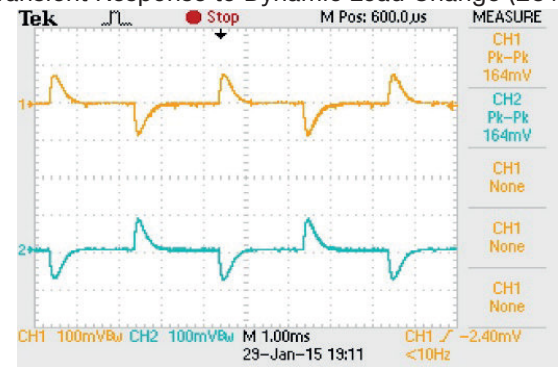
Efficiency vs Input Voltage



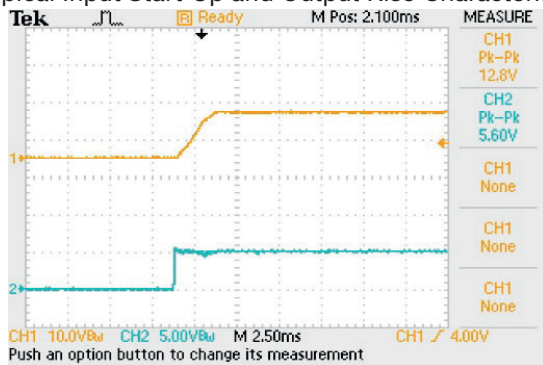
Typical Output Ripple and Noise



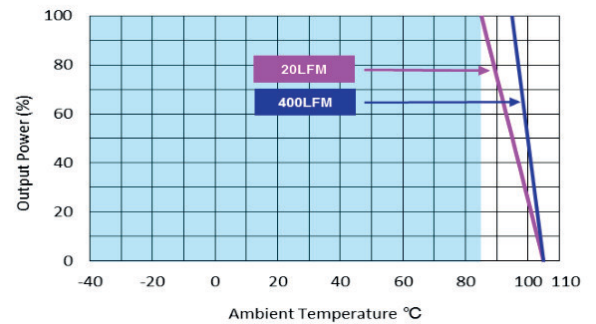
Transient Response to Dynamic Load Change (25%)



Typical Input Start-Up and Output Rise Characteristic

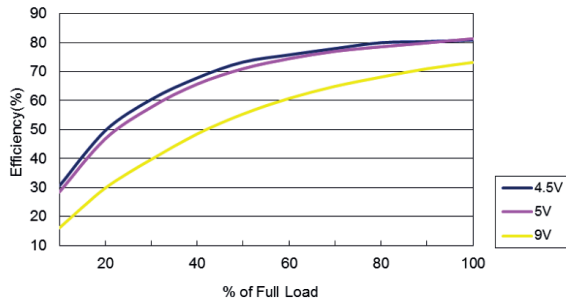


Derating Output Load versus Ambient Temperature

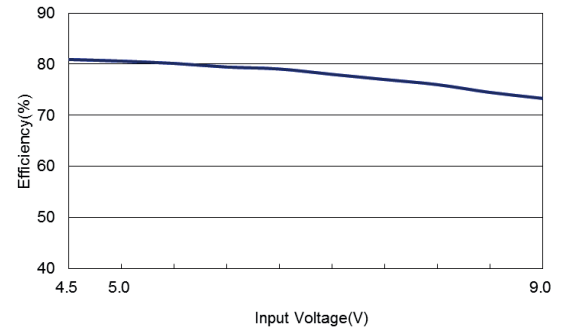


TMR 1-0523

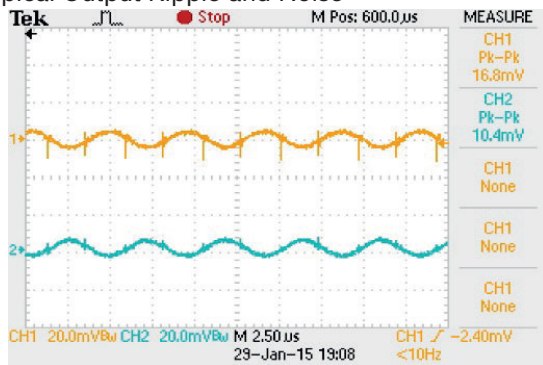
Efficiency vs Output Load



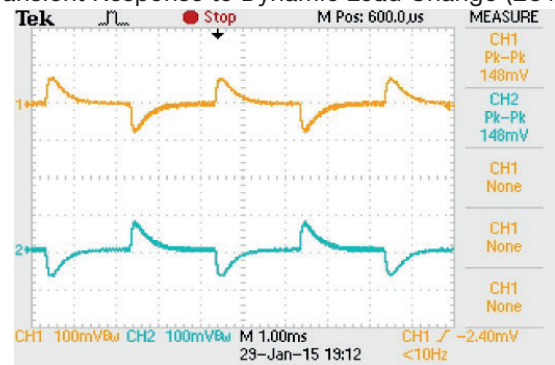
Efficiency vs Input Voltage



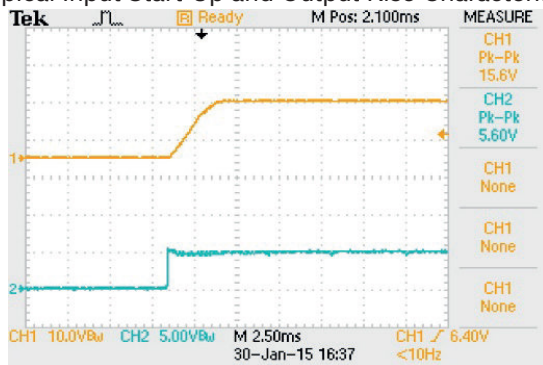
Typical Output Ripple and Noise



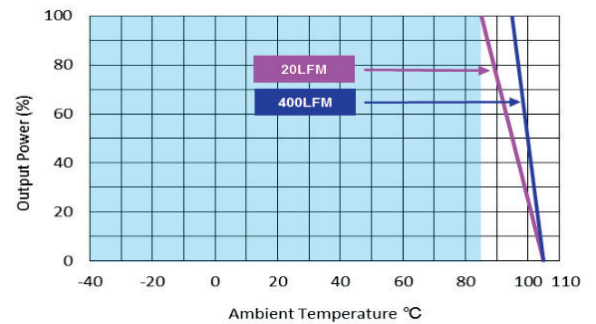
Transient Response to Dynamic Load Change (25%)



Typical Input Start-Up and Output Rise Characteristic

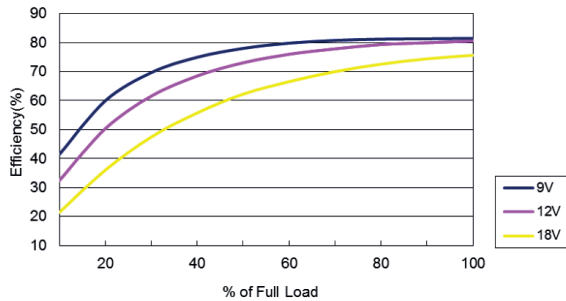


Derating Output Load versus Ambient Temperature

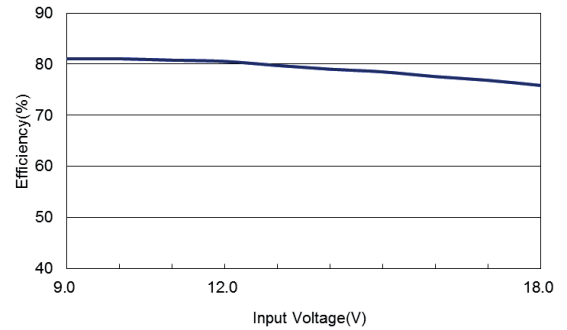


TMR 1-1211

Efficiency vs Output Load



Efficiency vs Input Voltage



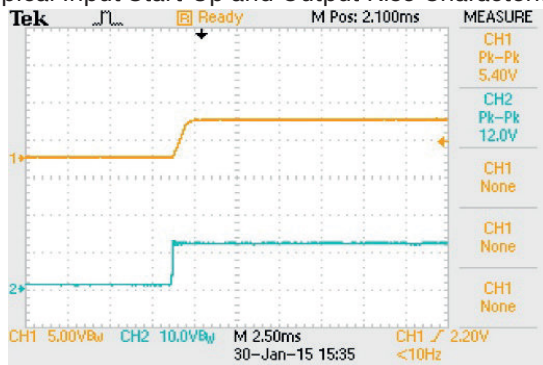
Typical Output Ripple and Noise



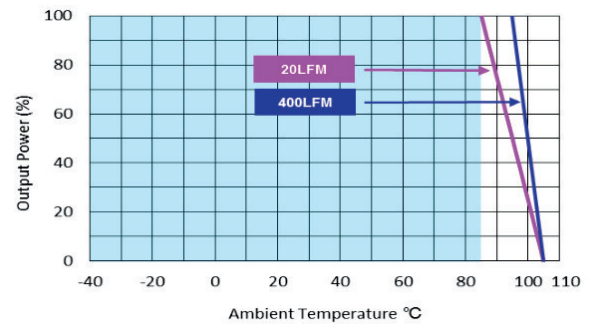
Transient Response to Dynamic Load Change (25%)



Typical Input Start-Up and Output Rise Characteristic

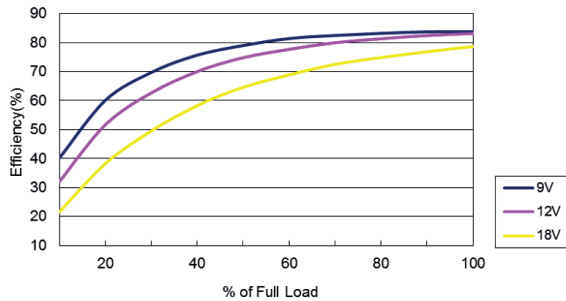


Derating Output Load versus Ambient Temperature

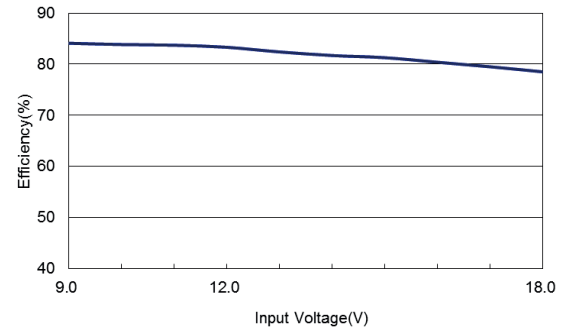


TMR 1-1212

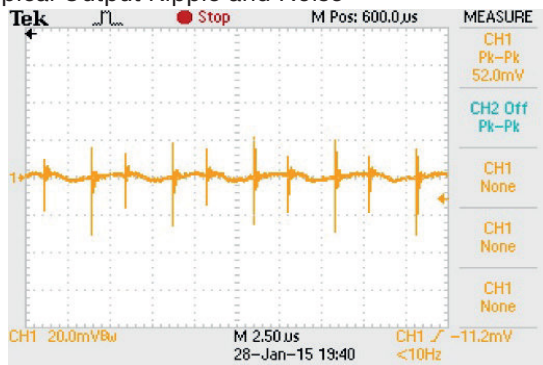
Efficiency vs Output Load



Efficiency vs Input Voltage



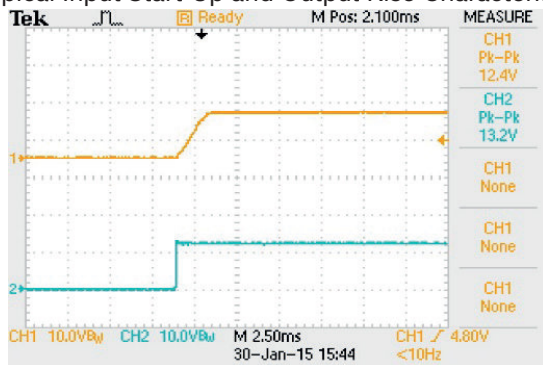
Typical Output Ripple and Noise



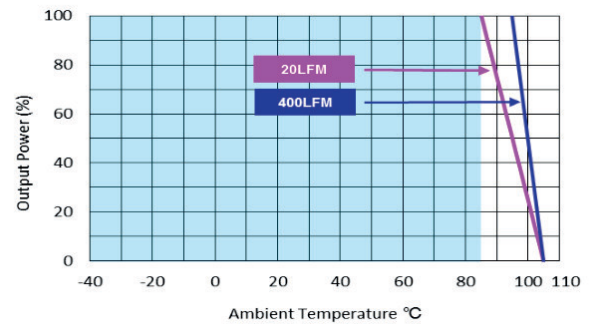
Transient Response to Dynamic Load Change (25%)



Typical Input Start-Up and Output Rise Characteristic

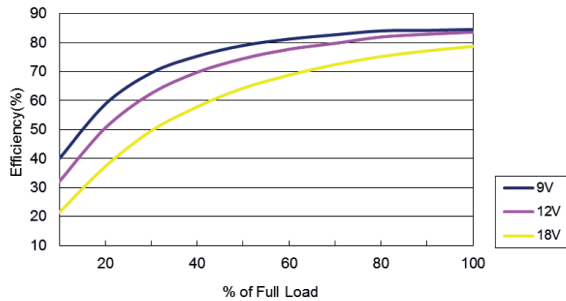


Derating Output Load versus Ambient Temperature

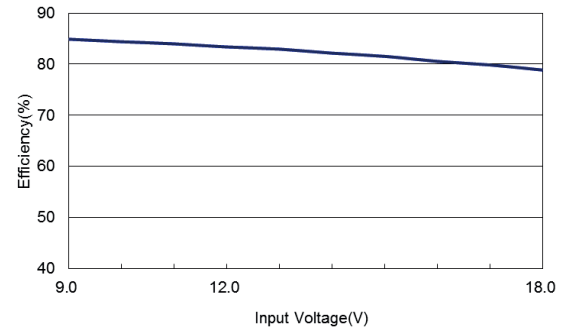


TMR 1-1213

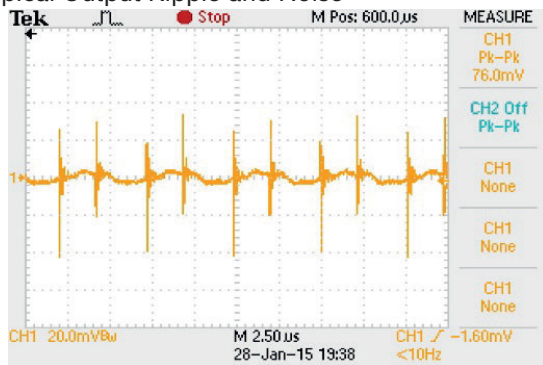
Efficiency vs Output Load



Efficiency vs Input Voltage



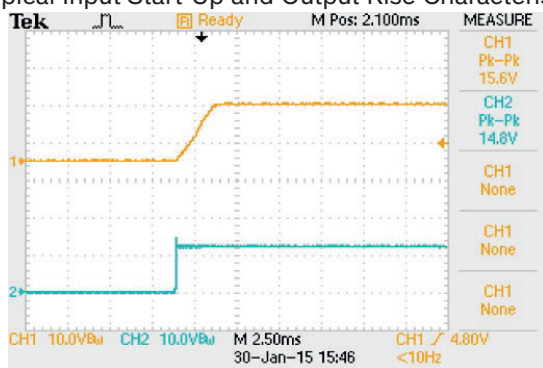
Typical Output Ripple and Noise



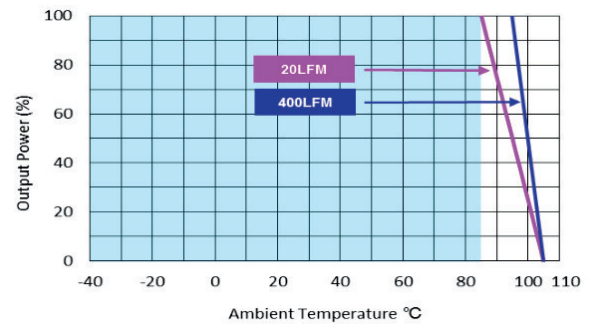
Transient Response to Dynamic Load Change (25%)



Typical Input Start-Up and Output Rise Characteristic

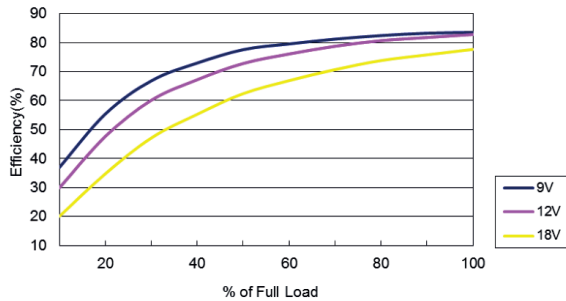


Derating Output Load versus Ambient Temperature

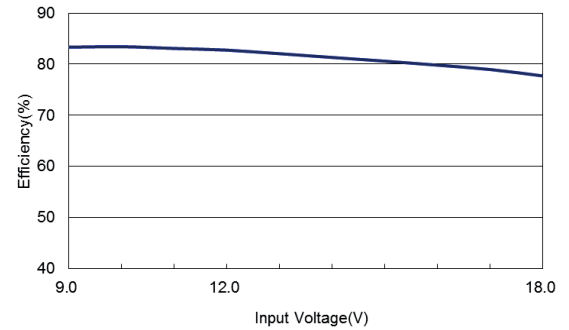


TMR 1-1215

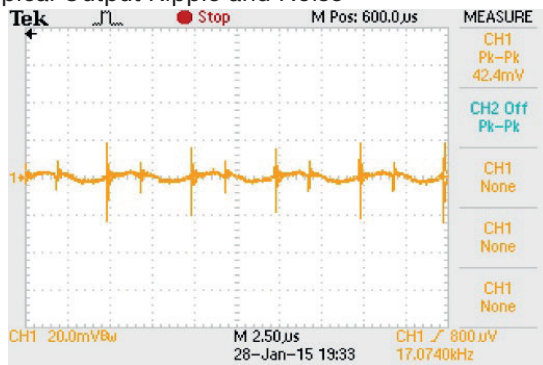
Efficiency vs Output Load



Efficiency vs Input Voltage



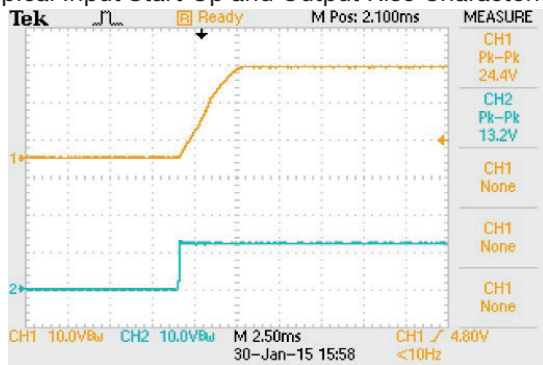
Typical Output Ripple and Noise



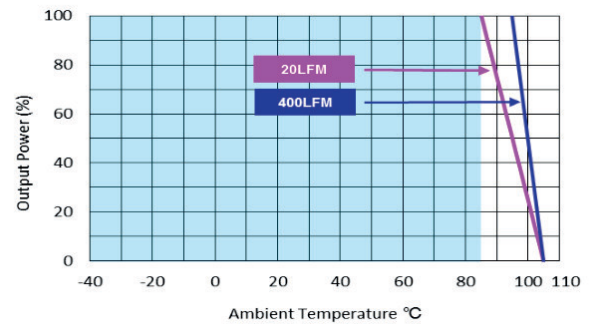
Transient Response to Dynamic Load Change (25%)



Typical Input Start-Up and Output Rise Characteristic

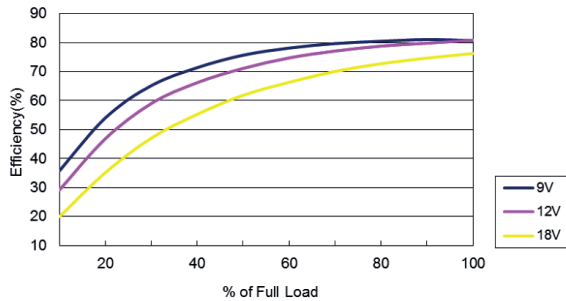


Derating Output Load versus Ambient Temperature

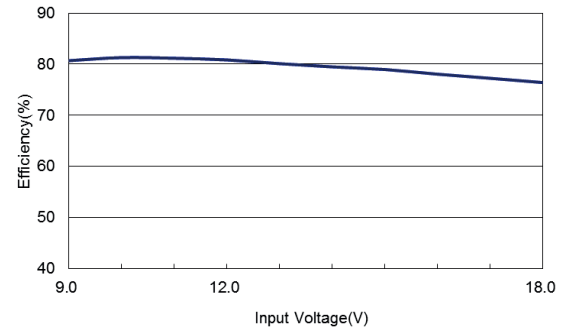


TMR 1-1222

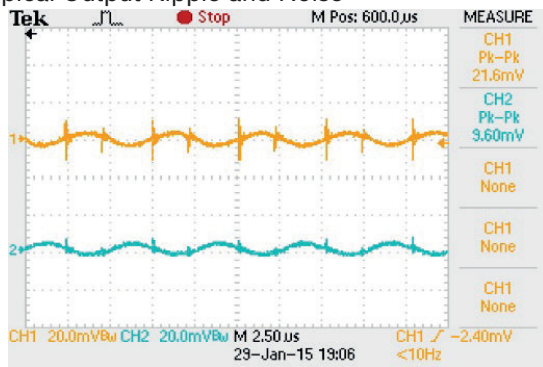
Efficiency vs Output Load



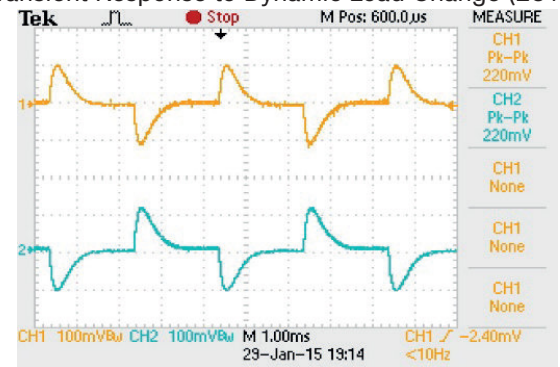
Efficiency vs Input Voltage



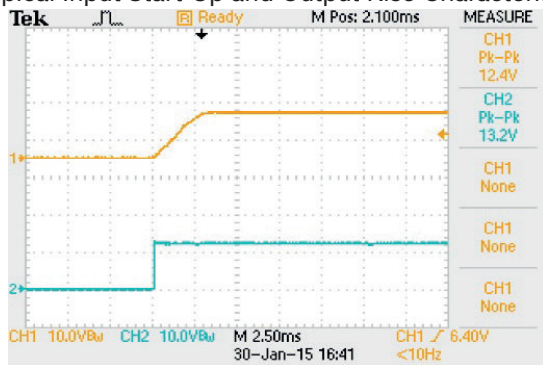
Typical Output Ripple and Noise



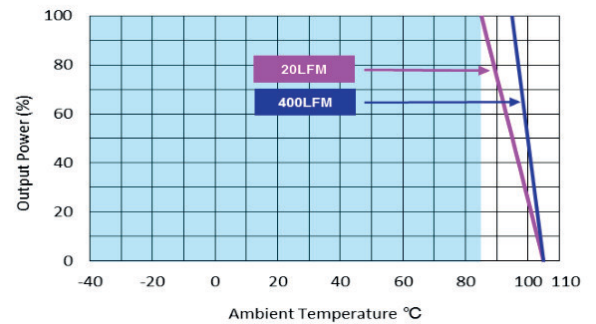
Transient Response to Dynamic Load Change (25%)



Typical Input Start-Up and Output Rise Characteristic

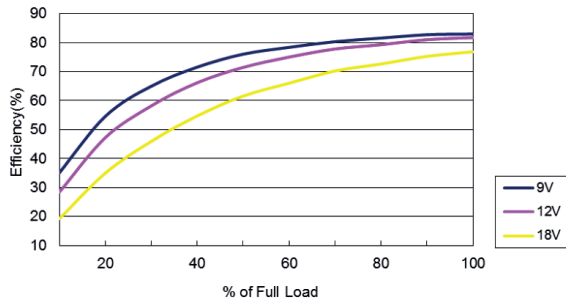


Derating Output Load versus Ambient Temperature

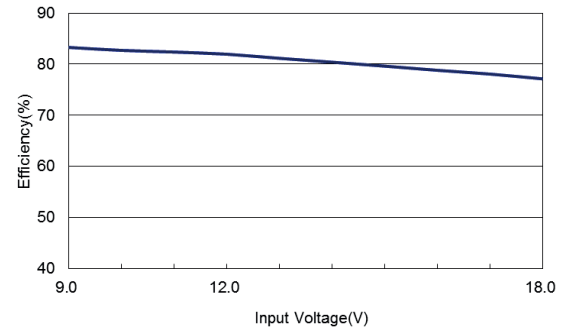


TMR 1-1223

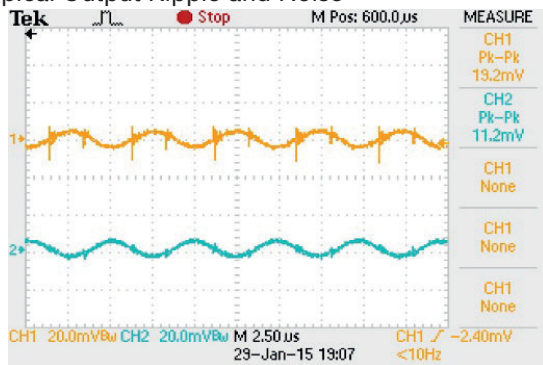
Efficiency vs Output Load



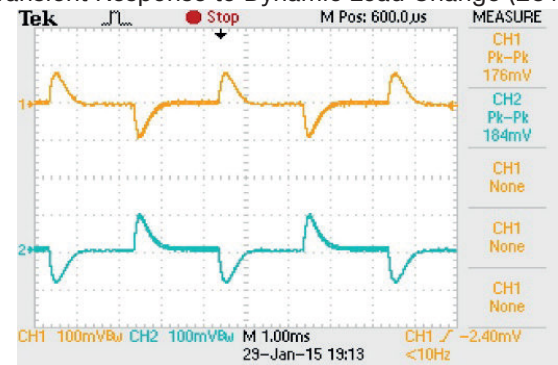
Efficiency vs Input Voltage



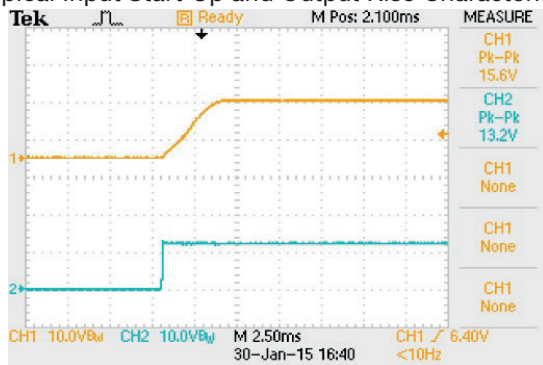
Typical Output Ripple and Noise



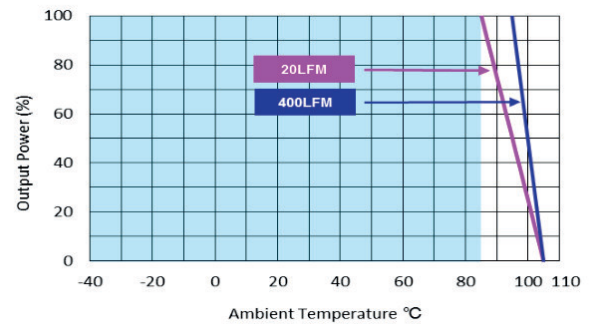
Transient Response to Dynamic Load Change (25%)



Typical Input Start-Up and Output Rise Characteristic

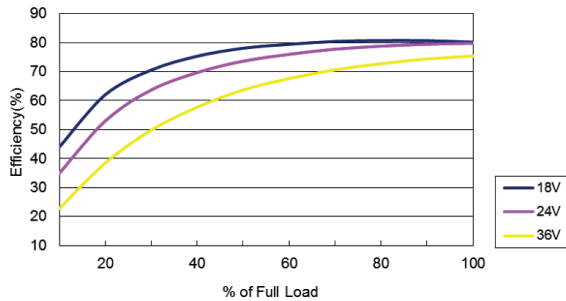


Derating Output Load versus Ambient Temperature

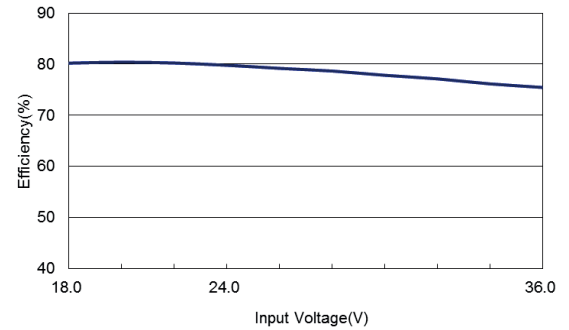


TMR 1-2411

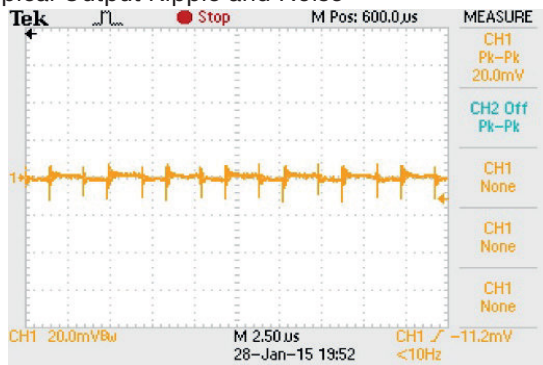
Efficiency vs Output Load



Efficiency vs Input Voltage



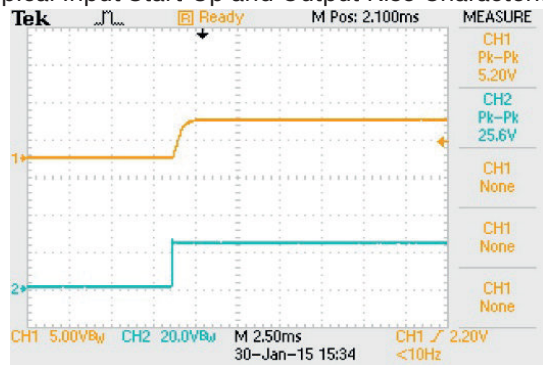
Typical Output Ripple and Noise



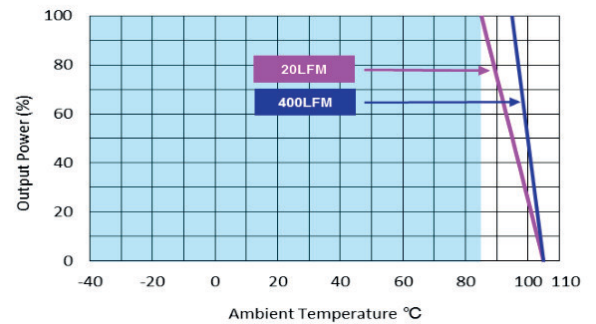
Transient Response to Dynamic Load Change (25%)



Typical Input Start-Up and Output Rise Characteristic

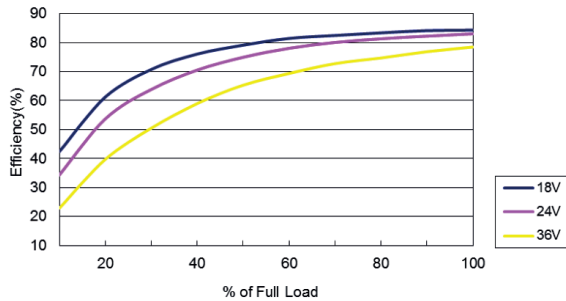


Derating Output Load versus Ambient Temperature

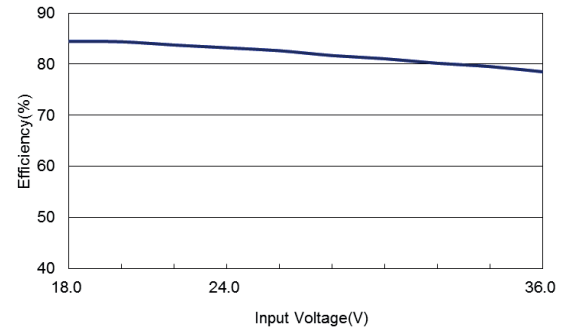


TMR 1-2412

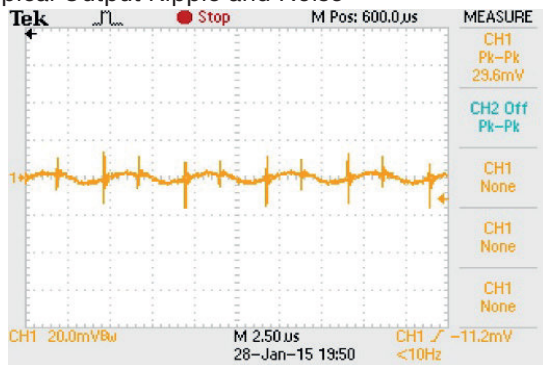
Efficiency vs Output Load



Efficiency vs Input Voltage



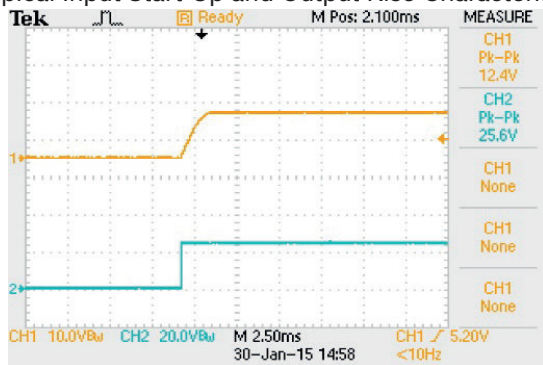
Typical Output Ripple and Noise



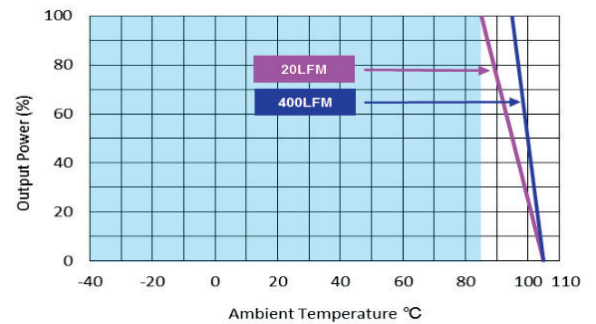
Transient Response to Dynamic Load Change (25%)



Typical Input Start-Up and Output Rise Characteristic

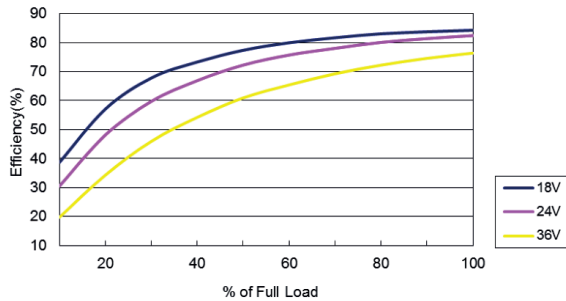


Derating Output Load versus Ambient Temperature

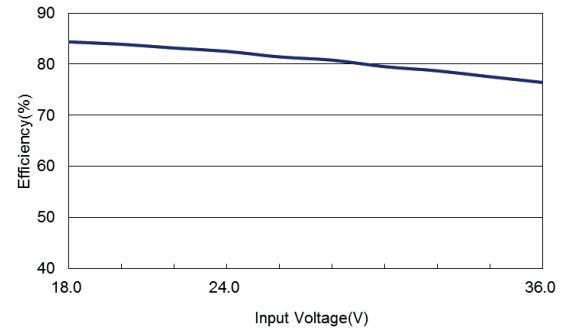


TMR 1-2413

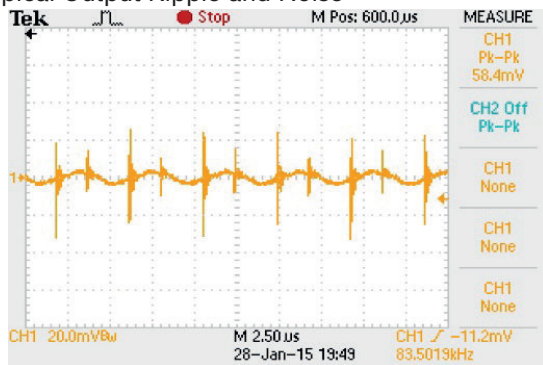
Efficiency vs Output Load



Efficiency vs Input Voltage



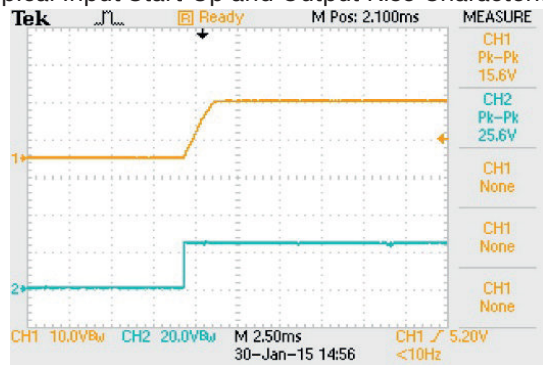
Typical Output Ripple and Noise



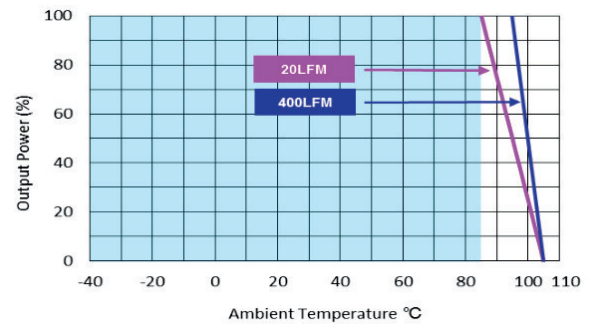
Transient Response to Dynamic Load Change (25%)



Typical Input Start-Up and Output Rise Characteristic

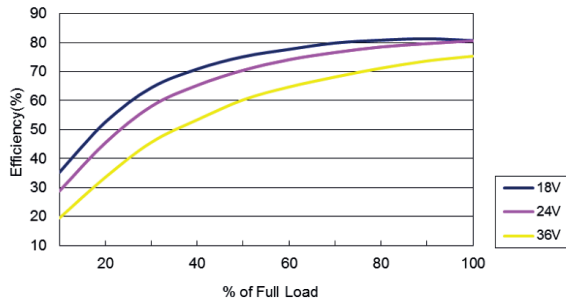


Derating Output Load versus Ambient Temperature

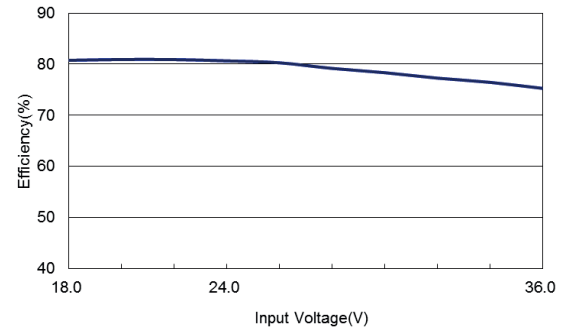


TMR 1-2415

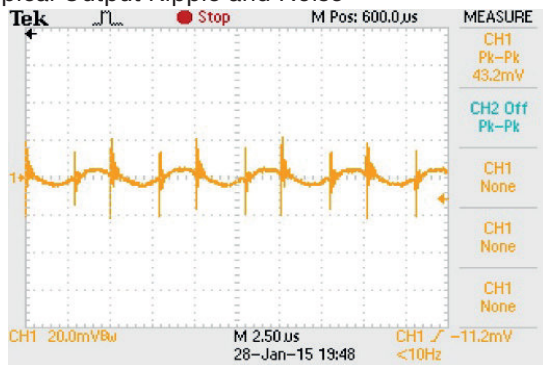
Efficiency vs Output Load



Efficiency vs Input Voltage



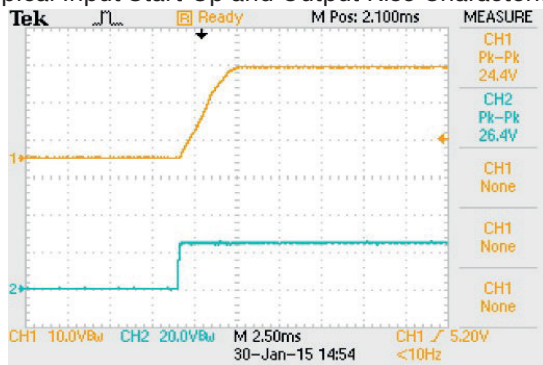
Typical Output Ripple and Noise



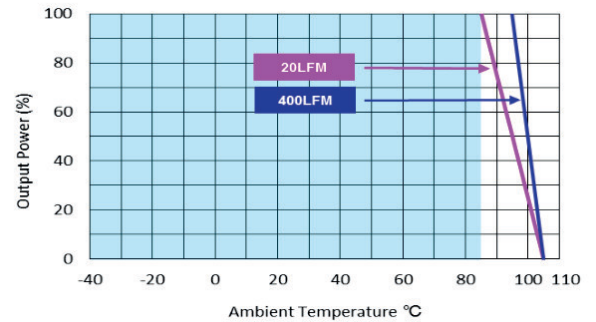
Transient Response to Dynamic Load Change (25%)



Typical Input Start-Up and Output Rise Characteristic

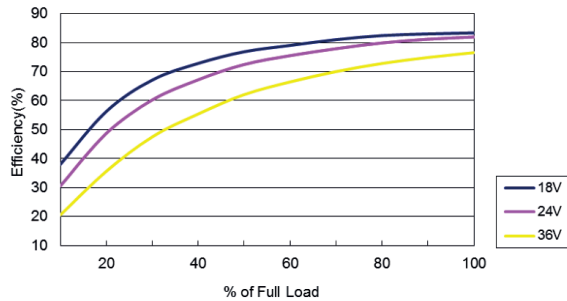


Derating Output Load versus Ambient Temperature

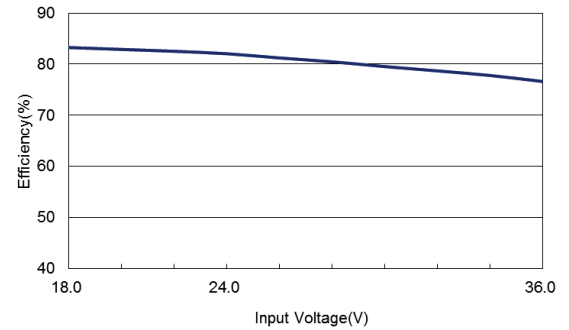


TMR 1-2422

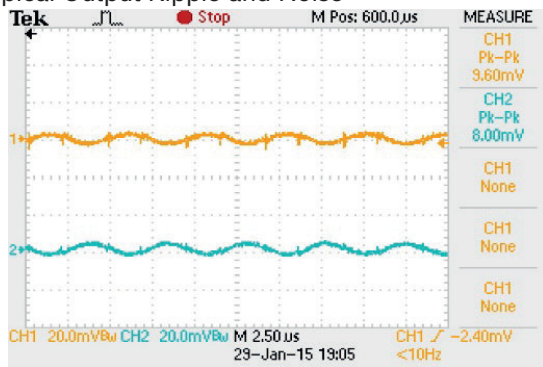
Efficiency vs Output Load



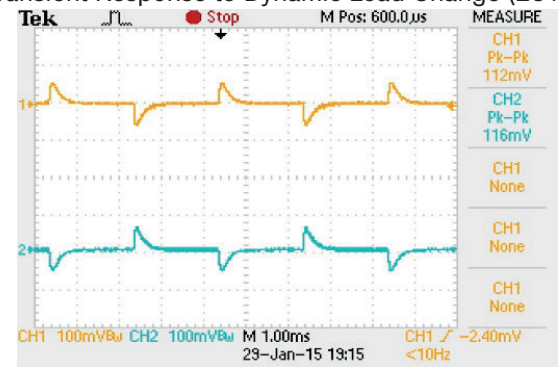
Efficiency vs Input Voltage



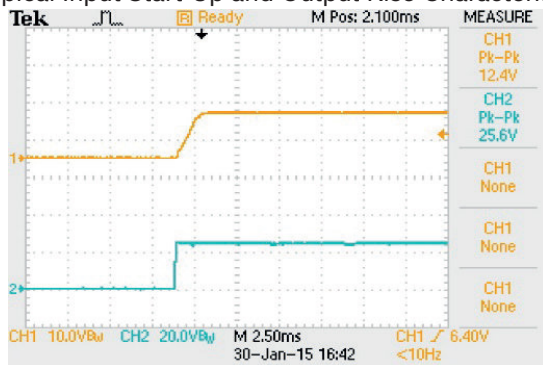
Typical Output Ripple and Noise



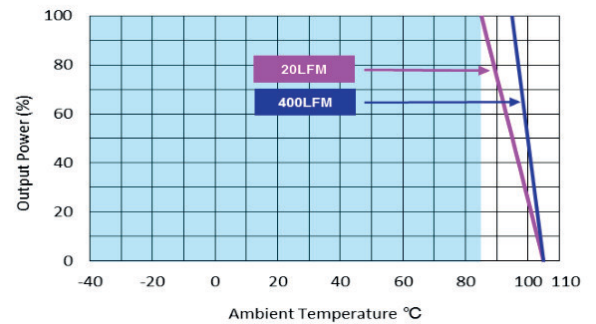
Transient Response to Dynamic Load Change (25%)



Typical Input Start-Up and Output Rise Characteristic

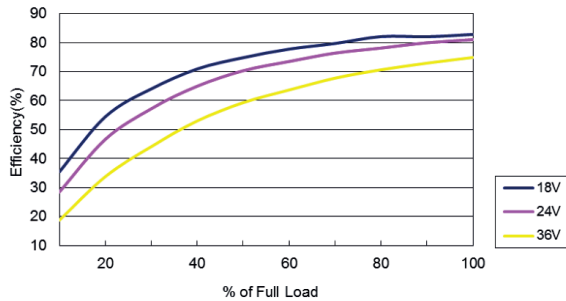


Derating Output Load versus Ambient Temperature

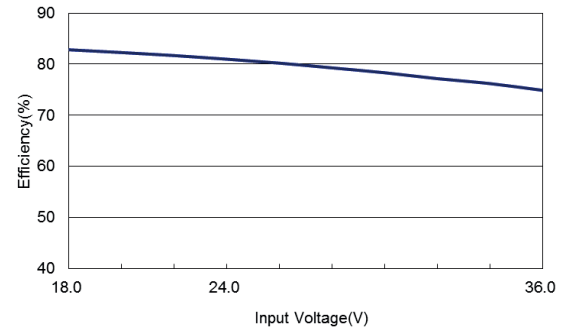


TMR 1-2423

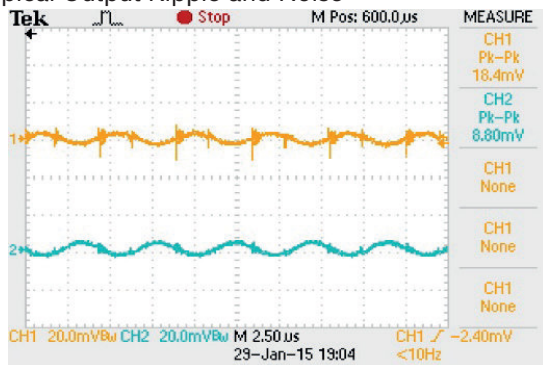
Efficiency vs Output Load



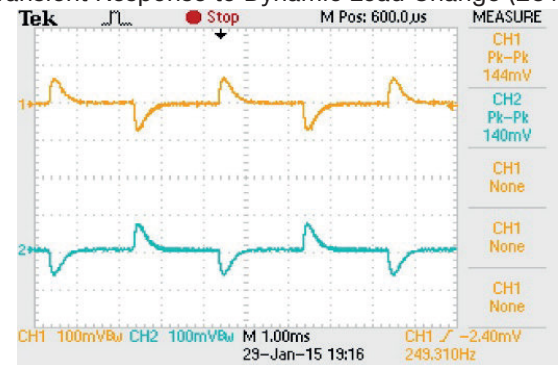
Efficiency vs Input Voltage



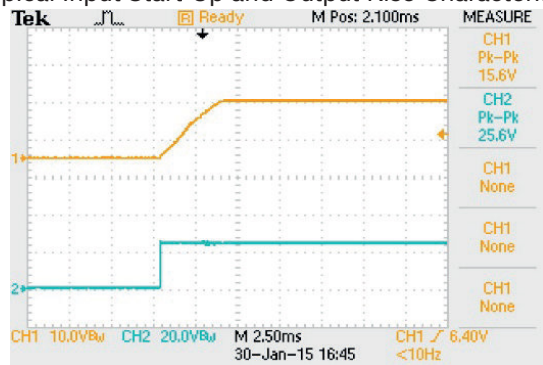
Typical Output Ripple and Noise



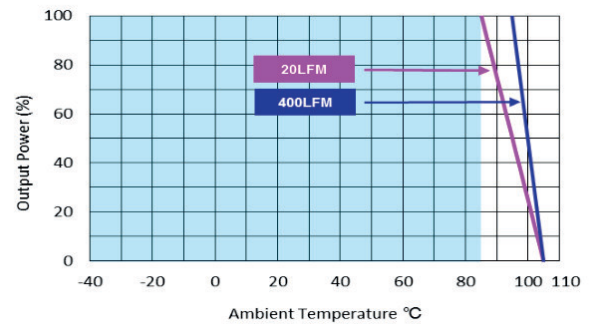
Transient Response to Dynamic Load Change (25%)



Typical Input Start-Up and Output Rise Characteristic

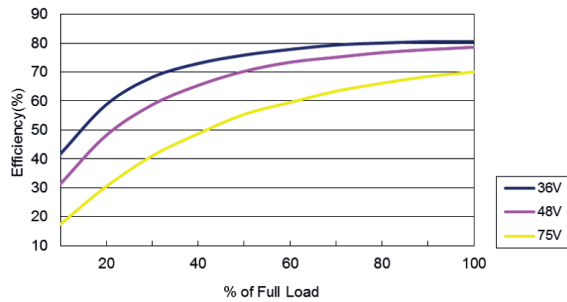


Derating Output Load versus Ambient Temperature

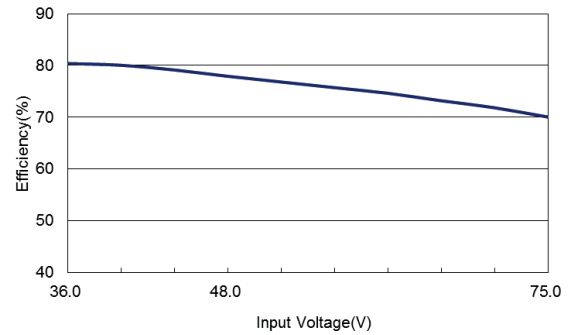


TMR 1-4811

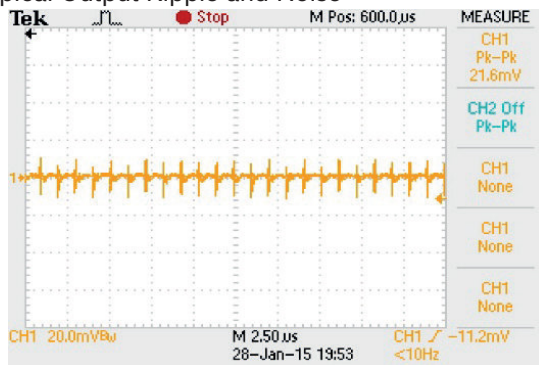
Efficiency vs Output Load



Efficiency vs Input Voltage



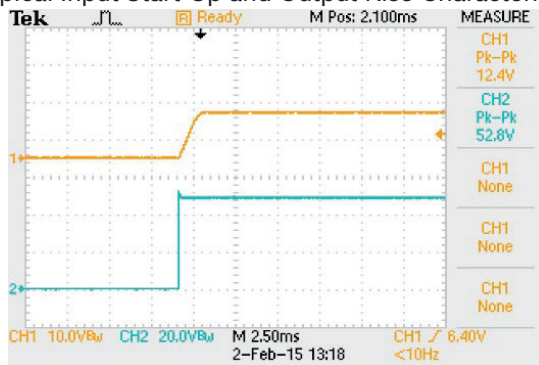
Typical Output Ripple and Noise



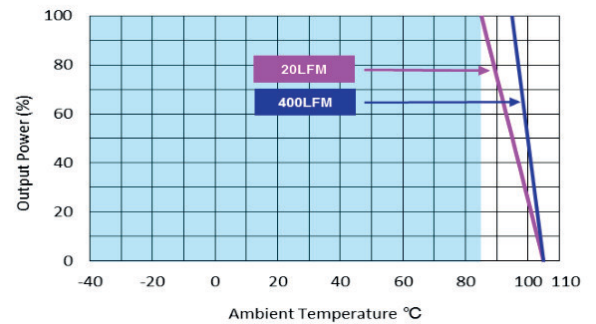
Transient Response to Dynamic Load Change (25%)



Typical Input Start-Up and Output Rise Characteristic

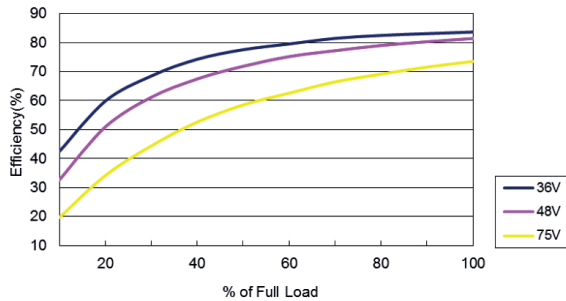


Derating Output Load versus Ambient Temperature

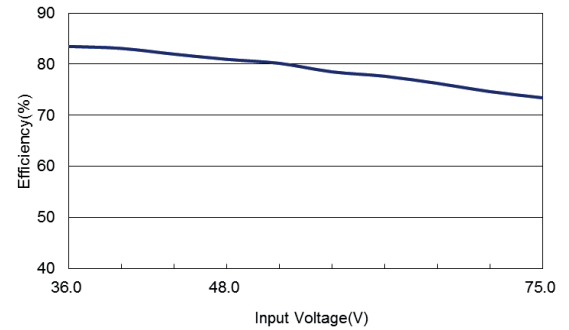


TMR 1-4812

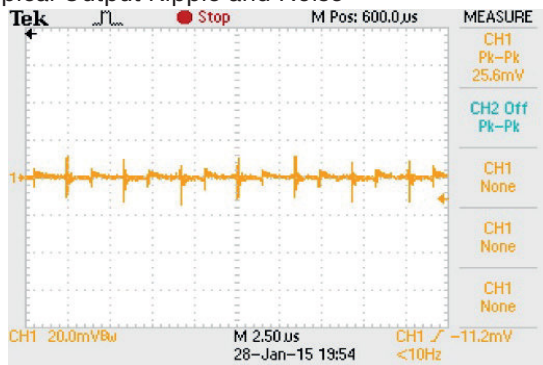
Efficiency vs Output Load



Efficiency vs Input Voltage



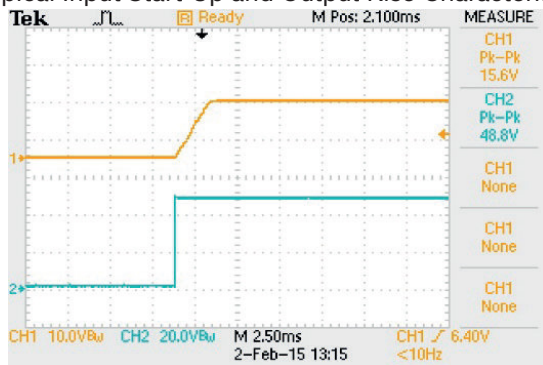
Typical Output Ripple and Noise



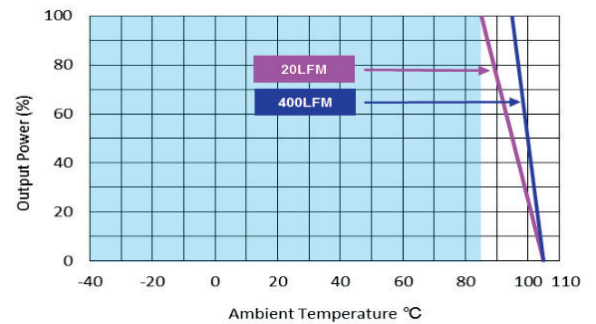
Transient Response to Dynamic Load Change (25%)



Typical Input Start-Up and Output Rise Characteristic

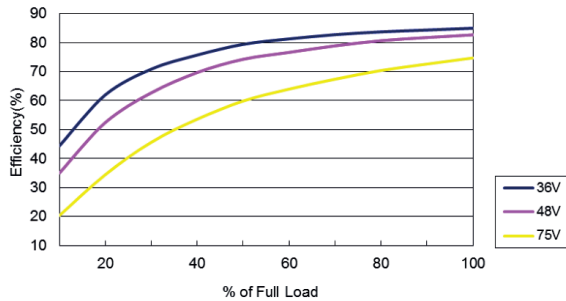


Derating Output Load versus Ambient Temperature

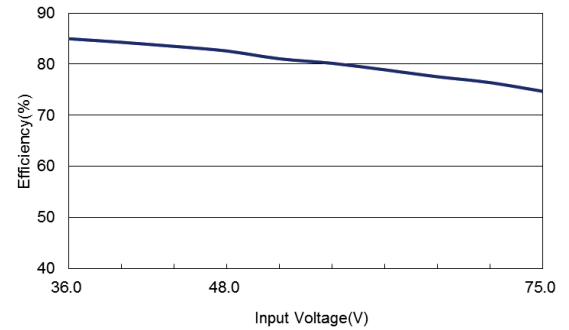


TMR 1-4813

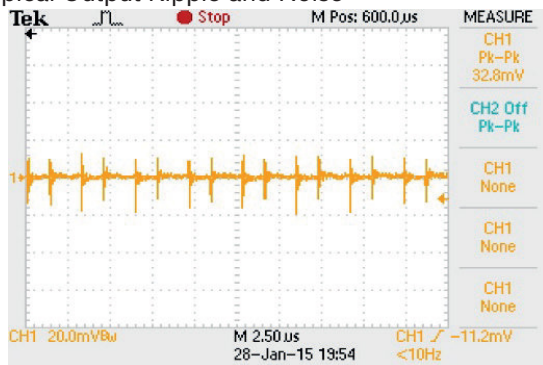
Efficiency vs Output Load



Efficiency vs Input Voltage



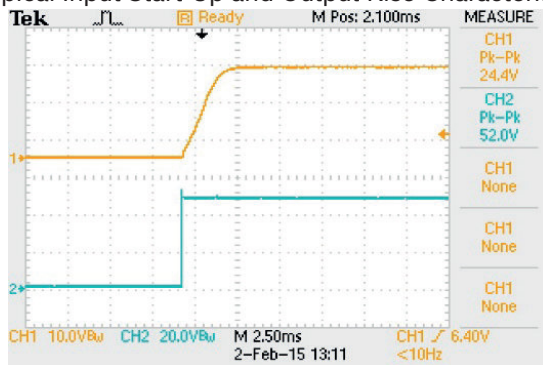
Typical Output Ripple and Noise



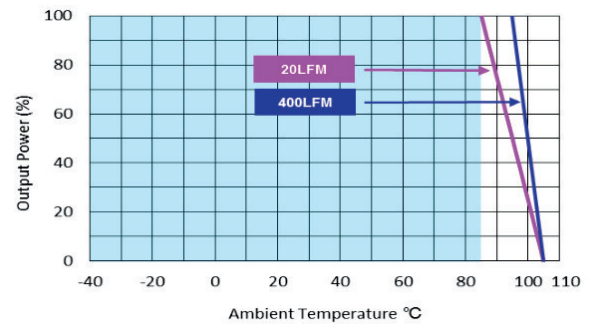
Transient Response to Dynamic Load Change (25%)



Typical Input Start-Up and Output Rise Characteristic

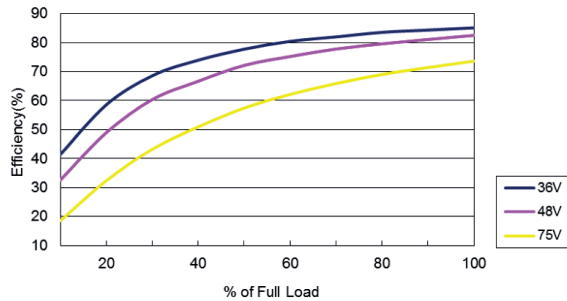


Derating Output Load versus Ambient Temperature

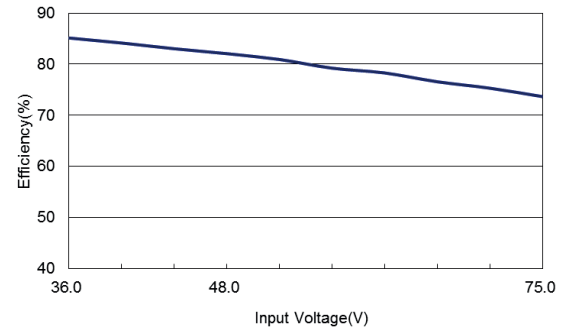


TMR 1-4815

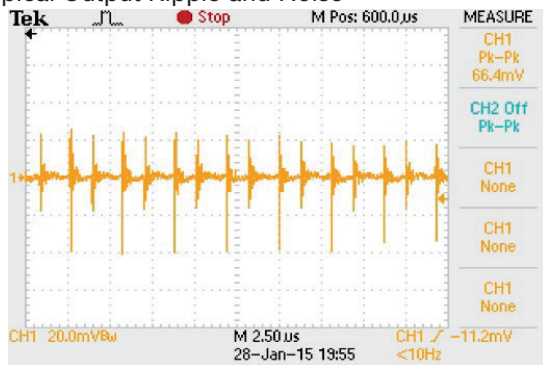
Efficiency vs Output Load



Efficiency vs Input Voltage



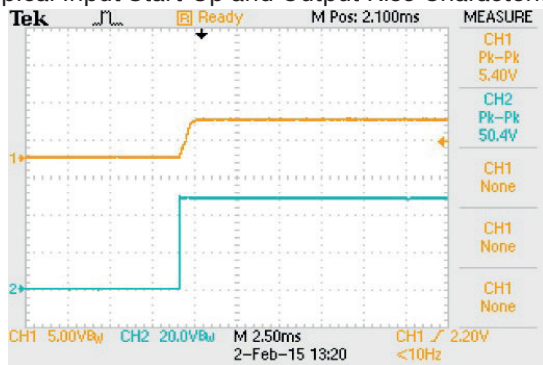
Typical Output Ripple and Noise



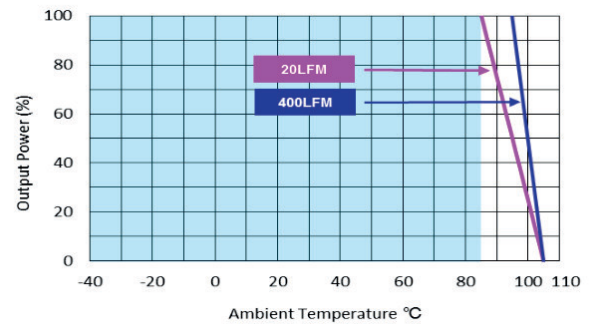
Transient Response to Dynamic Load Change (25%)



Typical Input Start-Up and Output Rise Characteristic

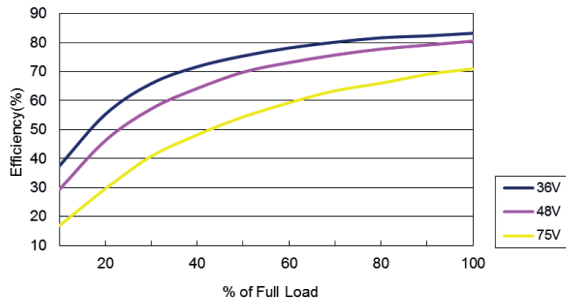


Derating Output Load versus Ambient Temperature

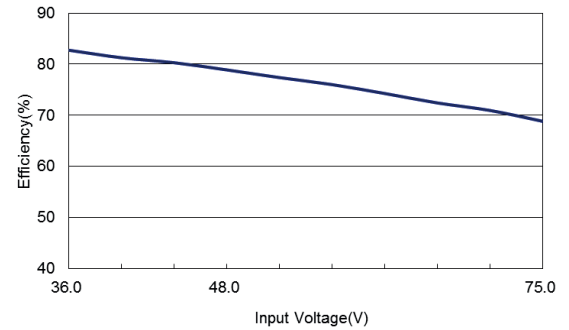


TMR 1-4822

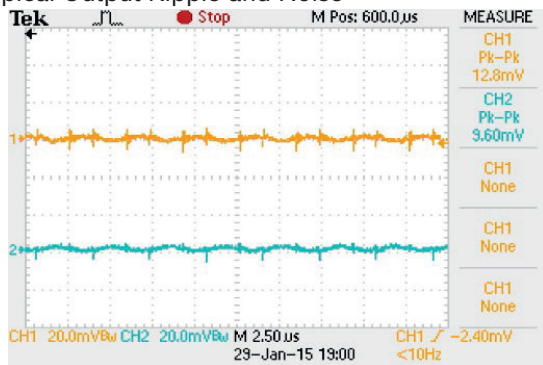
Efficiency vs Output Load



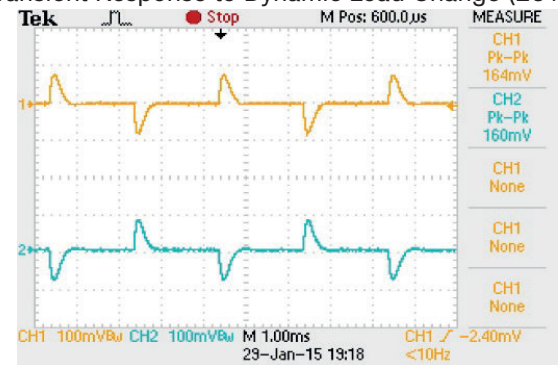
Efficiency vs Input Voltage



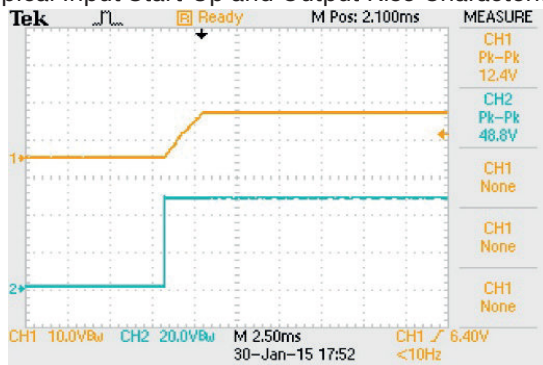
Typical Output Ripple and Noise



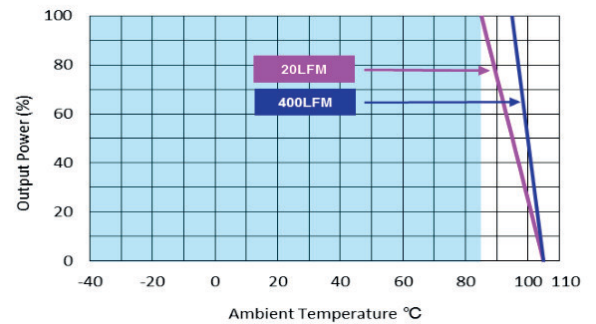
Transient Response to Dynamic Load Change (25%)



Typical Input Start-Up and Output Rise Characteristic

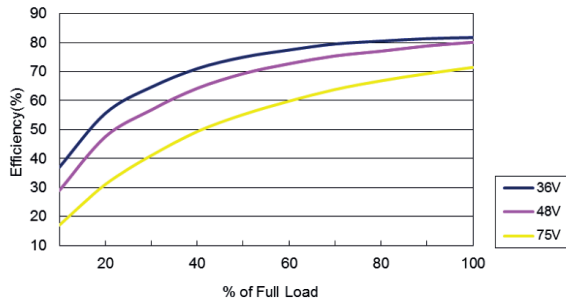


Derating Output Load versus Ambient Temperature

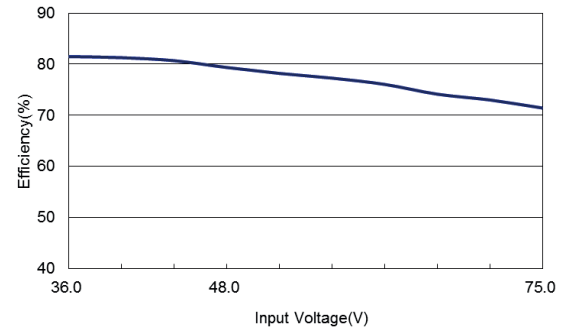


TMR 1-4823

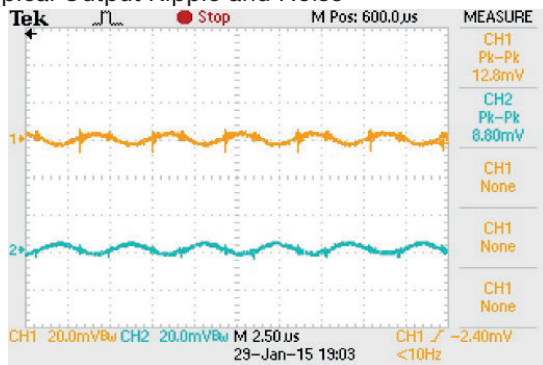
Efficiency vs Output Load



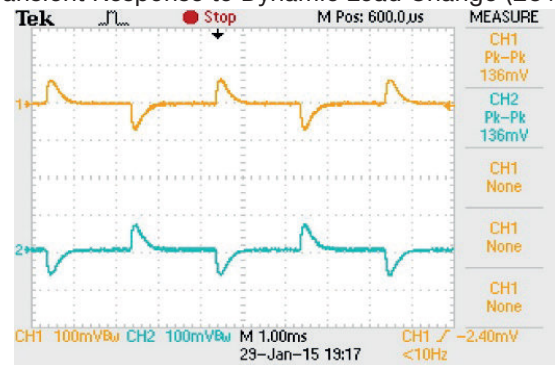
Efficiency vs Input Voltage



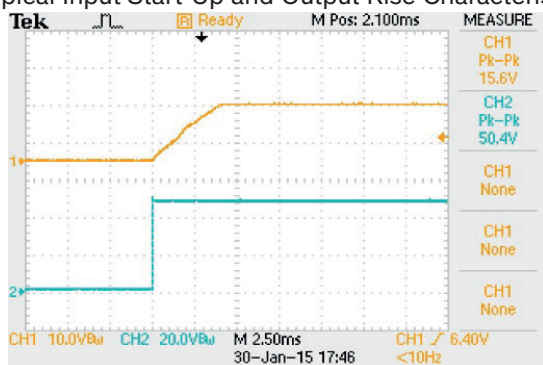
Typical Output Ripple and Noise



Transient Response to Dynamic Load Change (25%)



Typical Input Start-Up and Output Rise Characteristic



Derating Output Load versus Ambient Temperature

