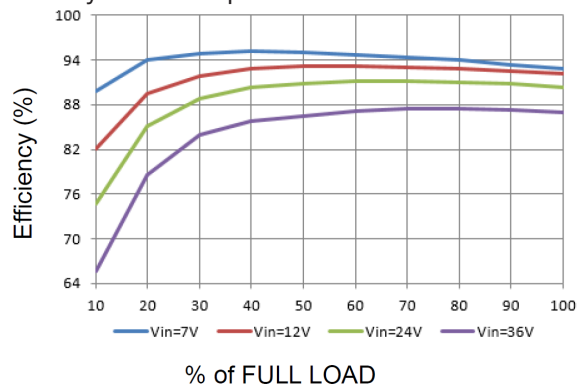


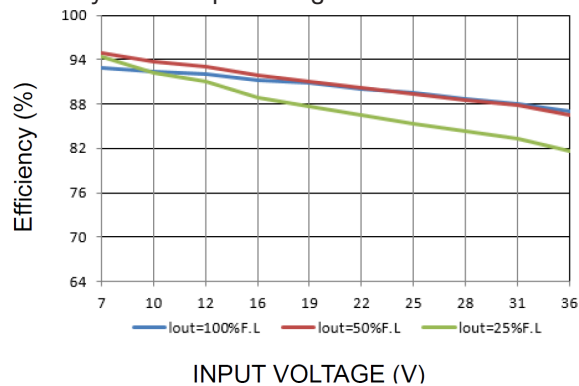
### Characteristic Curves

#### TSR 1.5-2433E

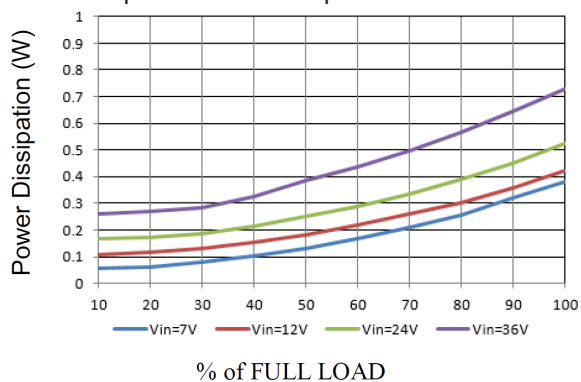
Efficiency versus Output Load



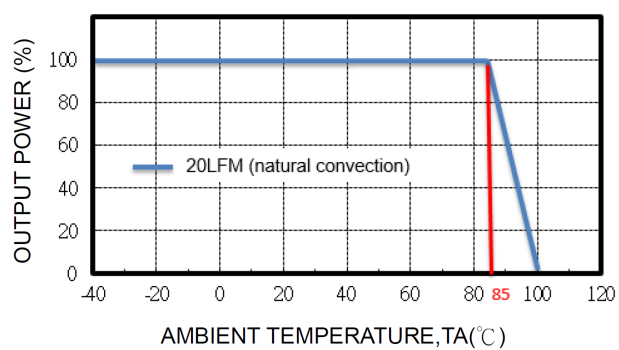
Efficiency versus Input Voltage



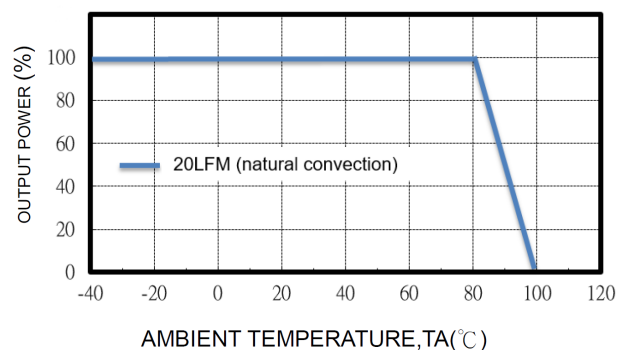
Power Dissipation versus Output Load



Derating Output Load vs Ambient Temperature  
(Input Voltage <29V)

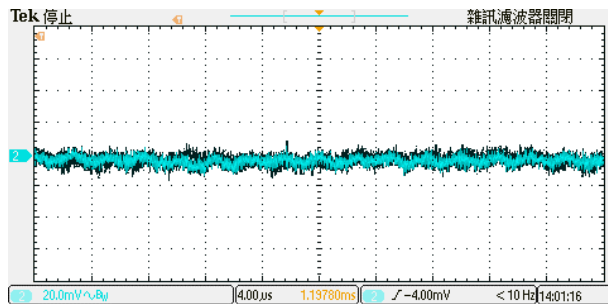


Derating Output Load vs Ambient Temperature  
(Input Voltage ≥29V)

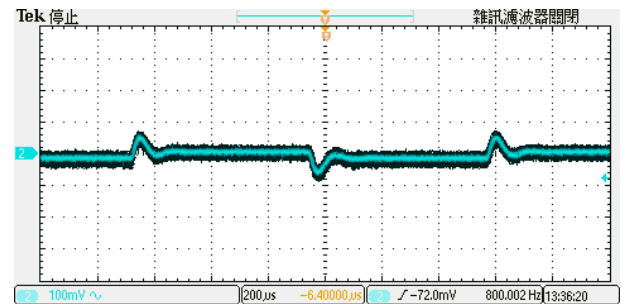


### TSR 1.5-2433E

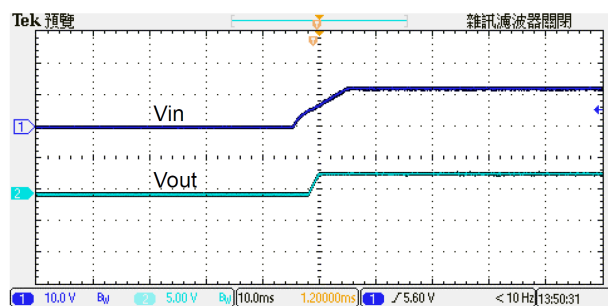
Typical Output Ripple and Noise  
(with external capacitor; see datasheet)



Transient Response to Dynamic Load Change (50%)

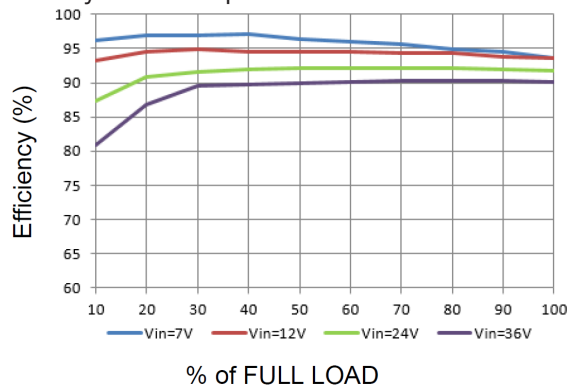


Typical Start-Up and Output Rise Characteristic

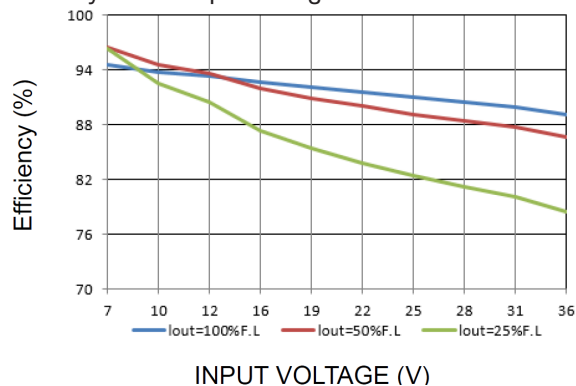


### TSR 1.5-2450E

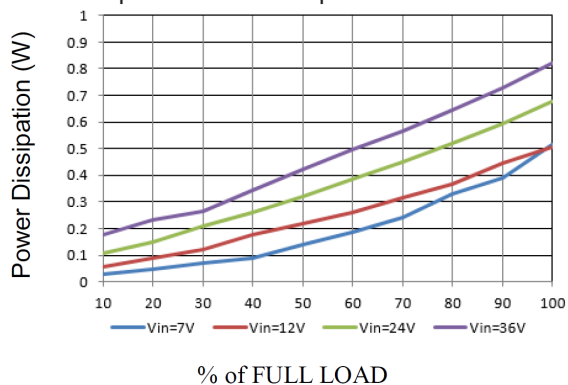
Efficiency versus Output Load



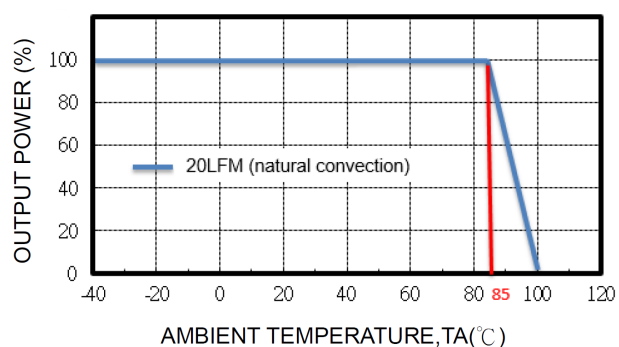
Efficiency versus Input Voltage



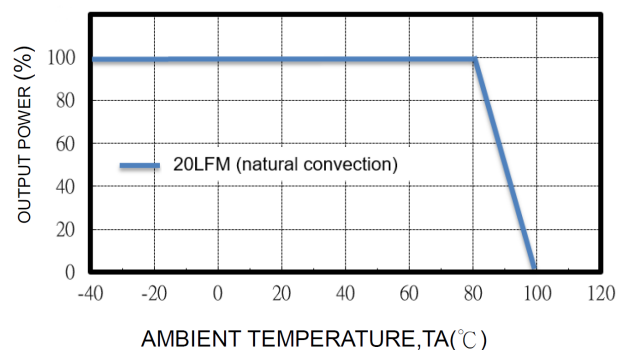
Power Dissipation versus Output Load



Derating Output Load vs Ambient Temperature  
(Input Voltage <29V)

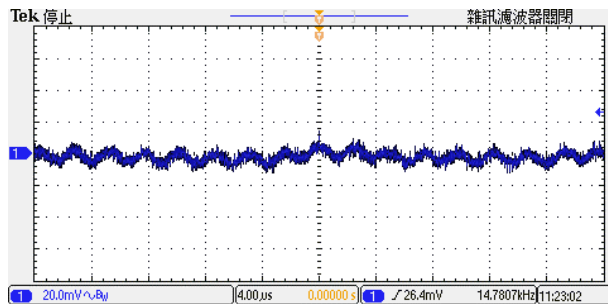


Derating Output Load vs Ambient Temperature  
(Input Voltage >=29V)

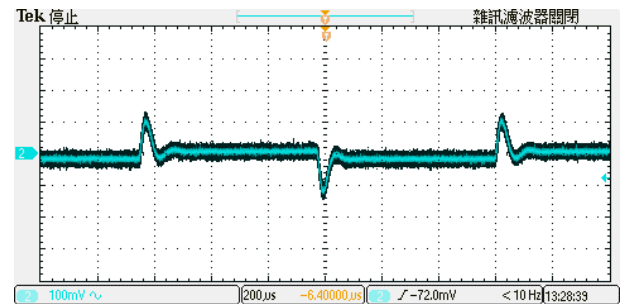


### TSR 1.5-2450E

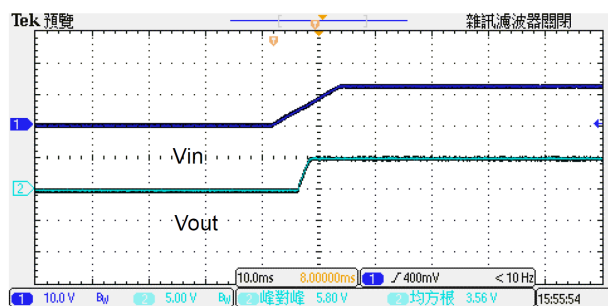
Typical Output Ripple and Noise  
(with external capacitor; see datasheet)



Transient Response to Dynamic Load Change (50%)

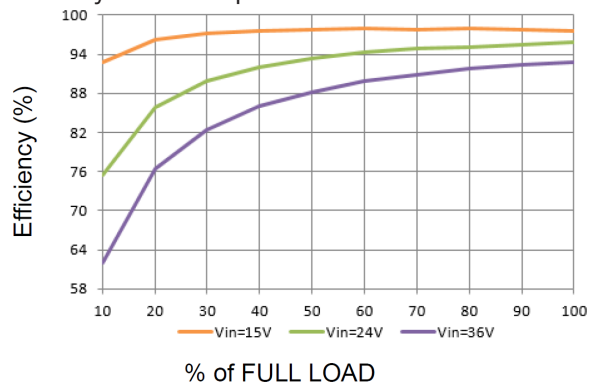


Typical Start-Up and Output Rise Characteristic

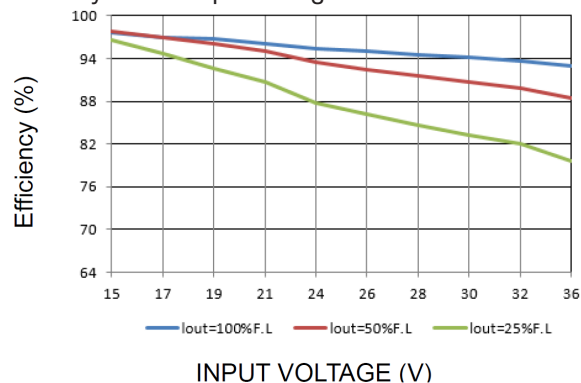


### TSR 1.5-24120E

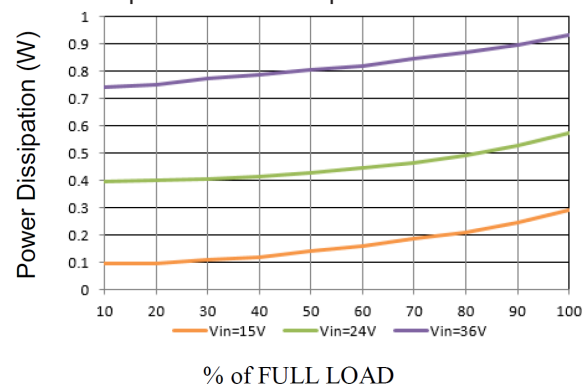
Efficiency versus Output Load



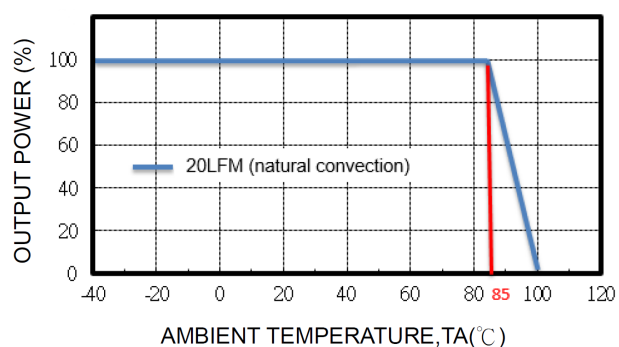
Efficiency versus Input Voltage



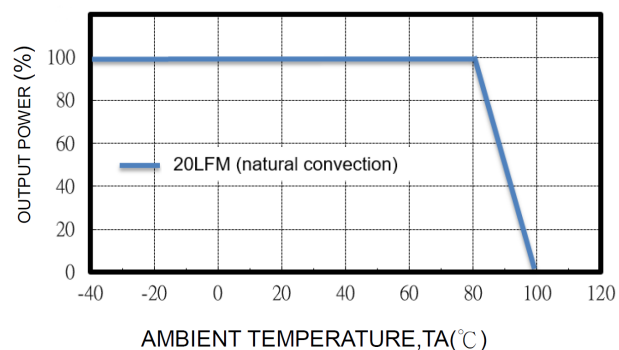
Power Dissipation versus Output Load



Derating Output Load vs Ambient Temperature  
(Input Voltage <29V)

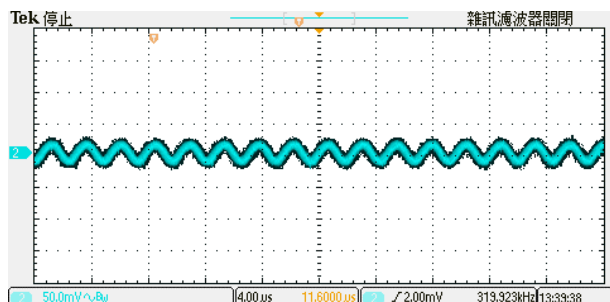


Derating Output Load vs Ambient Temperature  
(Input Voltage ≥29V)

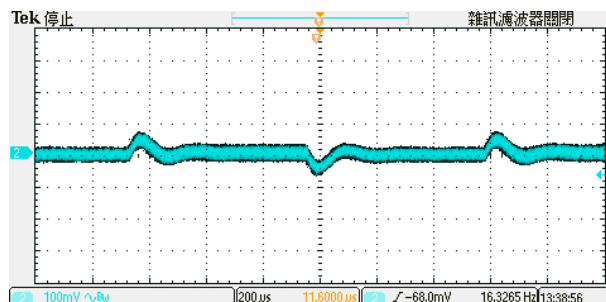


### TSR 1.5-24120E

Typical Output Ripple and Noise  
(with external capacitor; see datasheet)



Transient Response to Dynamic Load Change (50%)



Typical Start-Up and Output Rise Characteristic

