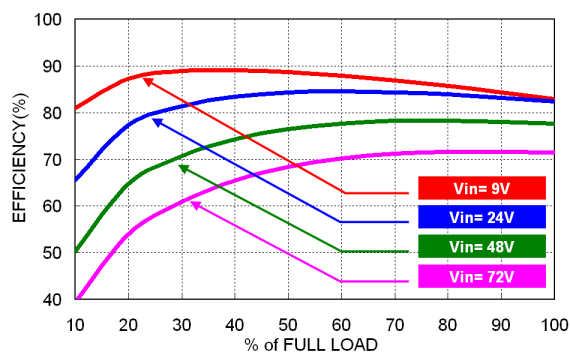


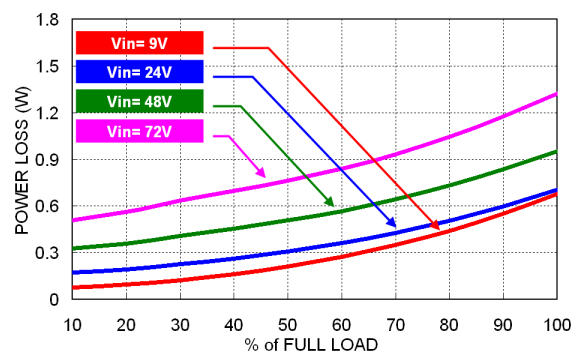
Characteristic Curves

TSR 1-4833WI

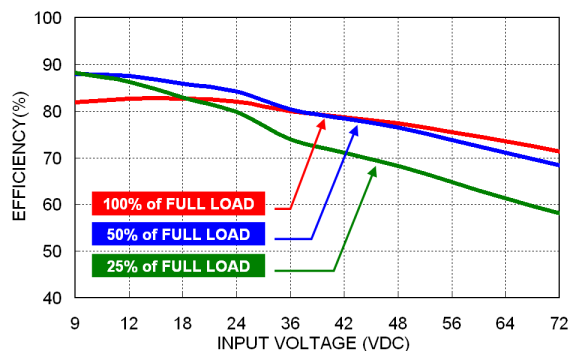
Efficiency versus Output Load



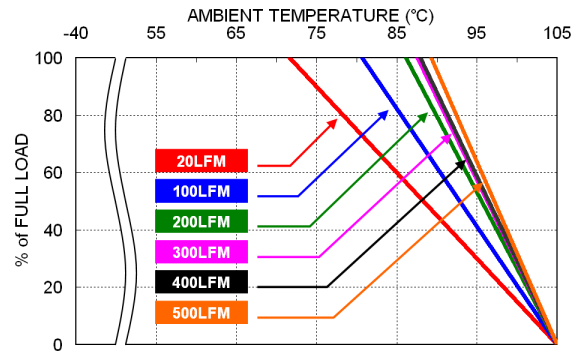
Power Dissipation versus Output Load



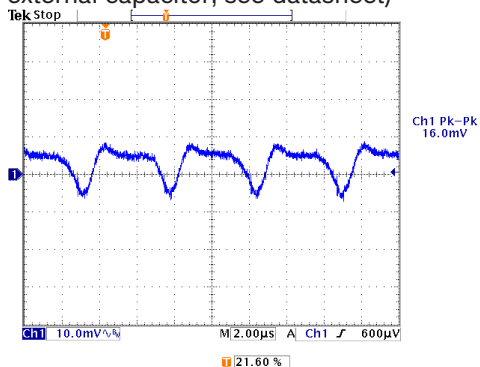
Efficiency versus Input Voltage



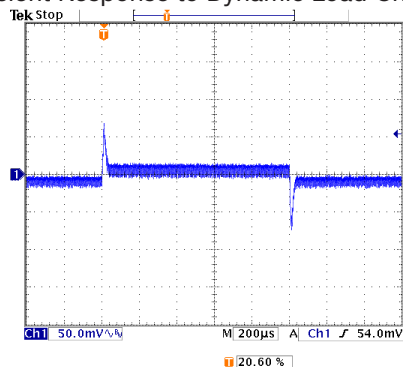
Derating Output Load versus Ambient Temperature



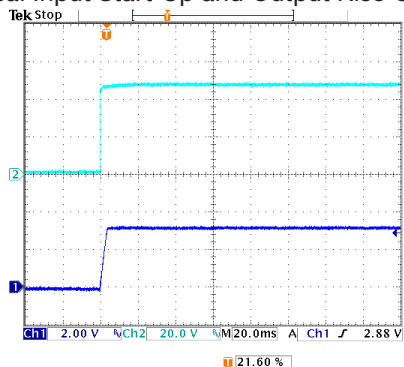
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



Transient Response to Dynamic Load Change (25%)

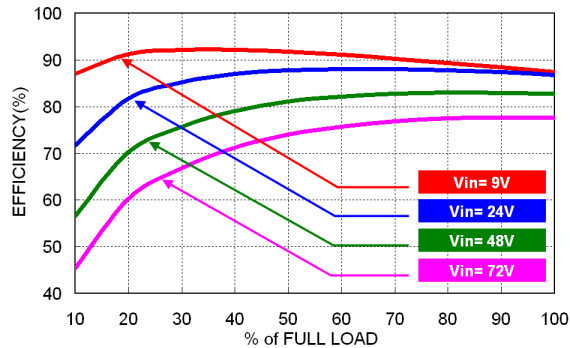


Typical Input Start-Up and Output Rise Characteristic

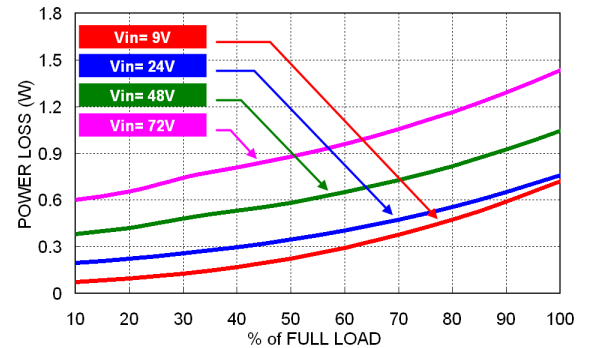


TSR 1-4850WI

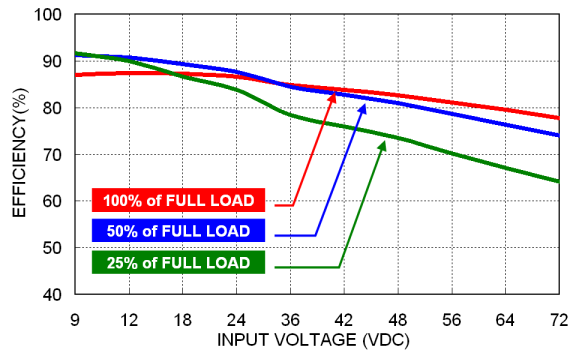
Efficiency versus Output Load



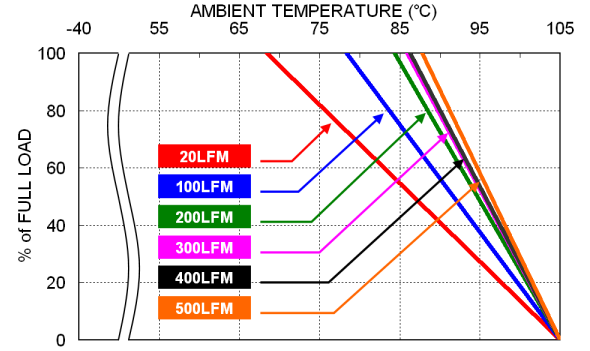
Power Dissipation versus Output Load



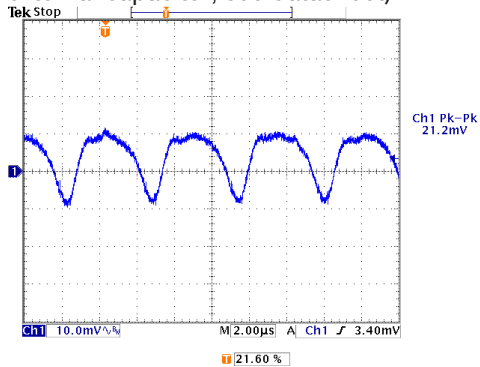
Efficiency versus Input Voltage



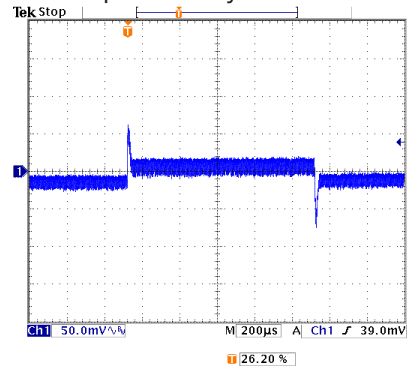
Derating Output Load versus Ambient Temperature



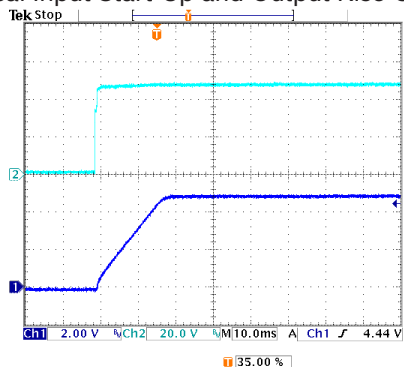
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



Transient Response to Dynamic Load Change (25%)

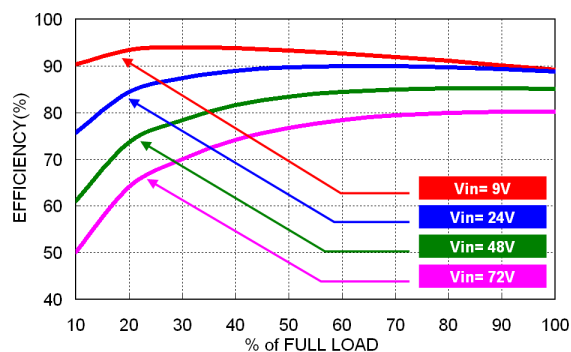


Typical Input Start-Up and Output Rise Characteristic

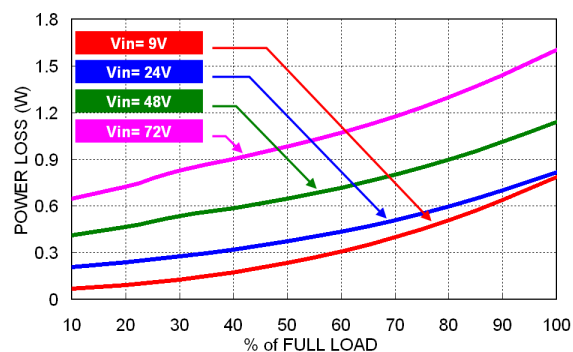


TSR 1-4865WI

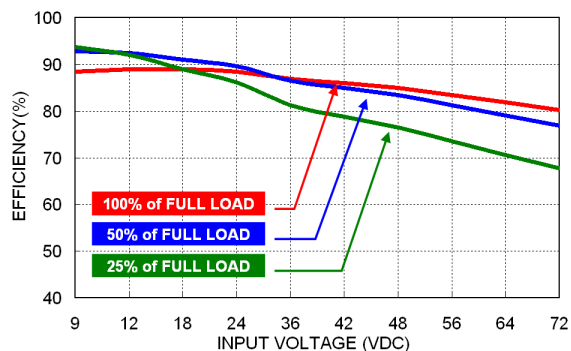
Efficiency versus Output Load



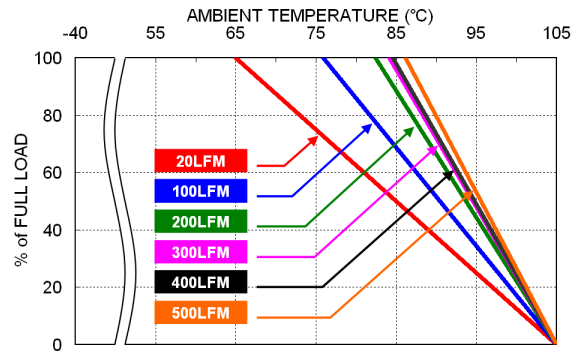
Power Dissipation versus Output Load



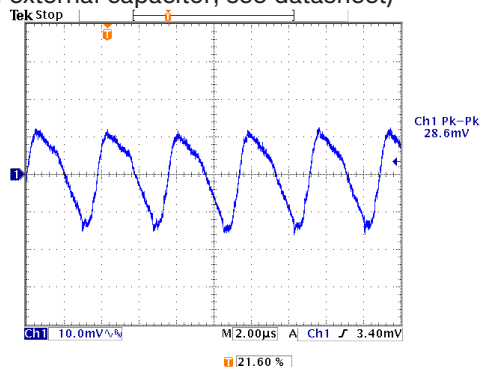
Efficiency versus Input Voltage



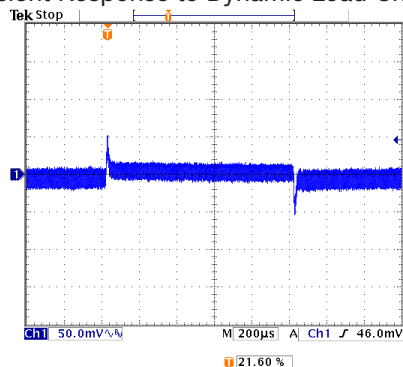
Derating Output Load versus Ambient Temperature



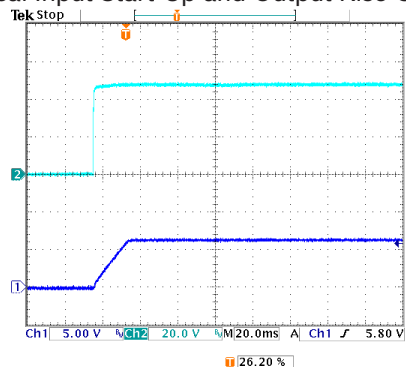
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



Transient Response to Dynamic Load Change (25%)

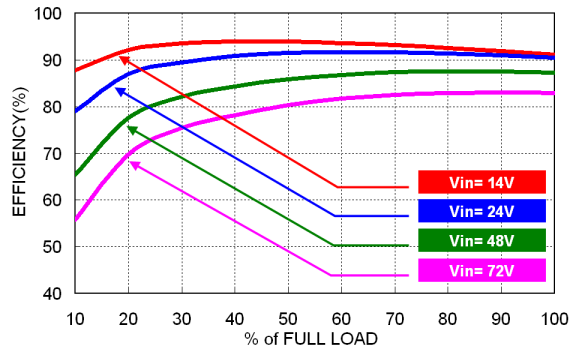


Typical Input Start-Up and Output Rise Characteristic

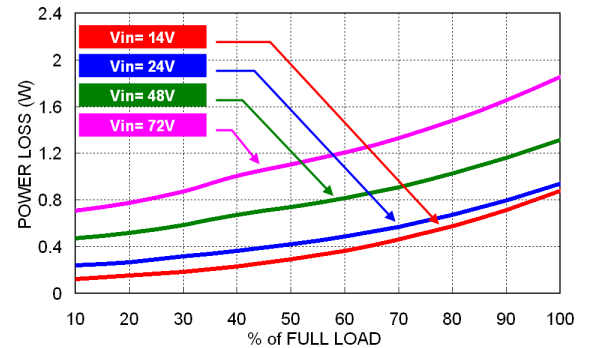


TSR 1-4890WI

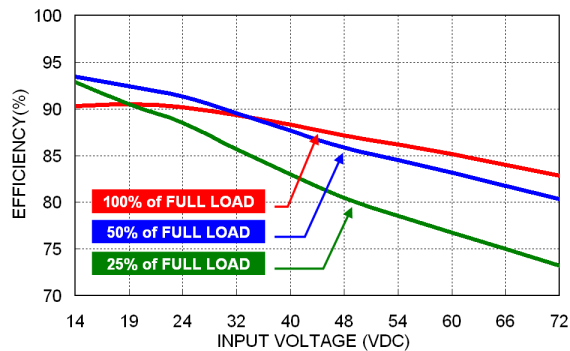
Efficiency versus Output Load



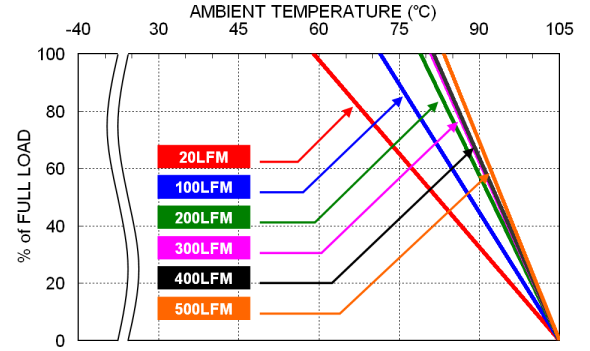
Power Dissipation versus Output Load



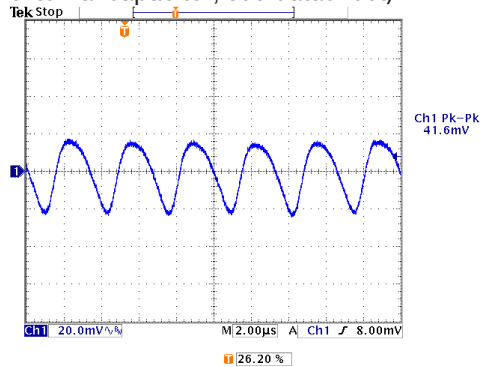
Efficiency versus Input Voltage



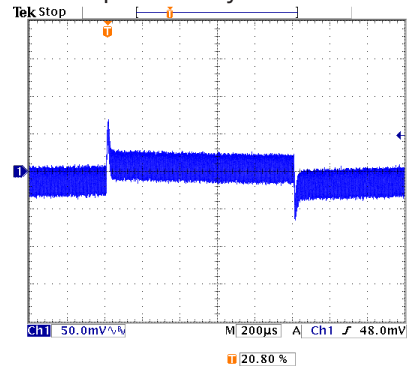
Derating Output Load versus Ambient Temperature



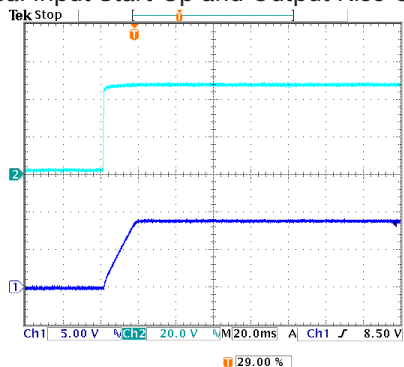
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



Transient Response to Dynamic Load Change (25%)

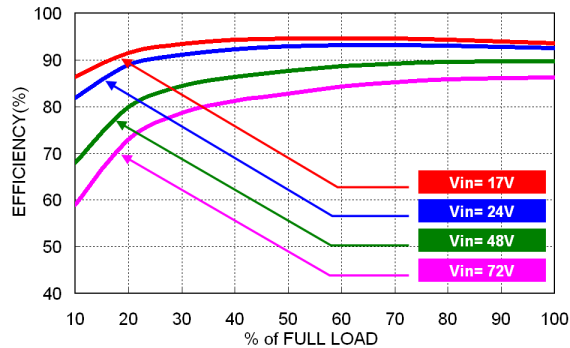


Typical Input Start-Up and Output Rise Characteristic

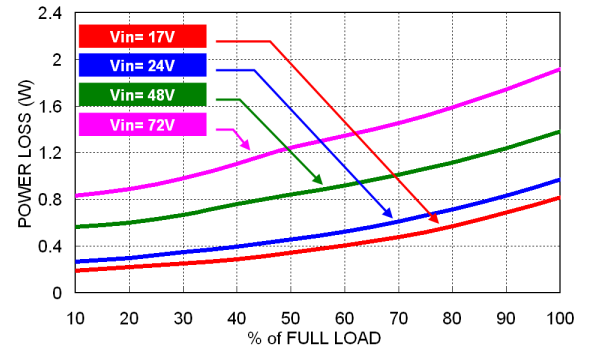


TSR 1-48120WI

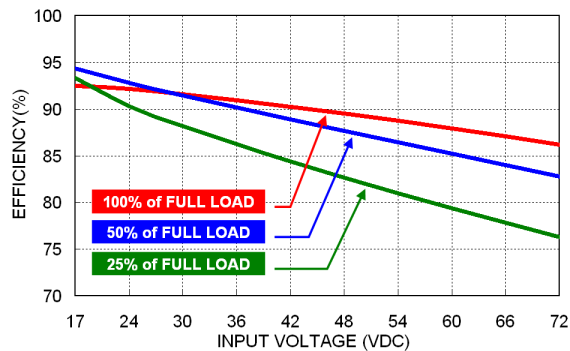
Efficiency versus Output Load



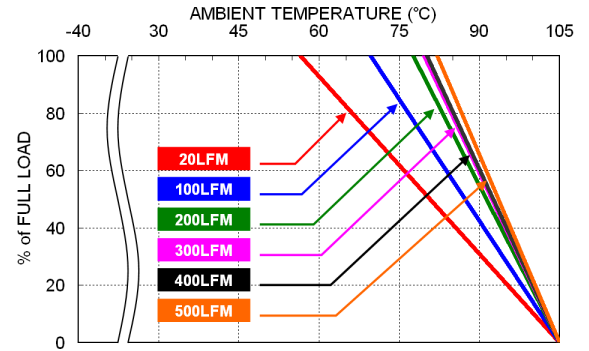
Power Dissipation versus Output Load



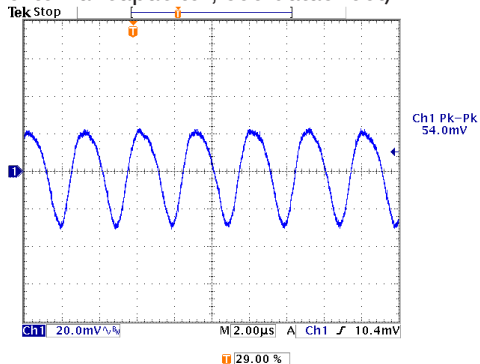
Efficiency versus Input Voltage



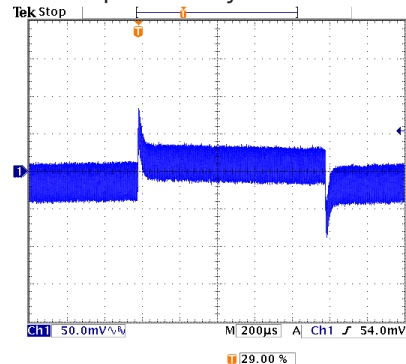
Derating Output Load versus Ambient Temperature



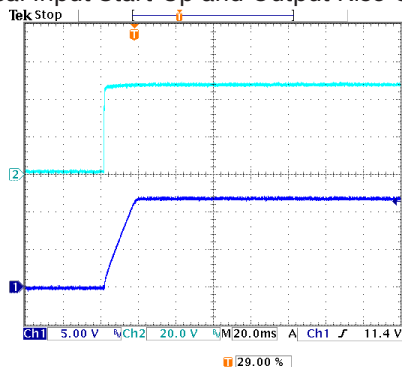
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



Transient Response to Dynamic Load Change (25%)

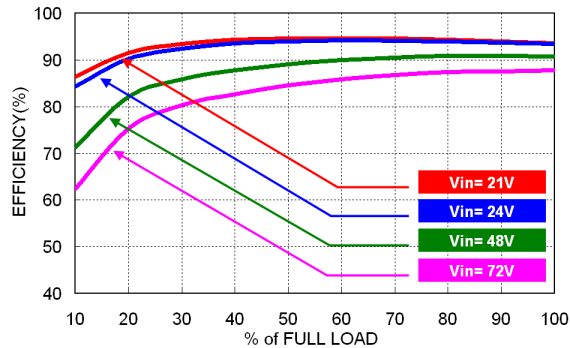


Typical Input Start-Up and Output Rise Characteristic

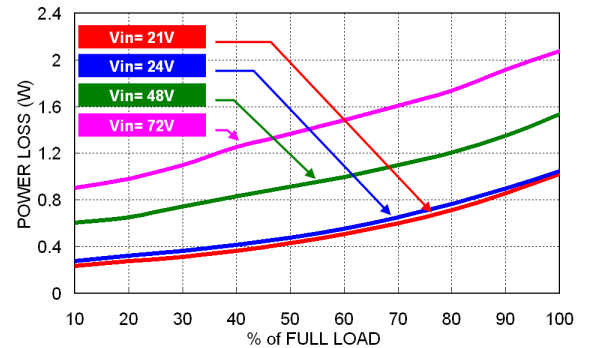


TSR 1-48150WI

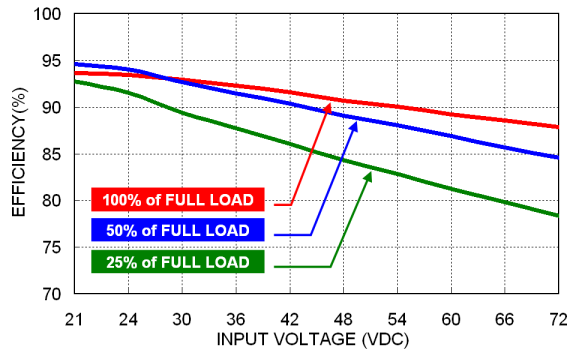
Efficiency versus Output Load



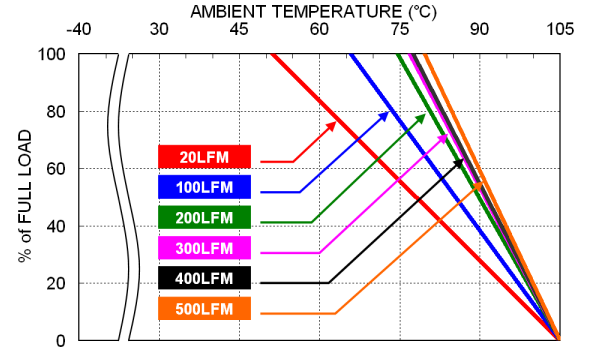
Power Dissipation versus Output Load



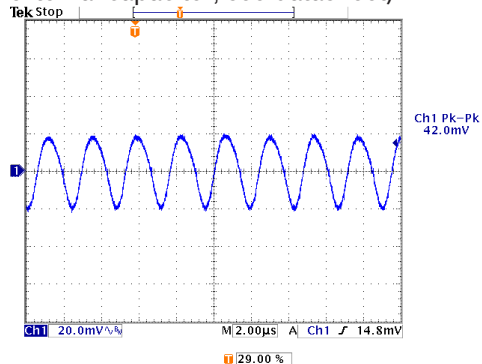
Efficiency versus Input Voltage



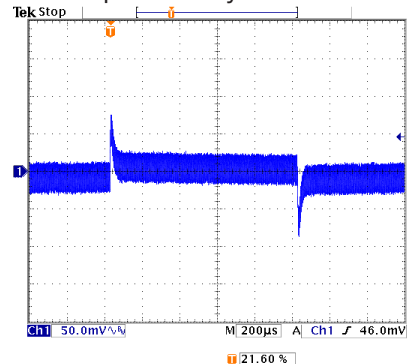
Derating Output Load versus Ambient Temperature



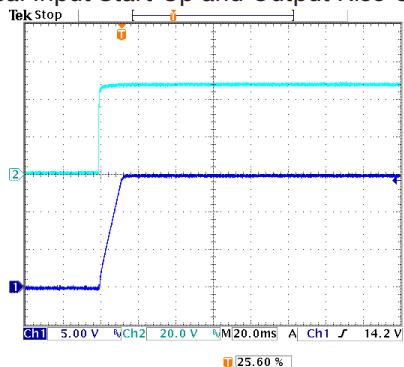
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



Transient Response to Dynamic Load Change (25%)

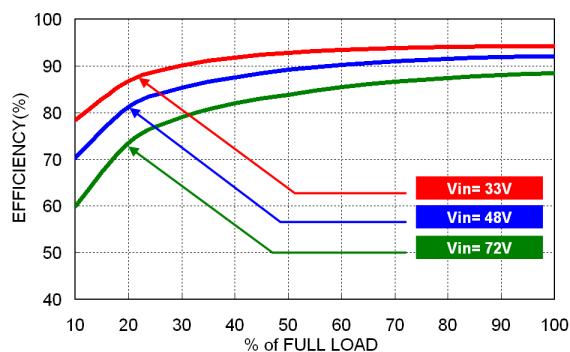


Typical Input Start-Up and Output Rise Characteristic

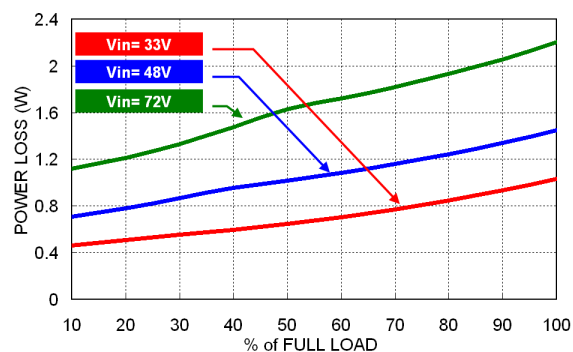


TSR 1-48240WI

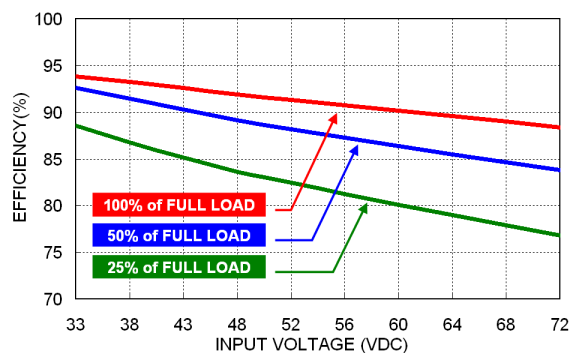
Efficiency versus Output Load



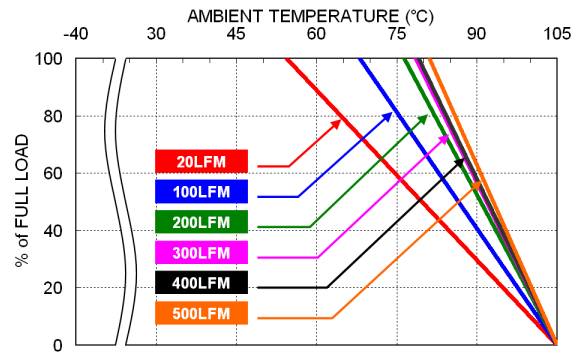
Power Dissipation versus Output Load



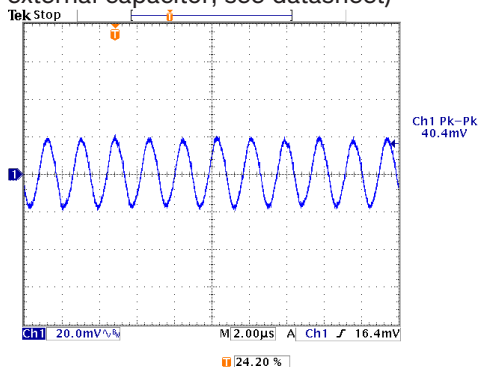
Efficiency versus Input Voltage



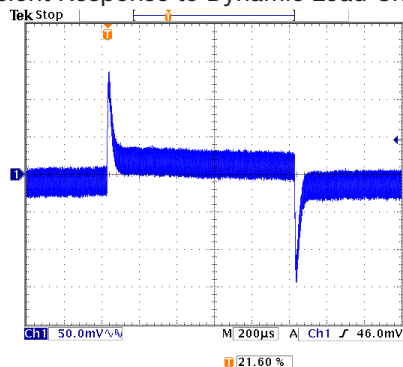
Derating Output Load versus Ambient Temperature



Typical Output Ripple and Noise
(with external capacitor; see datasheet)



Transient Response to Dynamic Load Change (25%)



Typical Input Start-Up and Output Rise Characteristic

