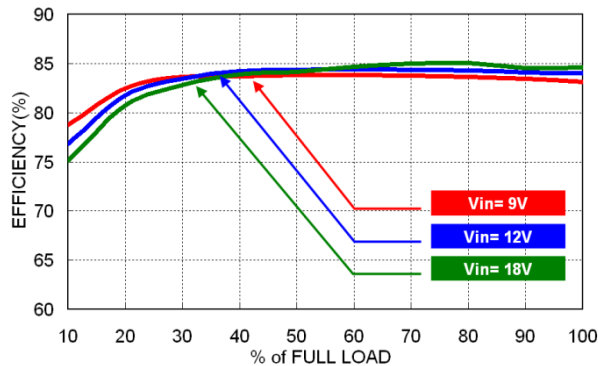


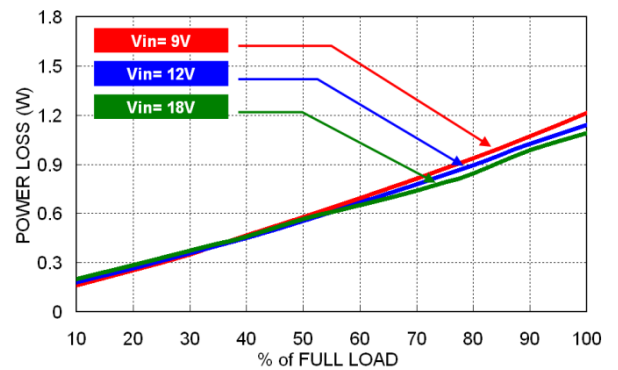
Characteristic Curves

TIM 6-1211

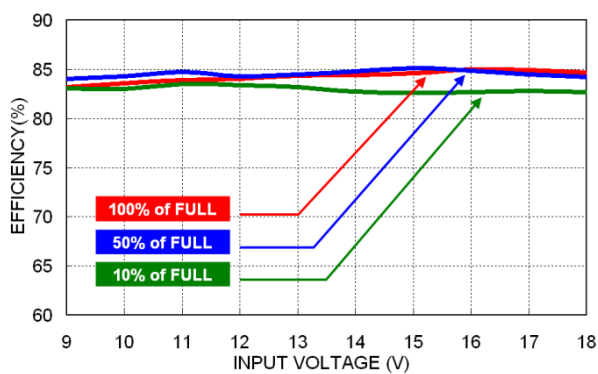
Efficiency versus Output Load



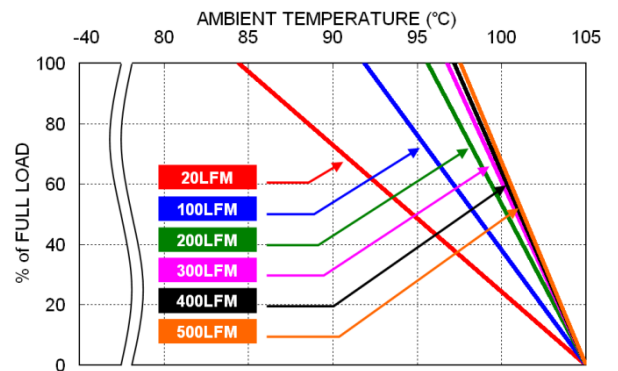
Power Dissipation versus Output Load



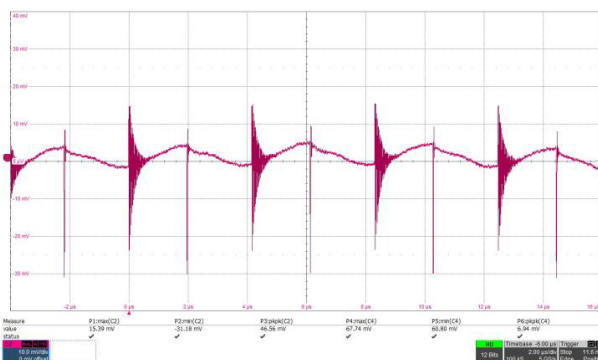
Efficiency versus Input Voltage



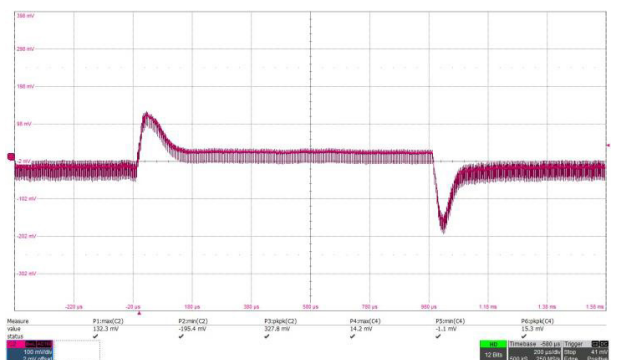
Derating Output Load versus Ambient Temperature



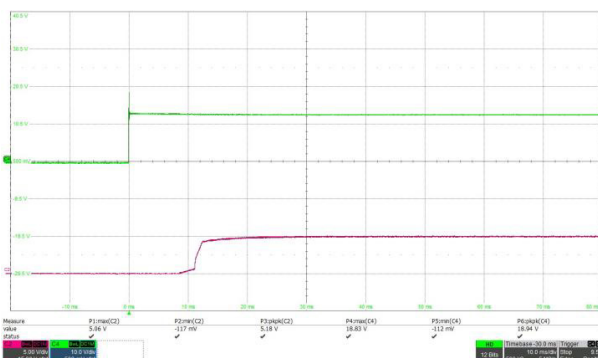
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



Transient Response to Dynamic Load Change (25%)

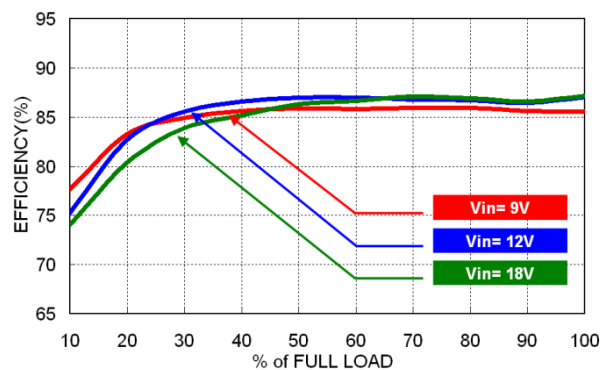


Typical Start-Up and Output Rise Characteristic

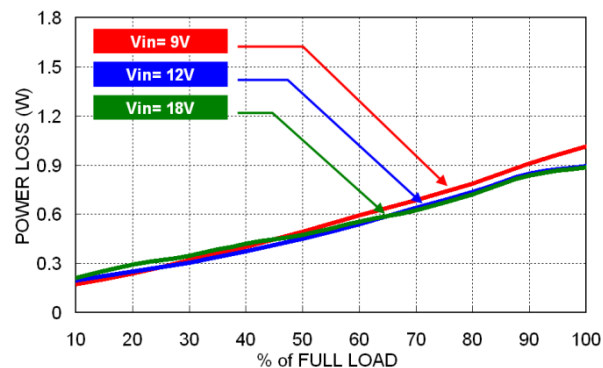


TIM 6-1212

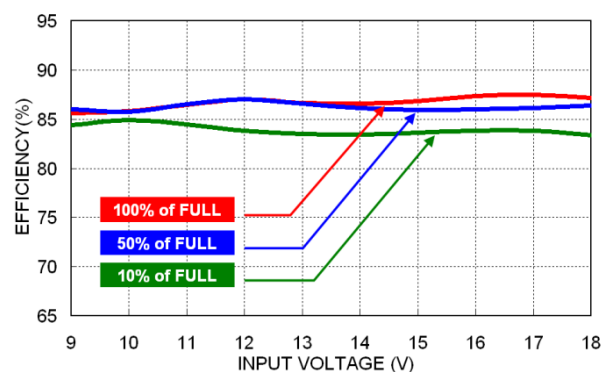
Efficiency versus Output Load



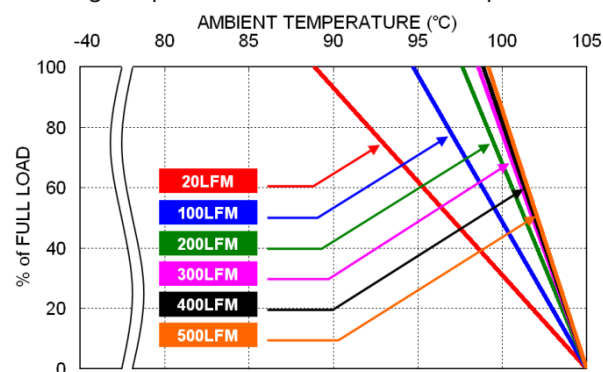
Power Dissipation versus Output Load



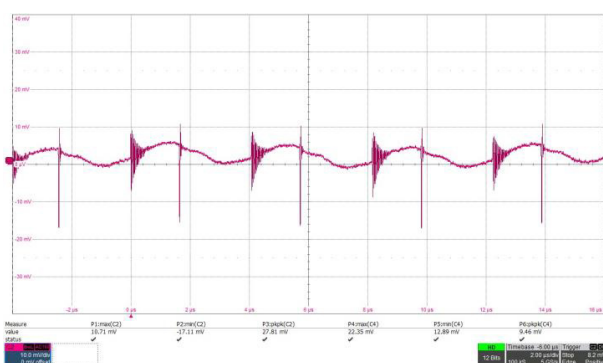
Efficiency versus Input Voltage



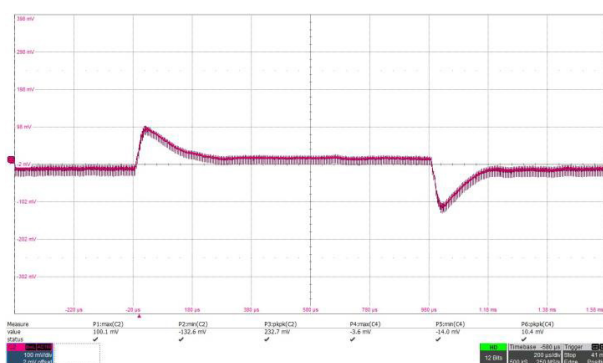
Derating Output Load versus Ambient Temperature



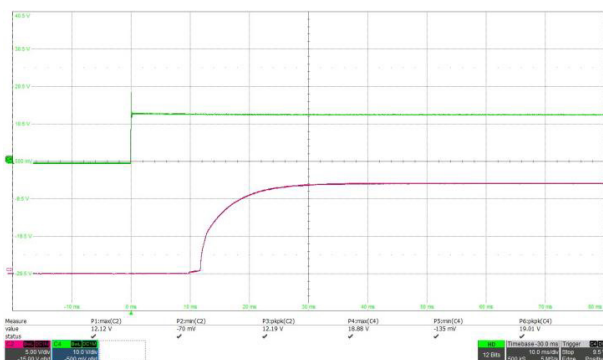
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



Transient Response to Dynamic Load Change (25%)

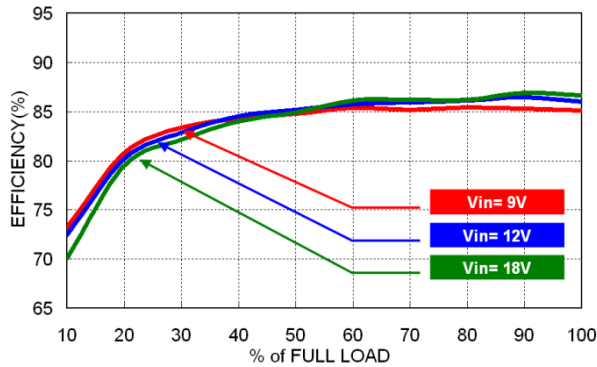


Typical Start-Up and Output Rise Characteristic

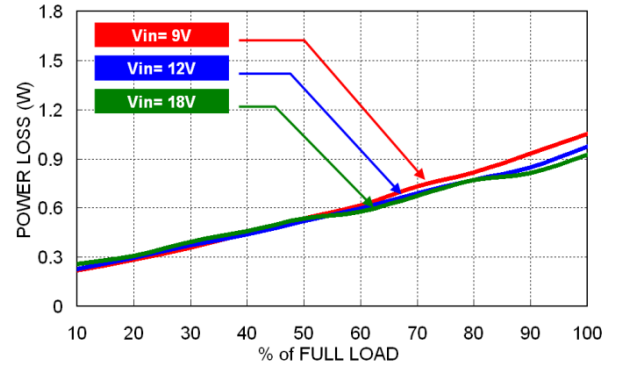


TIM 6-1213

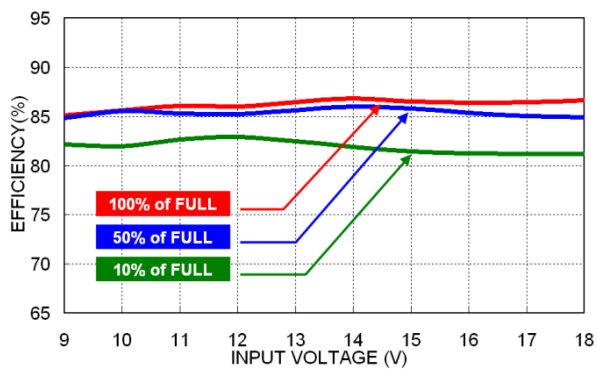
Efficiency versus Output Load



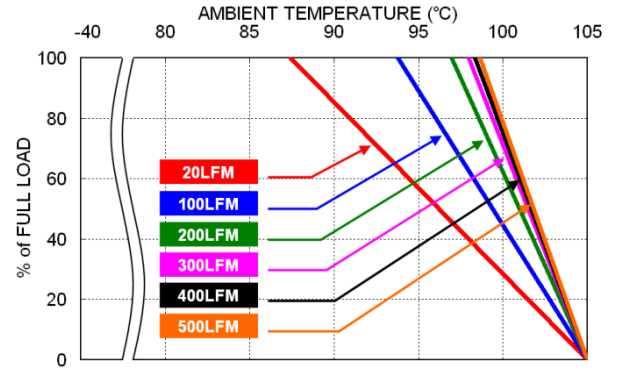
Power Dissipation versus Output Load



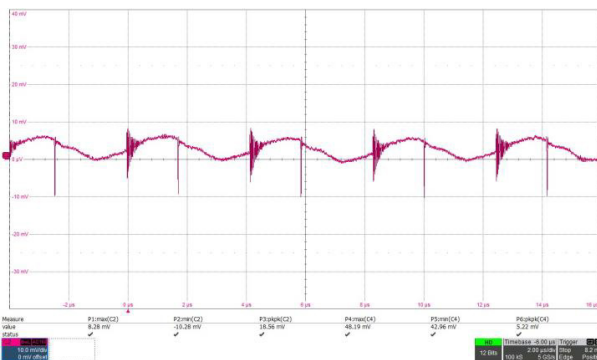
Efficiency versus Input Voltage



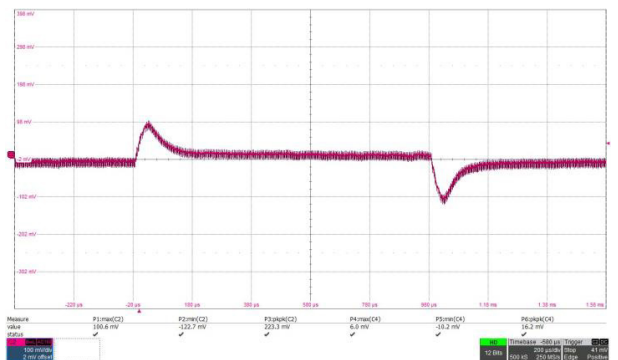
Derating Output Load versus Ambient Temperature



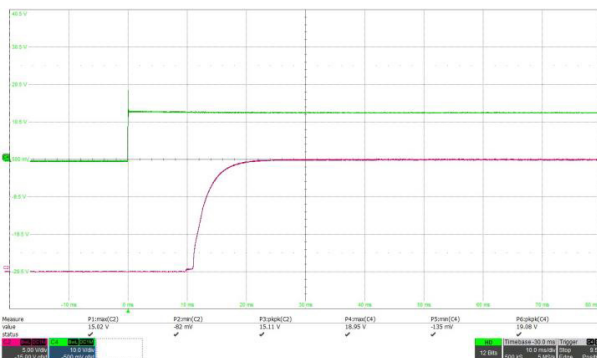
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



Transient Response to Dynamic Load Change (25%)

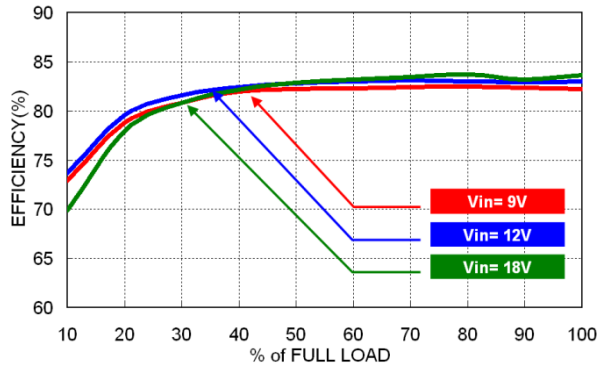


Typical Start-Up and Output Rise Characteristic

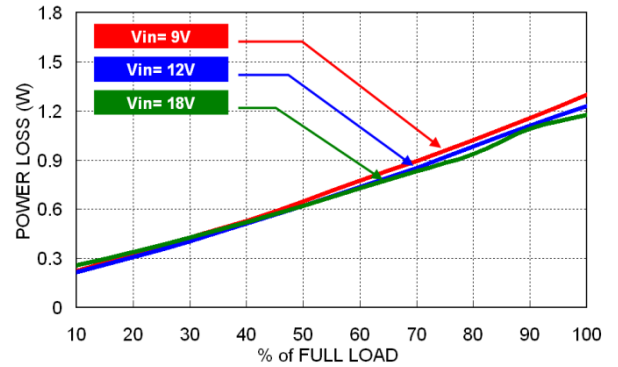


TIM 6-1221

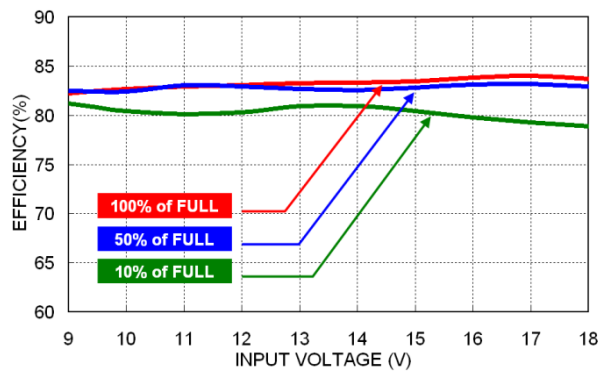
Efficiency versus Output Load



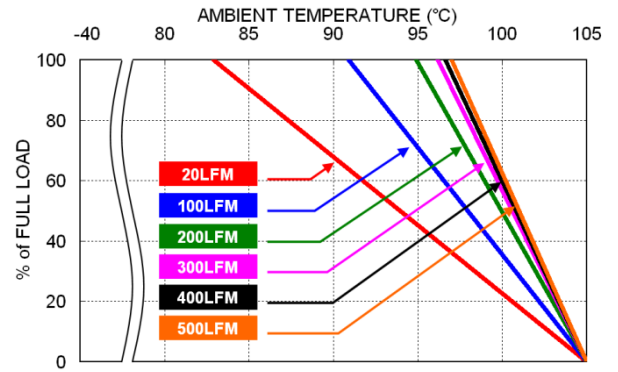
Power Dissipation versus Output Load



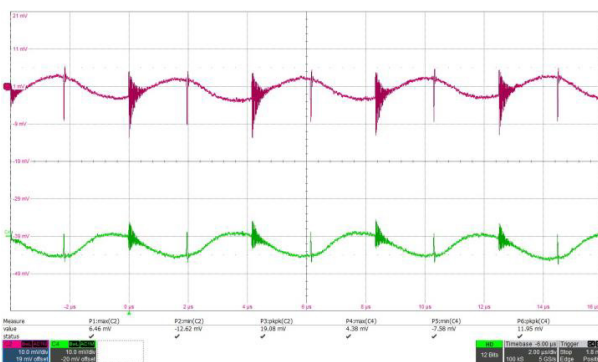
Efficiency versus Input Voltage



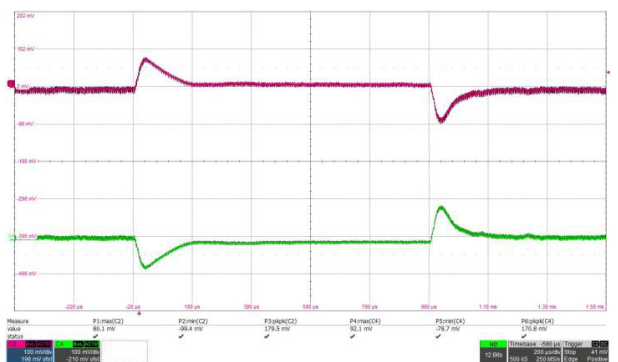
Derating Output Load versus Ambient Temperature



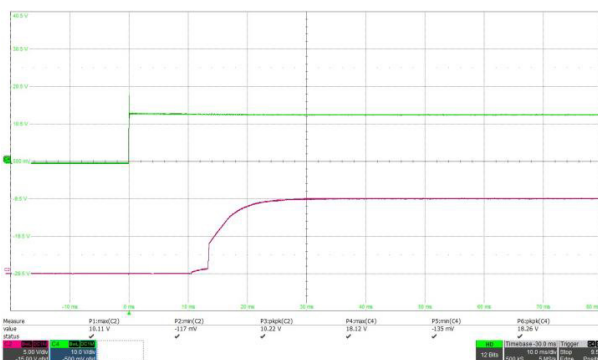
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



Transient Response to Dynamic Load Change (25%)

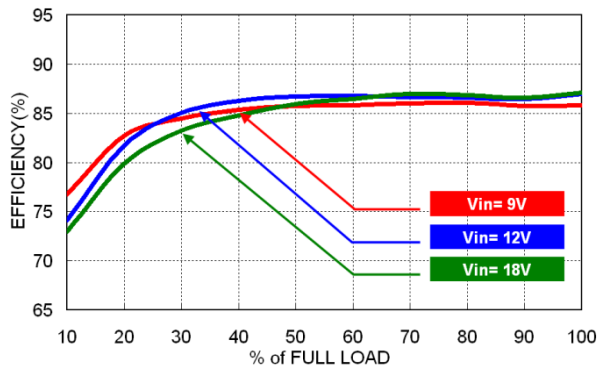


Typical Start-Up and Output Rise Characteristic

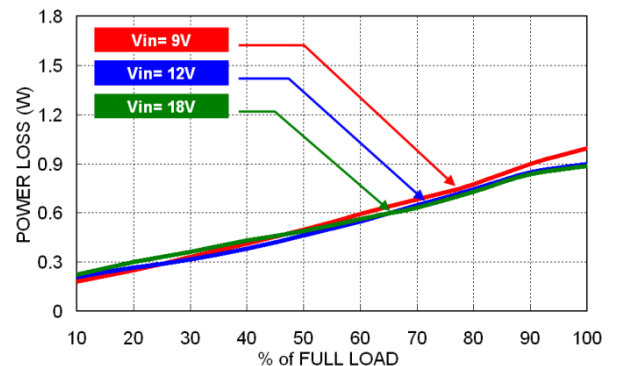


TIM 6-1222

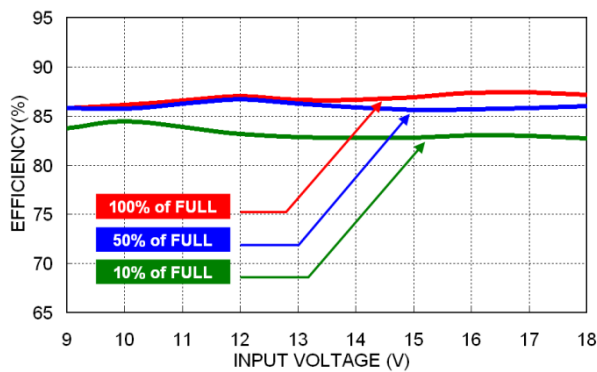
Efficiency versus Output Load



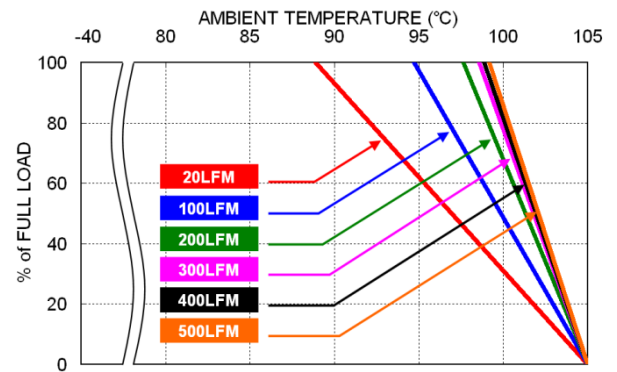
Power Dissipation versus Output Load



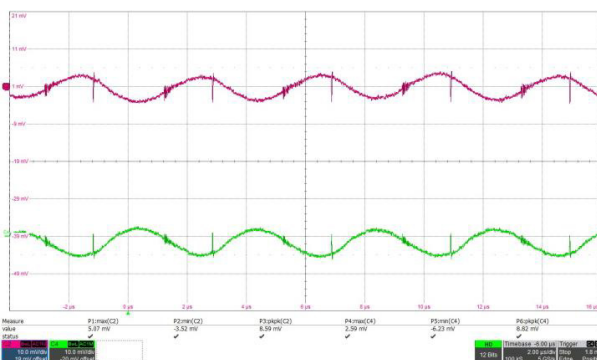
Efficiency versus Input Voltage



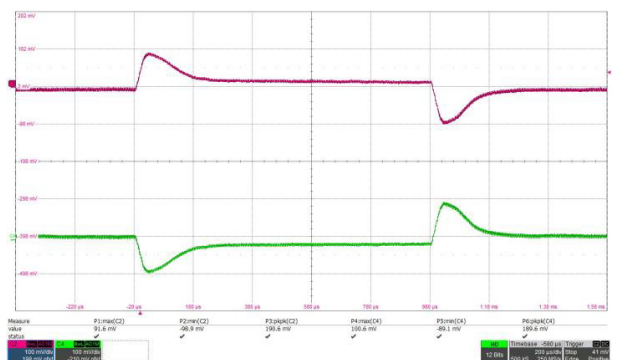
Derating Output Load versus Ambient Temperature



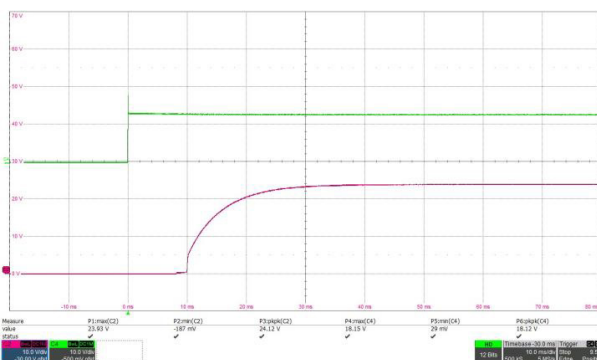
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



Transient Response to Dynamic Load Change (25%)

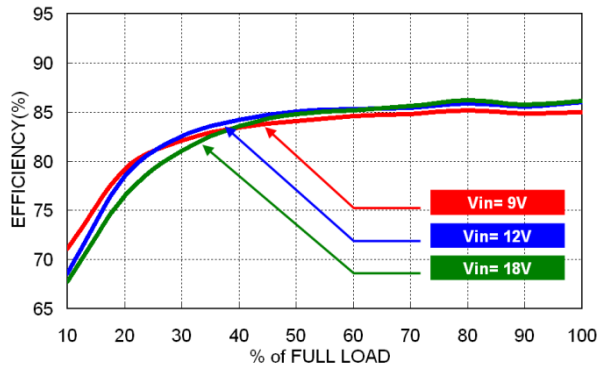


Typical Start-Up and Output Rise Characteristic

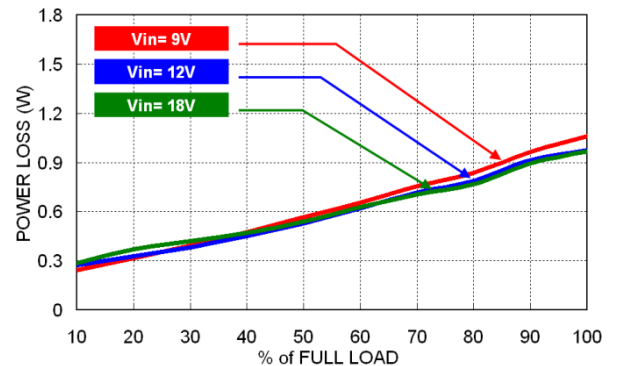


TIM 6-1223

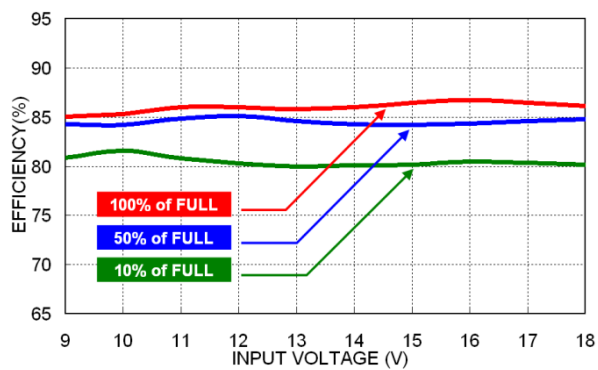
Efficiency versus Output Load



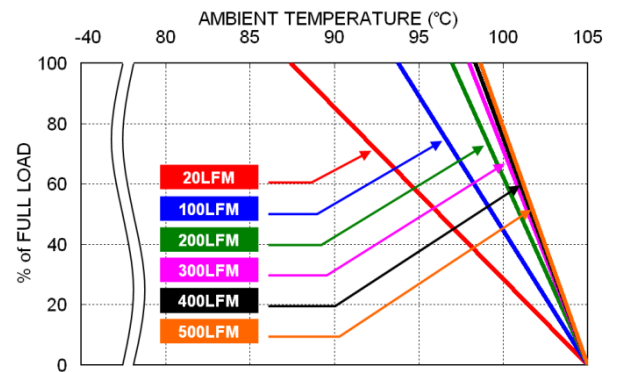
Power Dissipation versus Output Load



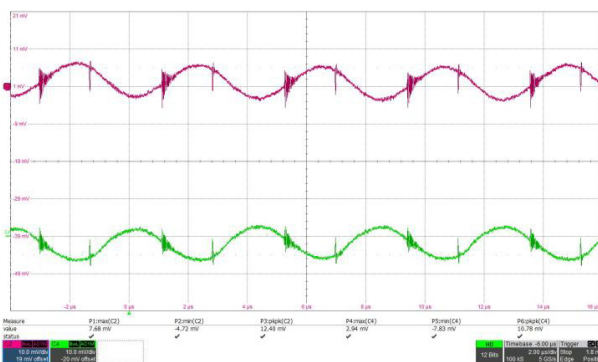
Efficiency versus Input Voltage



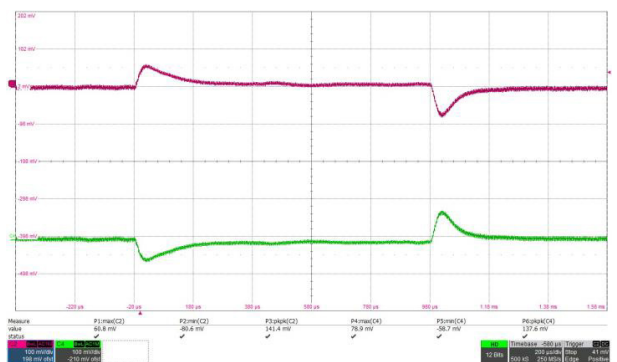
Derating Output Load versus Ambient Temperature



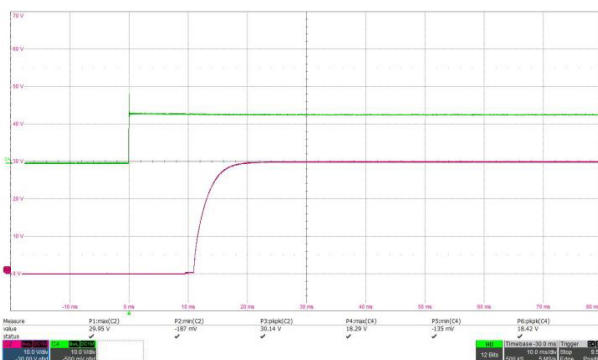
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



Transient Response to Dynamic Load Change (25%)

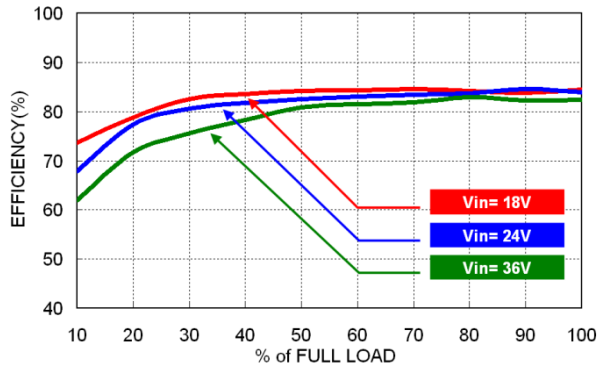


Typical Start-Up and Output Rise Characteristic

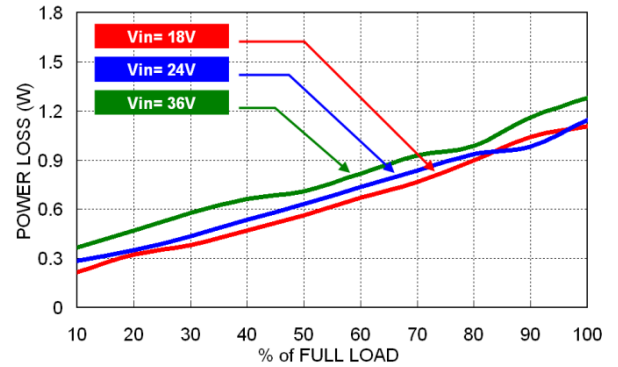


TIM 6-2411

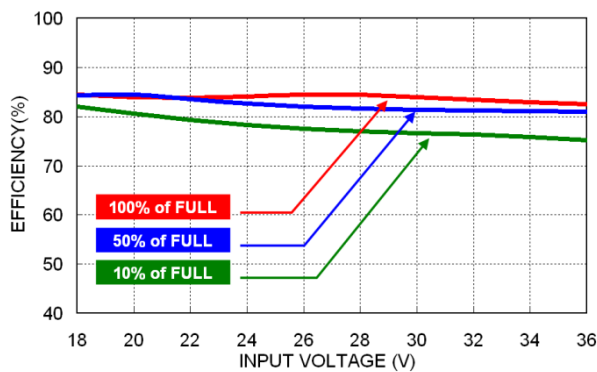
Efficiency versus Output Load



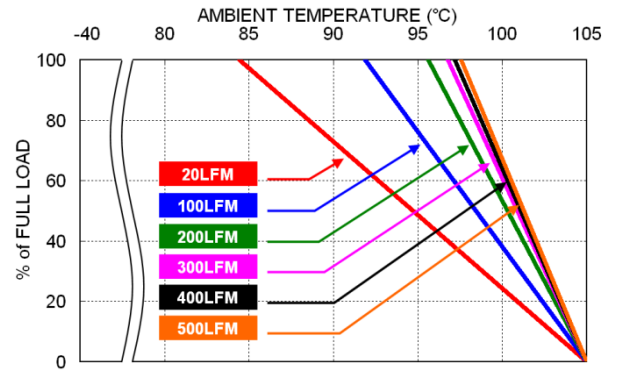
Power Dissipation versus Output Load



Efficiency versus Input Voltage



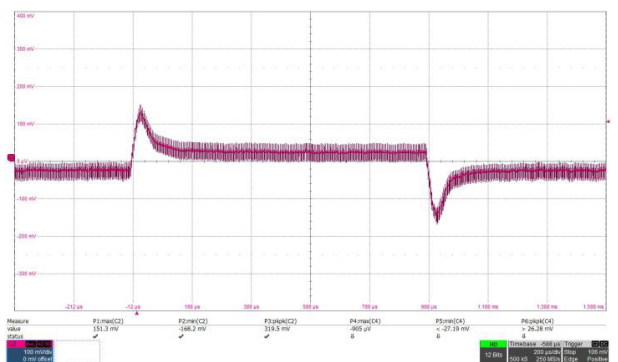
Derating Output Load versus Ambient Temperature



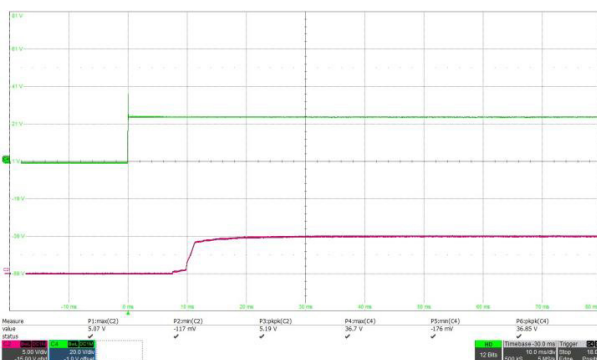
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



Transient Response to Dynamic Load Change (25%)

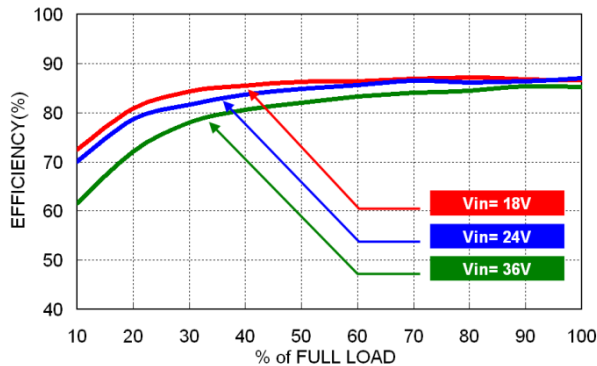


Typical Start-Up and Output Rise Characteristic

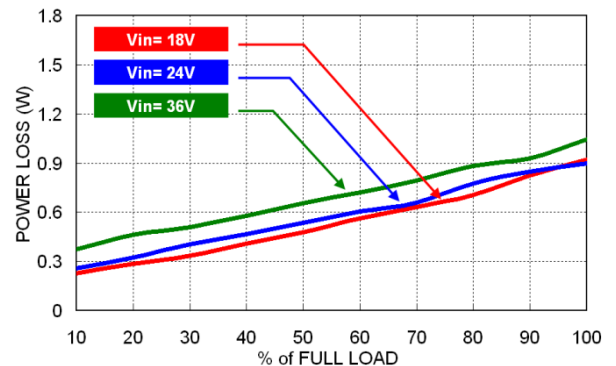


TIM 6-2412

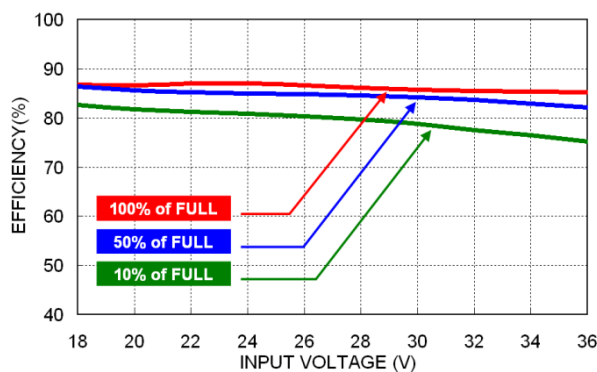
Efficiency versus Output Load



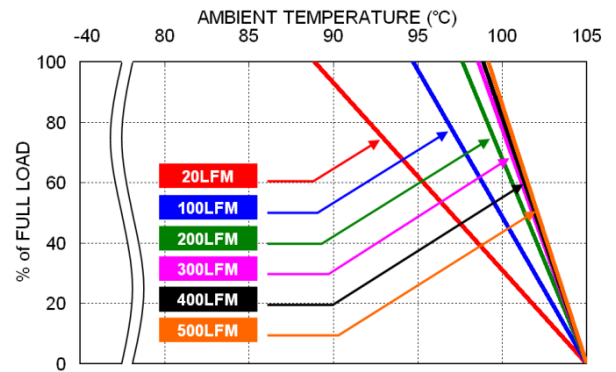
Power Dissipation versus Output Load



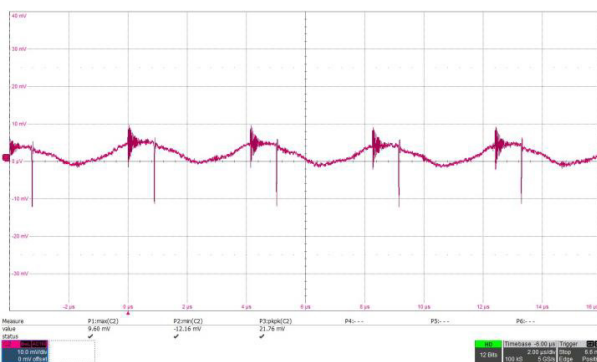
Efficiency versus Input Voltage



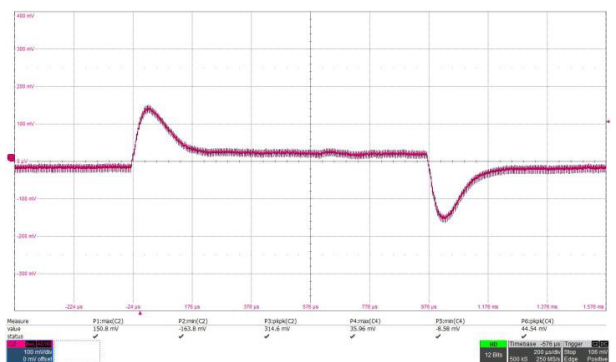
Derating Output Load versus Ambient Temperature



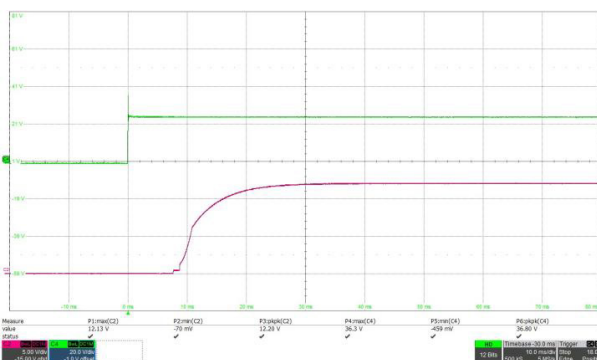
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



Transient Response to Dynamic Load Change (25%)

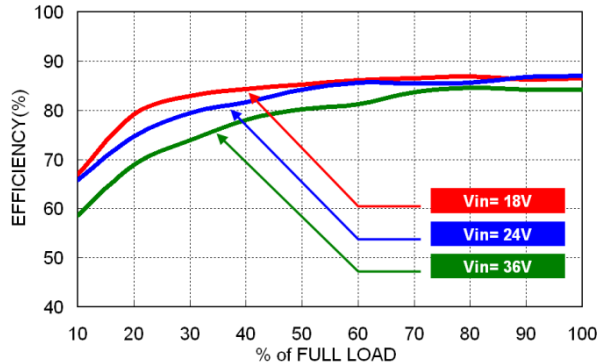


Typical Start-Up and Output Rise Characteristic

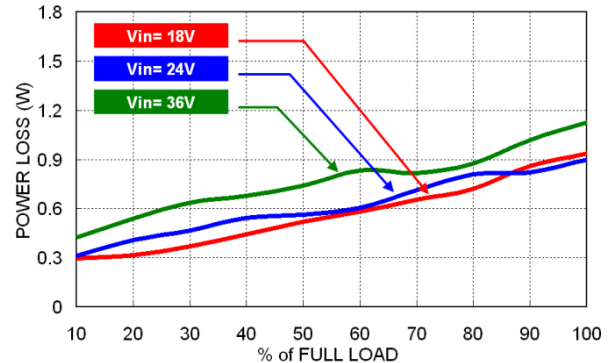


TIM 6-2413

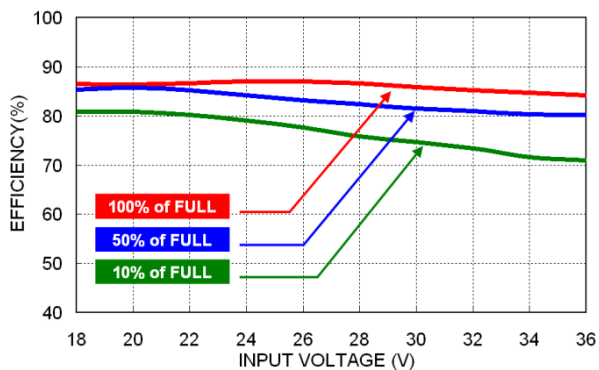
Efficiency versus Output Load



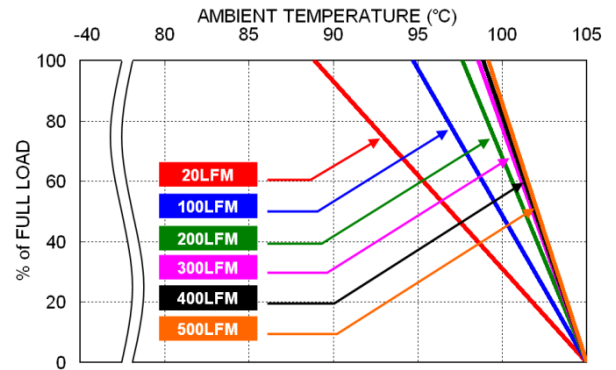
Power Dissipation versus Output Load



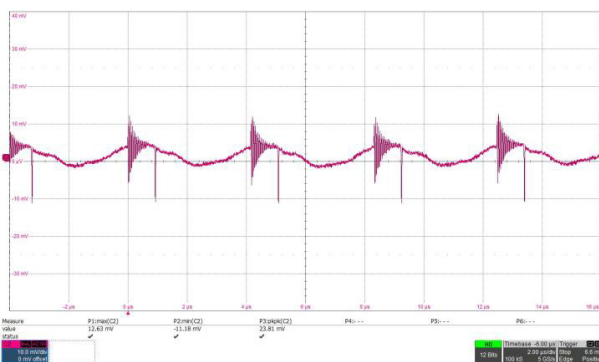
Efficiency versus Input Voltage



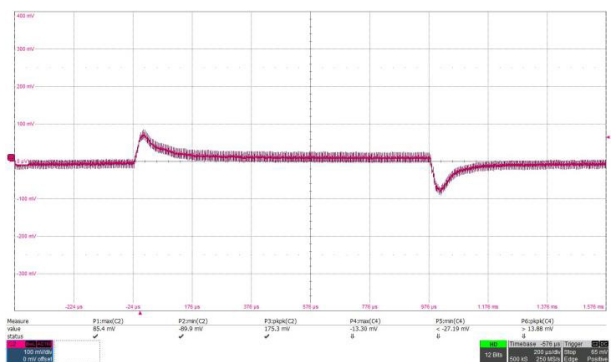
Derating Output Load versus Ambient Temperature



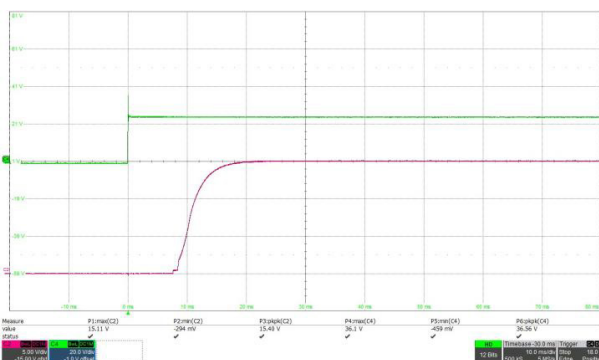
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



Transient Response to Dynamic Load Change (25%)

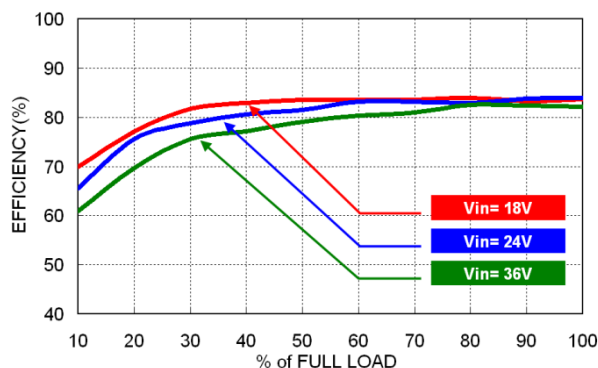


Typical Start-Up and Output Rise Characteristic

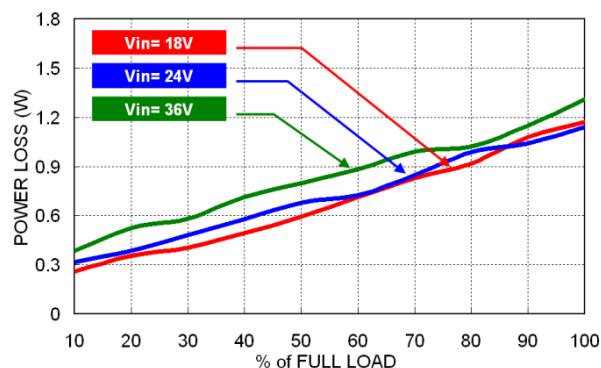


TIM 6-2421

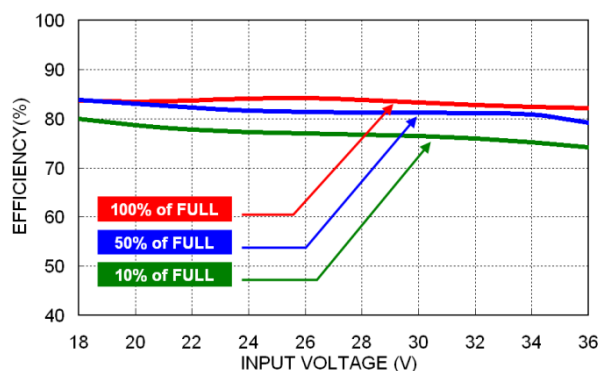
Efficiency versus Output Load



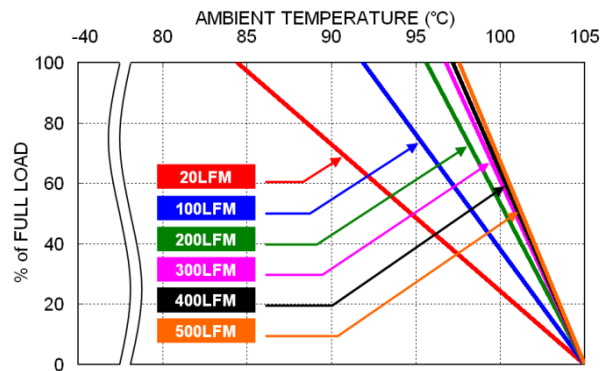
Power Dissipation versus Output Load



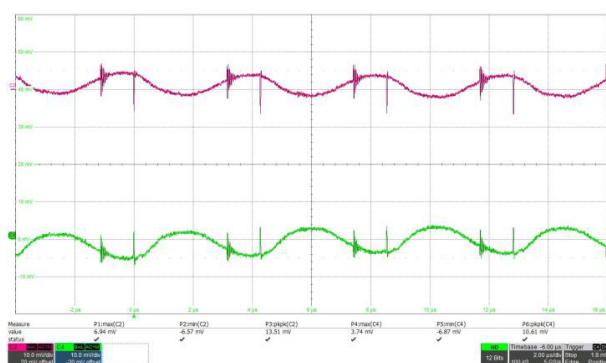
Efficiency versus Input Voltage



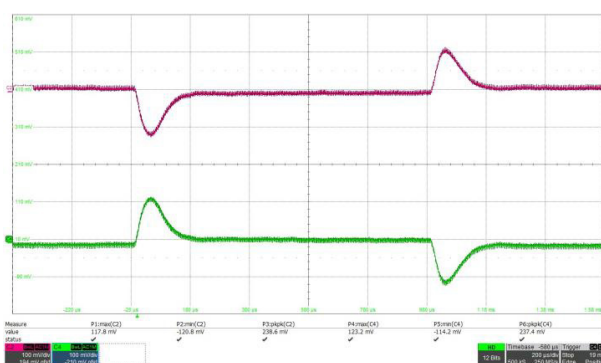
Derating Output Load versus Ambient Temperature



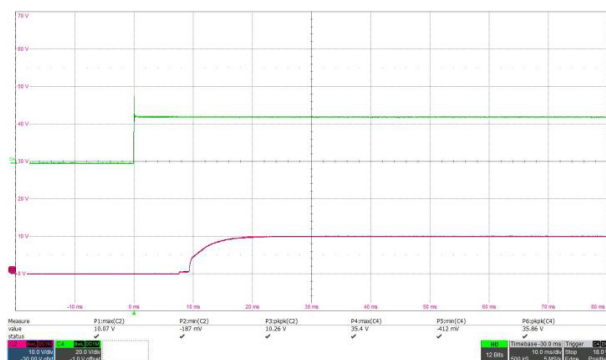
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



Transient Response to Dynamic Load Change (25%)

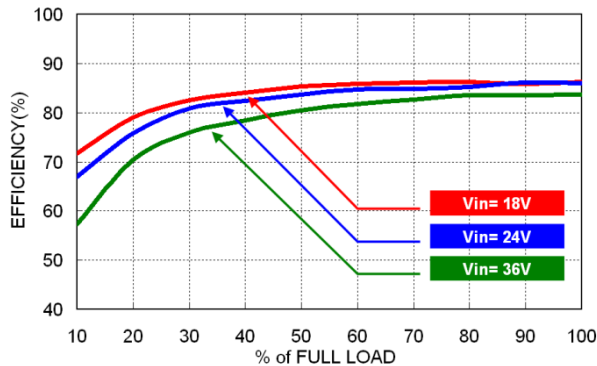


Typical Start-Up and Output Rise Characteristic

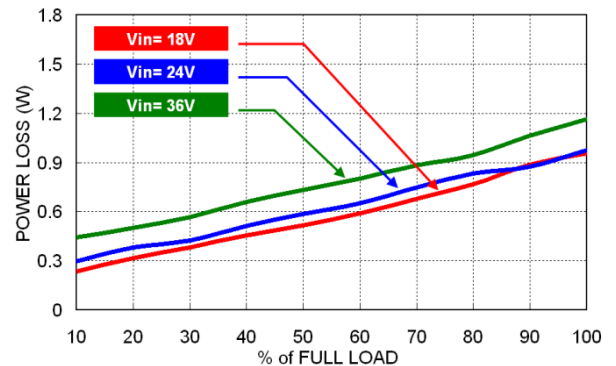


TIM 6-2422

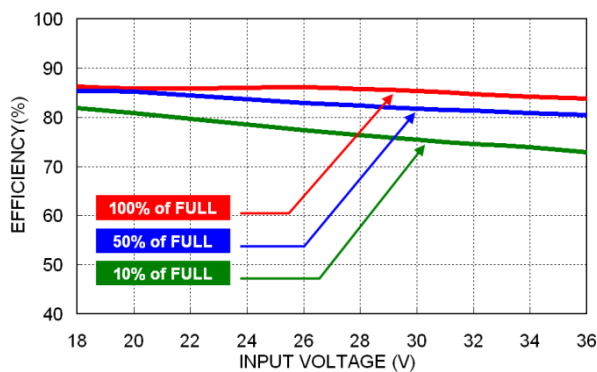
Efficiency versus Output Load



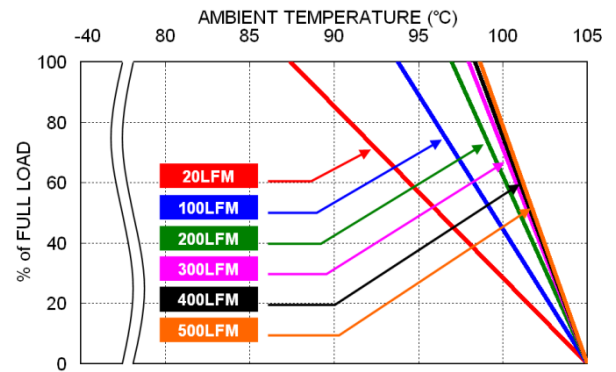
Power Dissipation versus Output Load



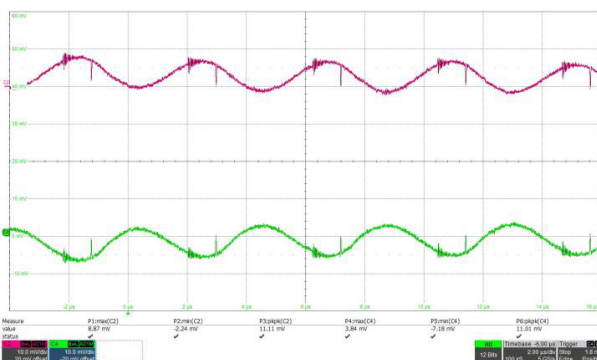
Efficiency versus Input Voltage



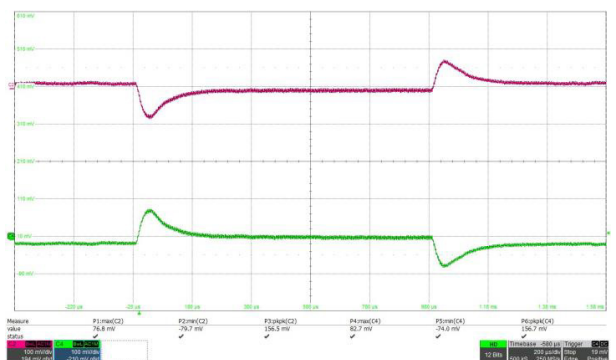
Derating Output Load versus Ambient Temperature



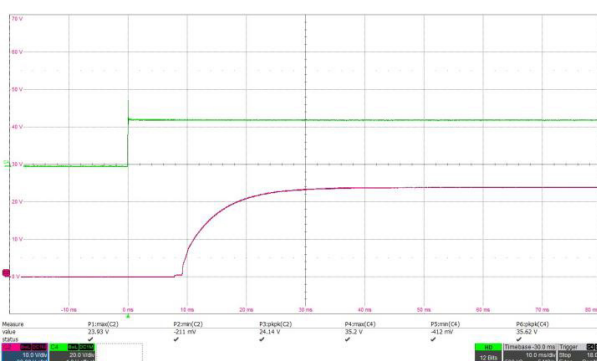
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



Transient Response to Dynamic Load Change (25%)

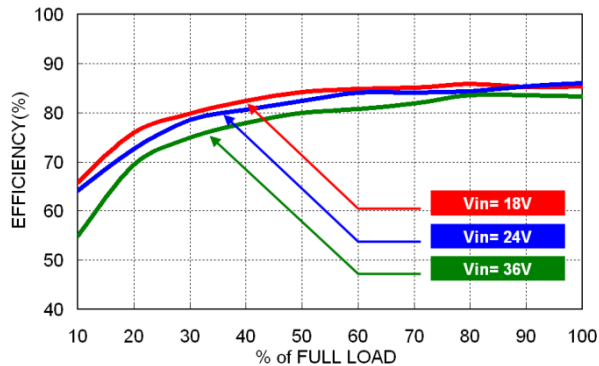


Typical Start-Up and Output Rise Characteristic

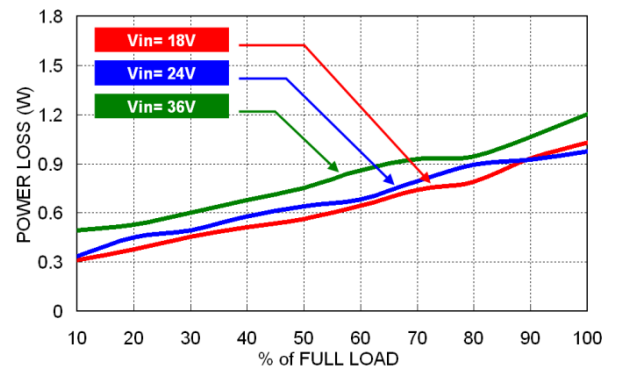


TIM 6-2423

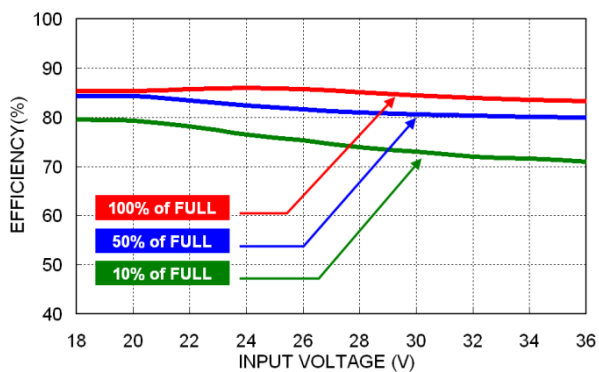
Efficiency versus Output Load



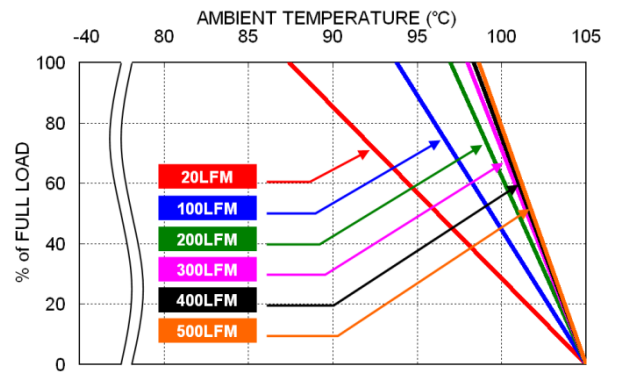
Power Dissipation versus Output Load



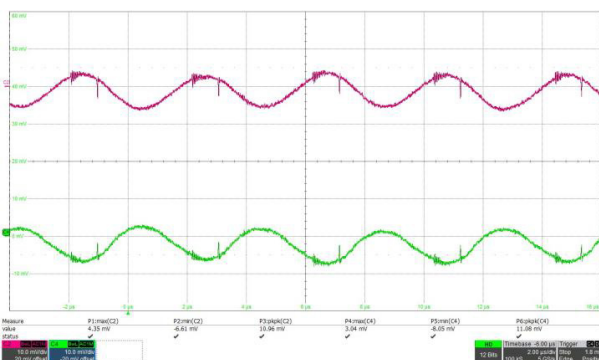
Efficiency versus Input Voltage



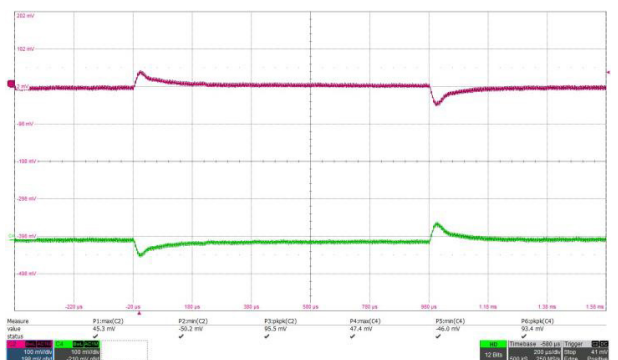
Derating Output Load versus Ambient Temperature



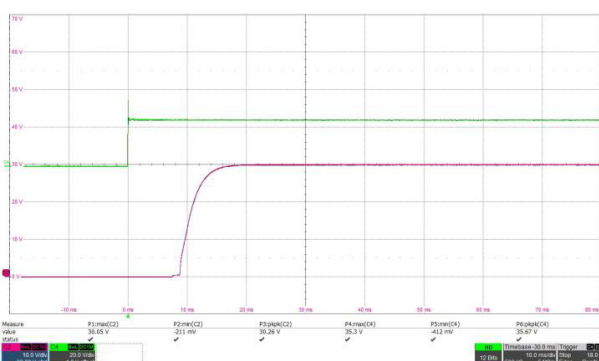
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



Transient Response to Dynamic Load Change (25%)

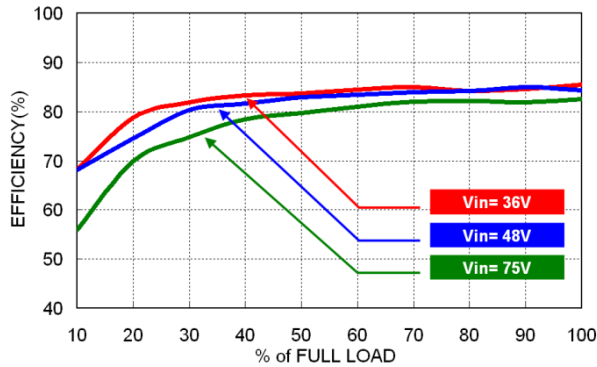


Typical Start-Up and Output Rise Characteristic

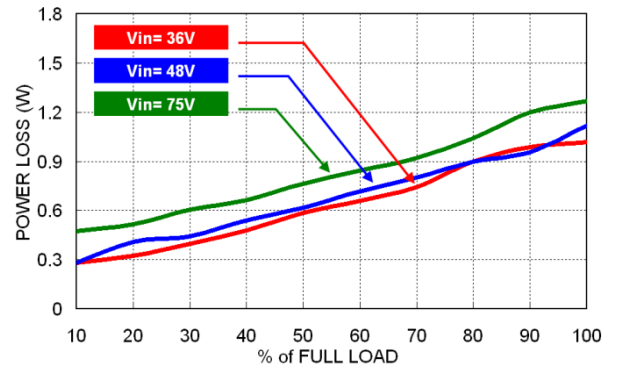


TIM 6-4811

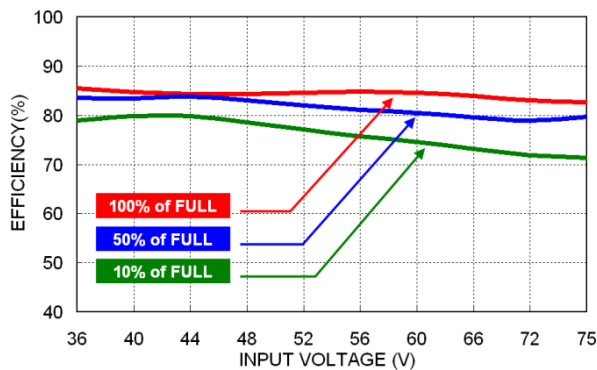
Efficiency versus Output Load



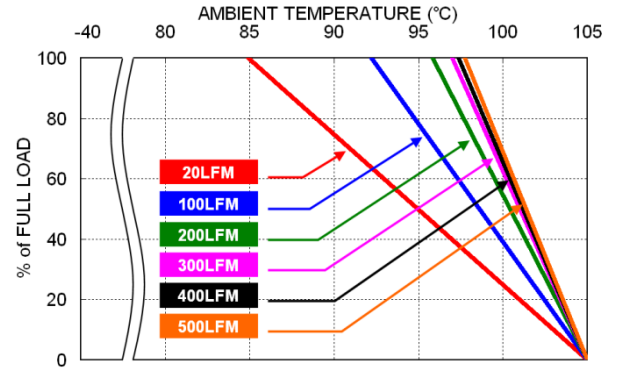
Power Dissipation versus Output Load



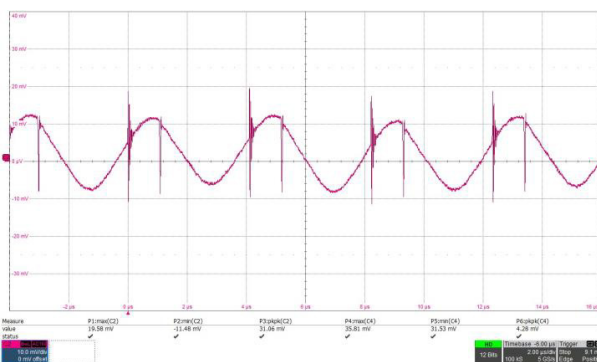
Efficiency versus Input Voltage



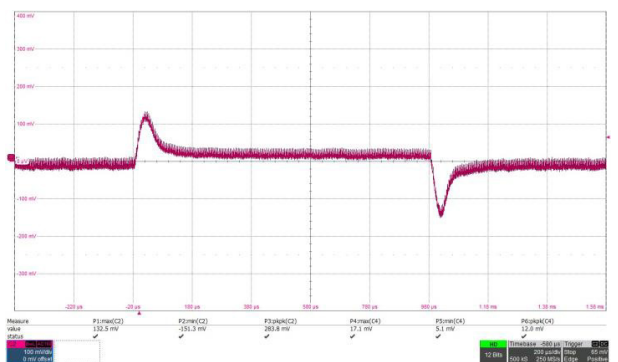
Derating Output Load versus Ambient Temperature



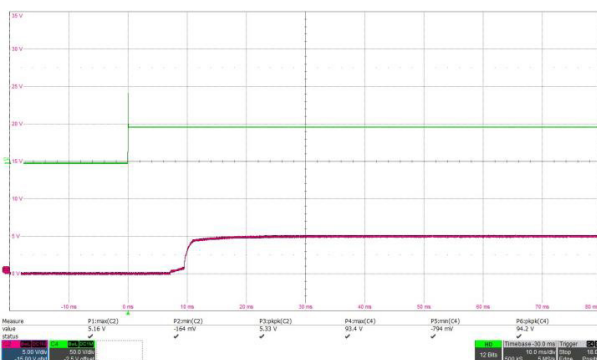
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



Transient Response to Dynamic Load Change (25%)

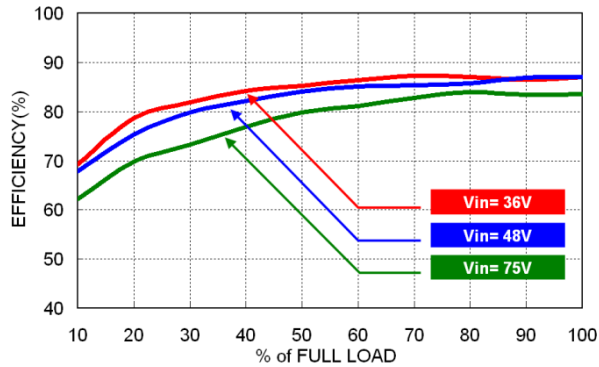


Typical Start-Up and Output Rise Characteristic

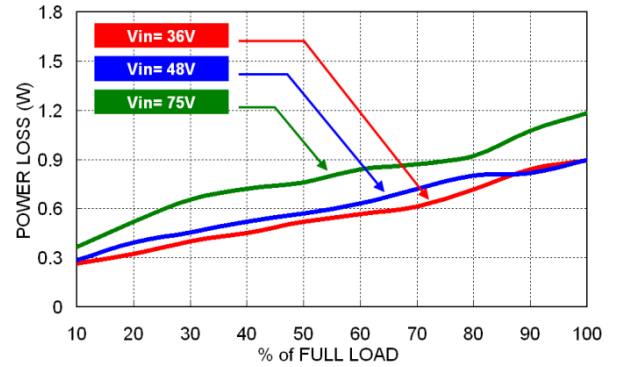


TIM 6-4812

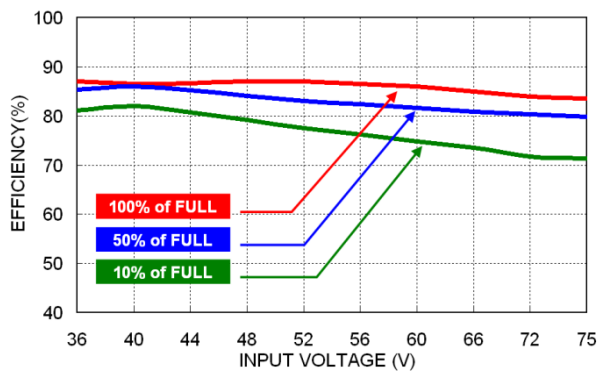
Efficiency versus Output Load



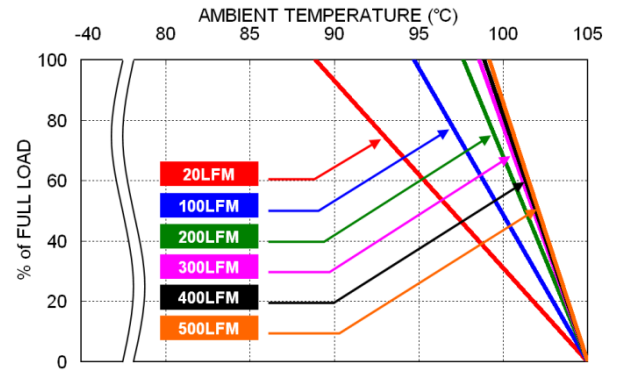
Power Dissipation versus Output Load



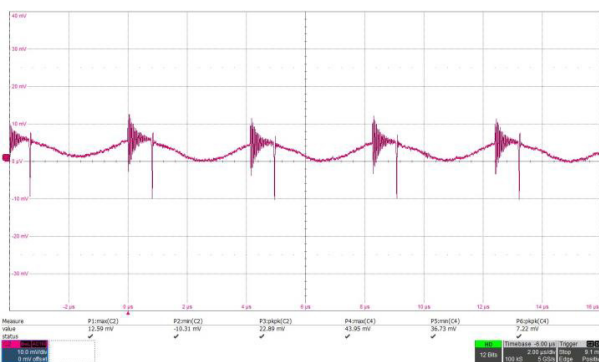
Efficiency versus Input Voltage



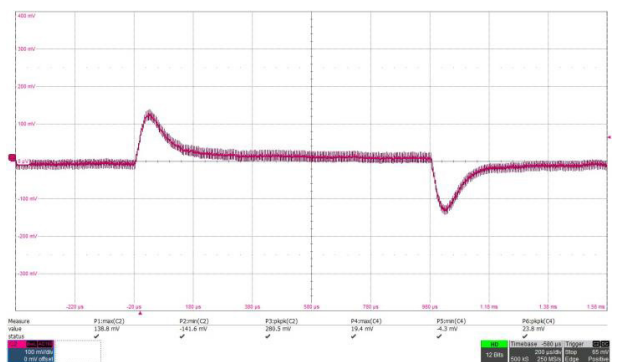
Derating Output Load versus Ambient Temperature



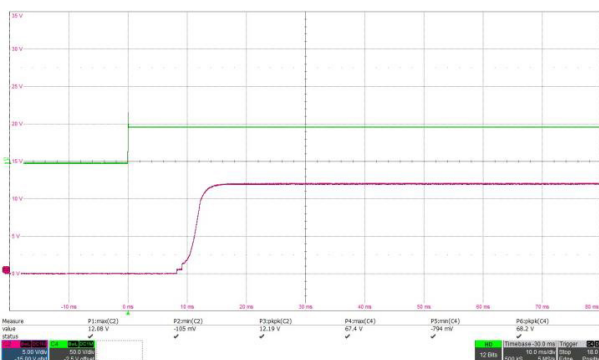
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



Transient Response to Dynamic Load Change (25%)

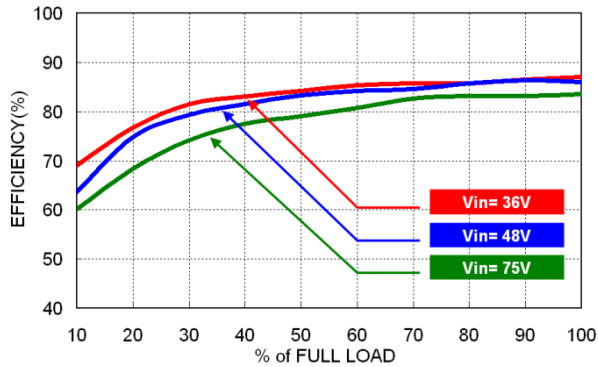


Typical Start-Up and Output Rise Characteristic

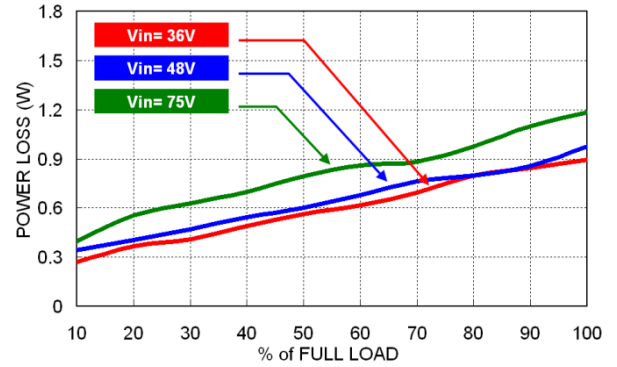


TIM 6-4813

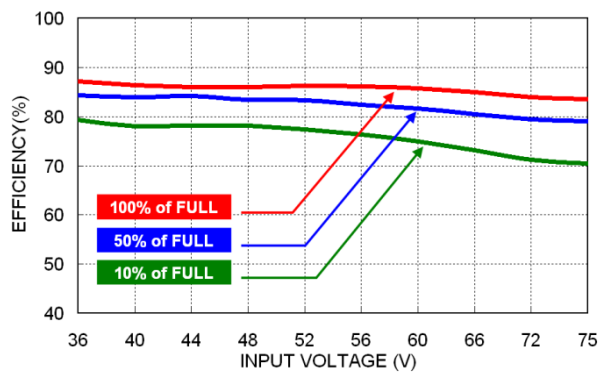
Efficiency versus Output Load



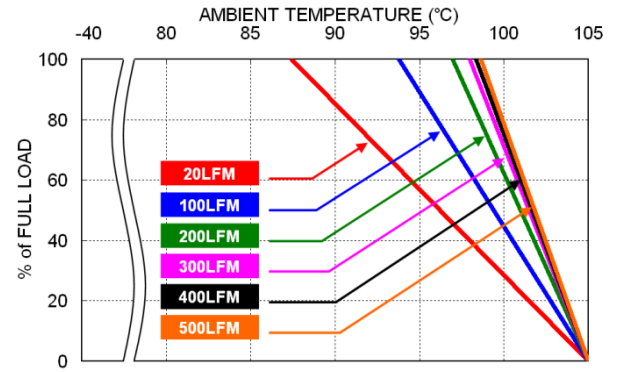
Power Dissipation versus Output Load



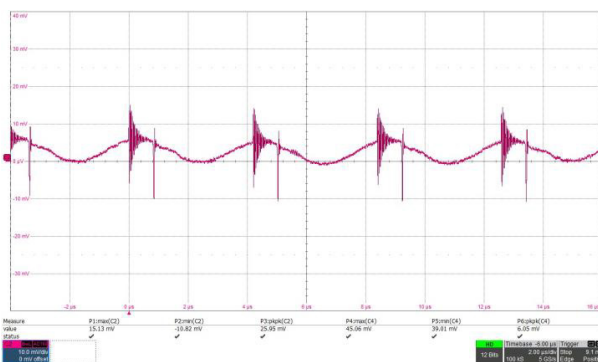
Efficiency versus Input Voltage



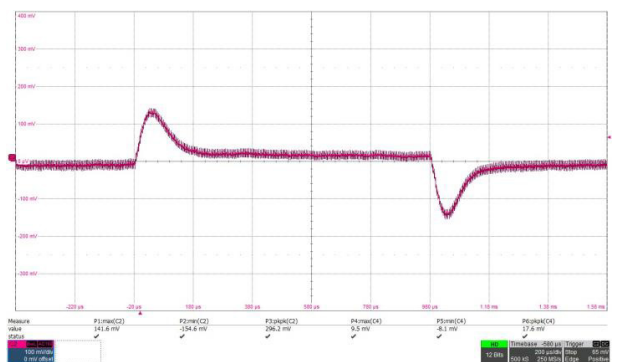
Derating Output Load versus Ambient Temperature



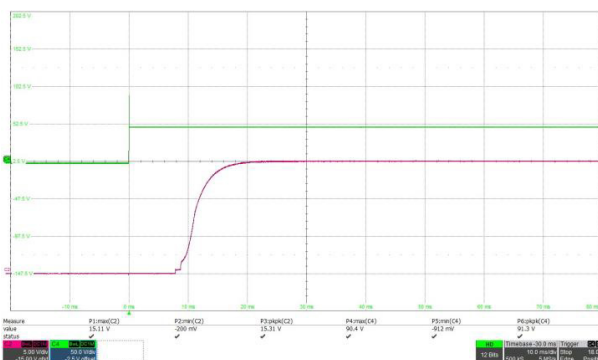
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



Transient Response to Dynamic Load Change (25%)

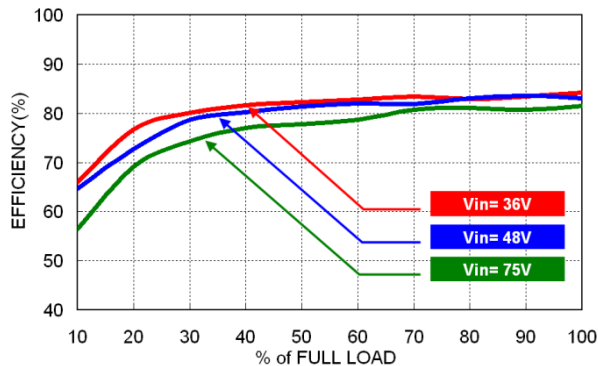


Typical Start-Up and Output Rise Characteristic

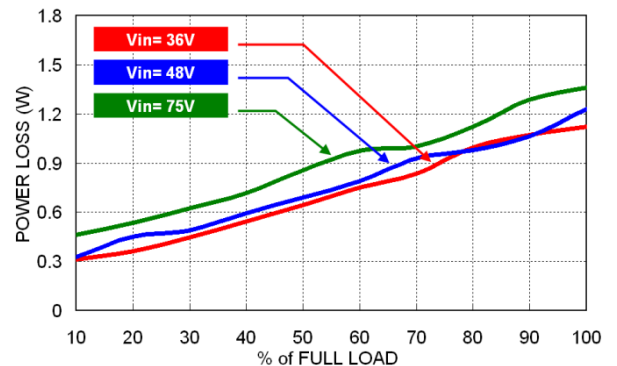


TIM 6-4821

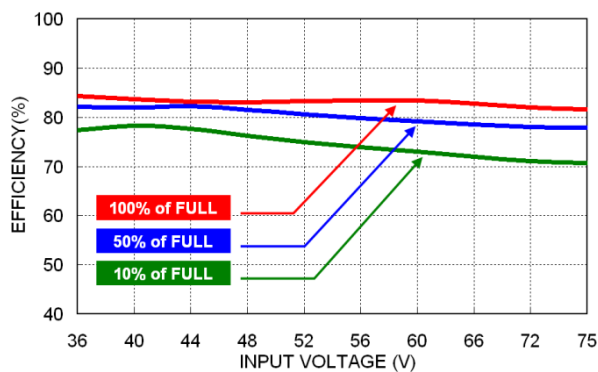
Efficiency versus Output Load



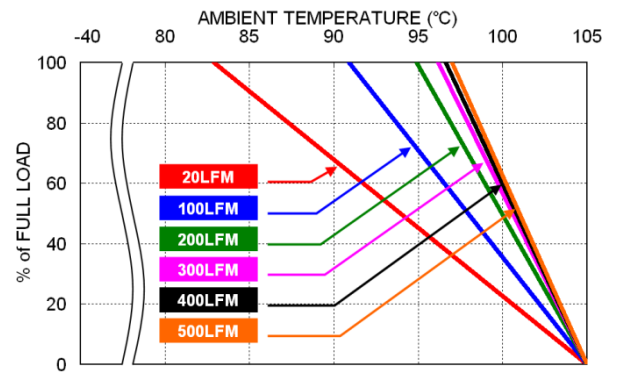
Power Dissipation versus Output Load



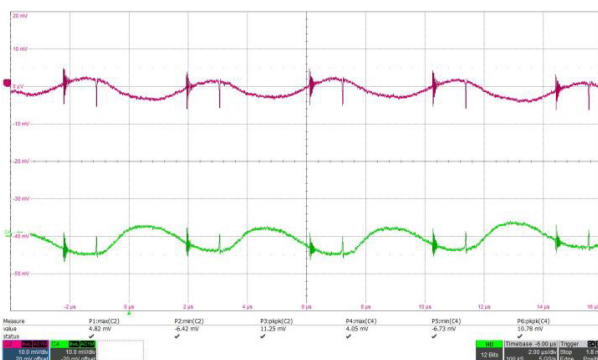
Efficiency versus Input Voltage



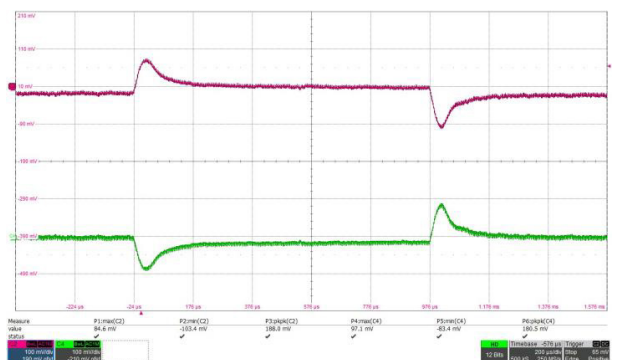
Derating Output Load versus Ambient Temperature



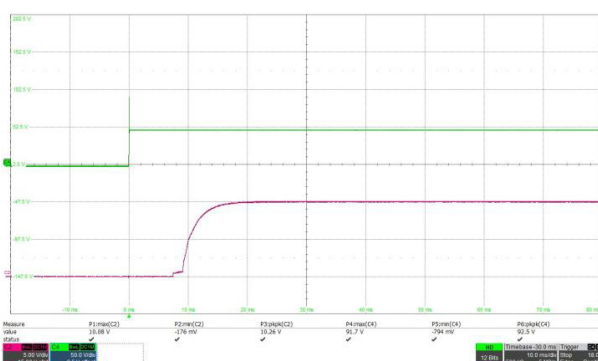
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



Transient Response to Dynamic Load Change (25%)

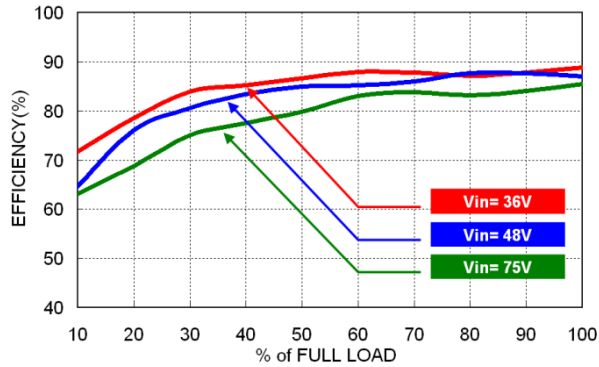


Typical Start-Up and Output Rise Characteristic

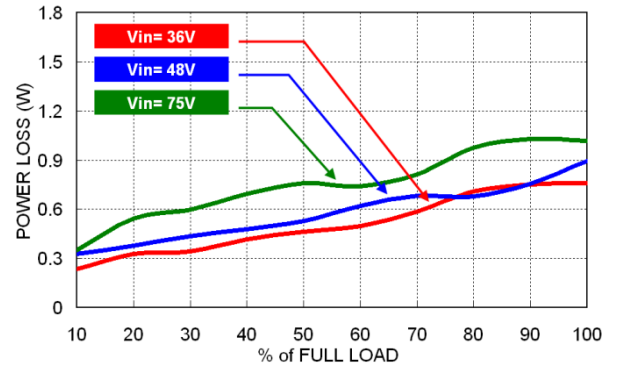


TIM 6-4822

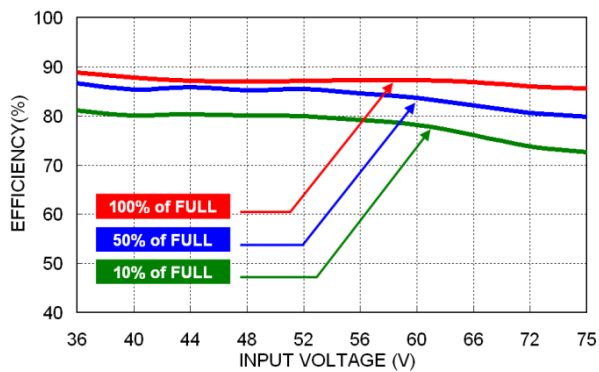
Efficiency versus Output Load



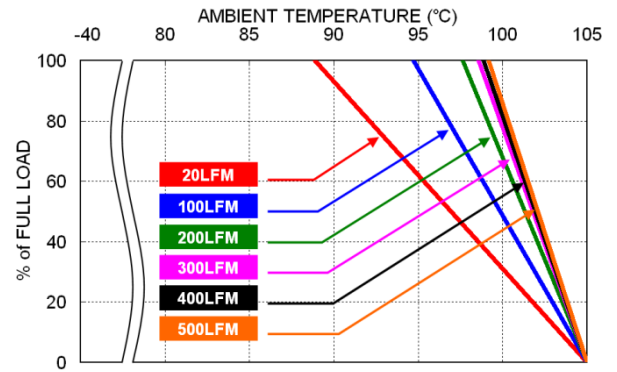
Power Dissipation versus Output Load



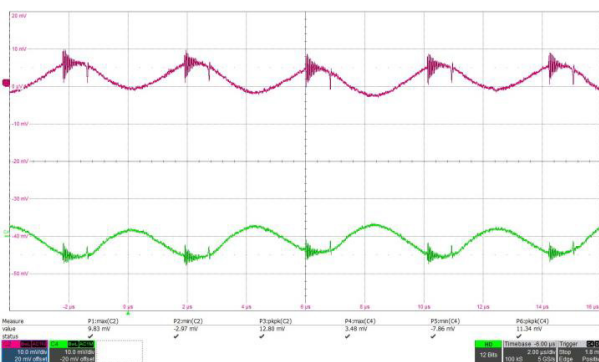
Efficiency versus Input Voltage



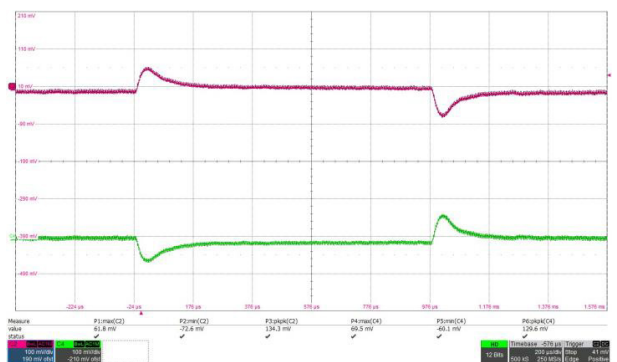
Derating Output Load versus Ambient Temperature



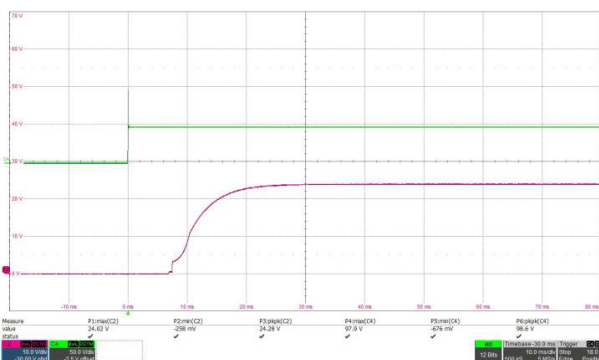
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



Transient Response to Dynamic Load Change (25%)

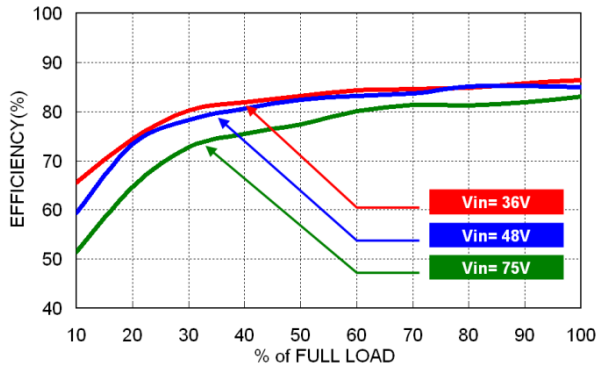


Typical Start-Up and Output Rise Characteristic

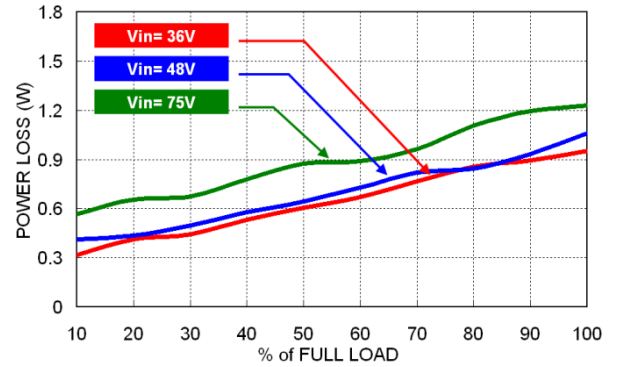


TIM 6-4823

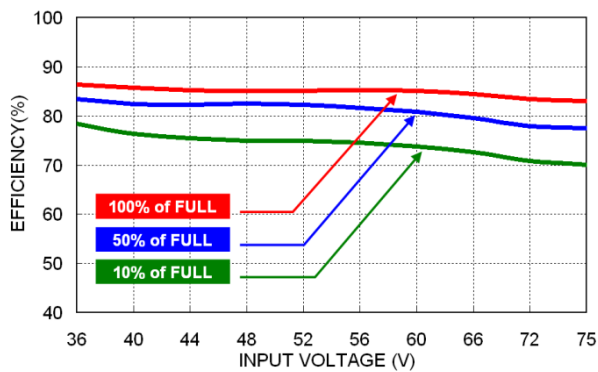
Efficiency versus Output Load



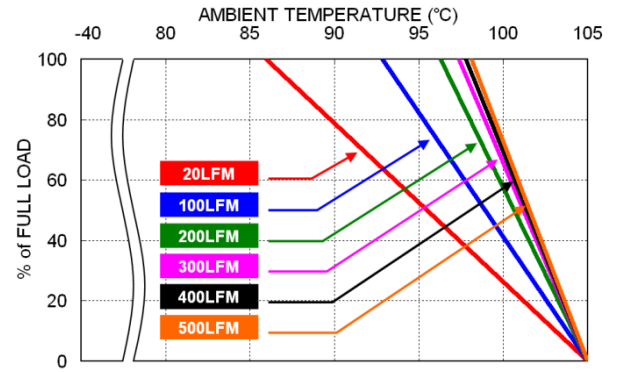
Power Dissipation versus Output Load



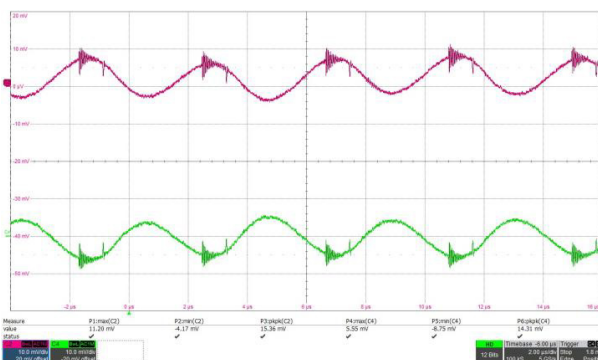
Efficiency versus Input Voltage



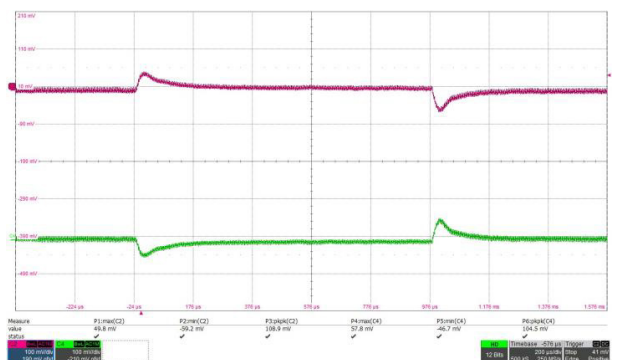
Derating Output Load versus Ambient Temperature



Typical Output Ripple and Noise
(with external capacitor; see datasheet)



Transient Response to Dynamic Load Change (25%)



Typical Start-Up and Output Rise Characteristic

