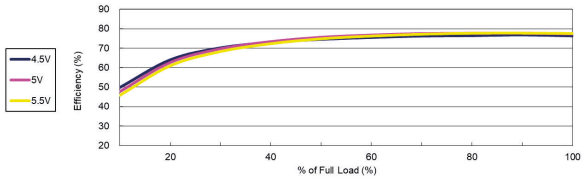
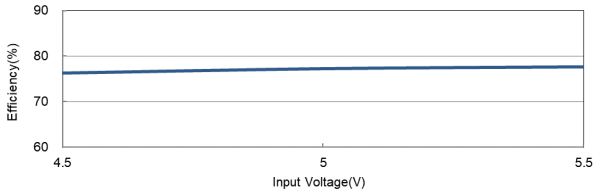
Characteristic Curves

TSM 0505S

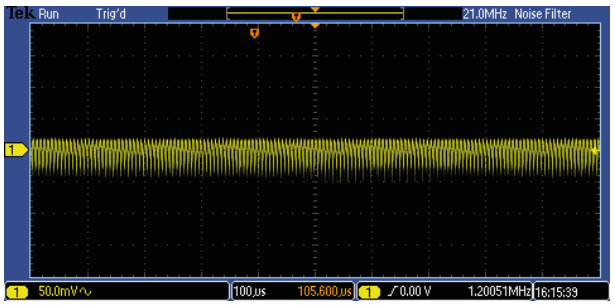
Efficiency versus Output Load



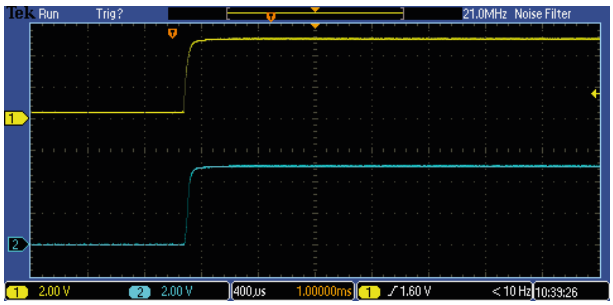
Efficiency versus Input Voltage



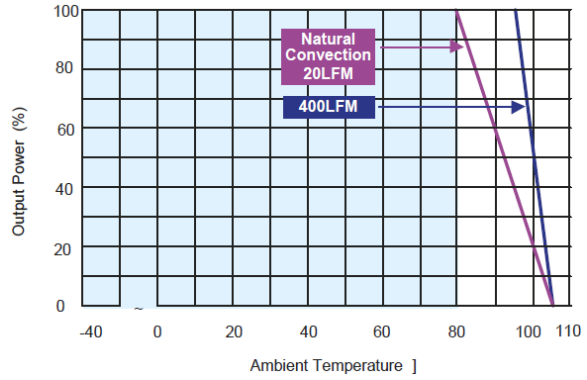
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



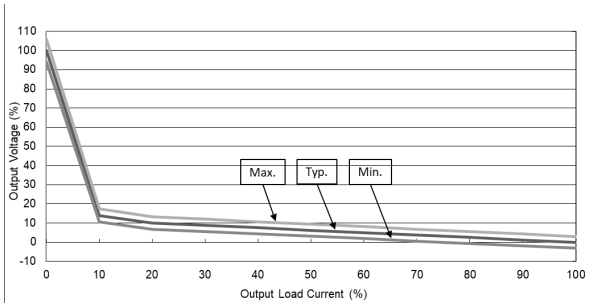
Typical Input Start-Up and Output Rise Characteristic



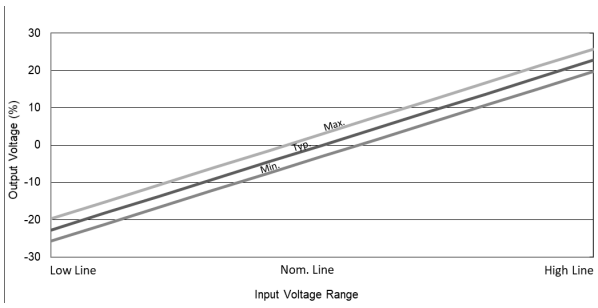
Derating Output versus Ambient Temperature



Load Variation versus Output Voltage

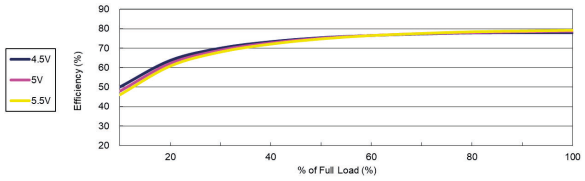


Input Variation versus Output Voltage

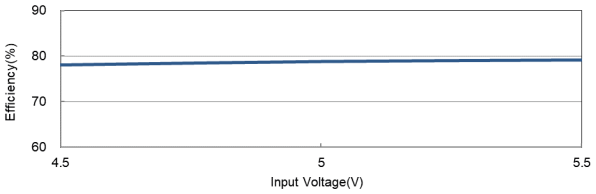


TSM 0509S

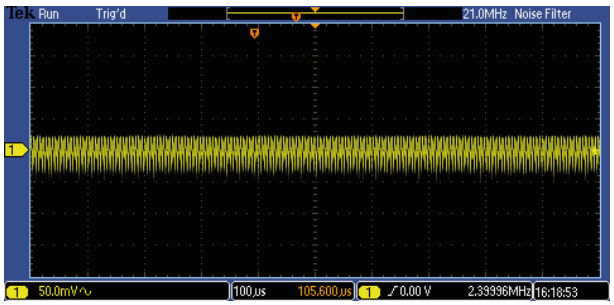
Efficiency versus Output Load



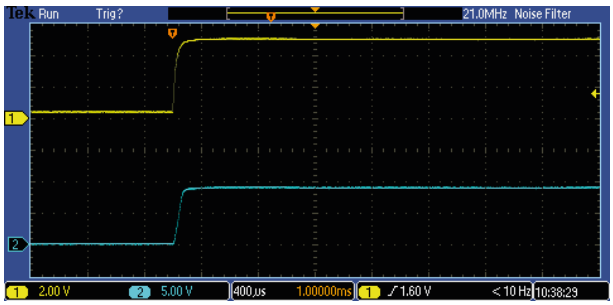
Efficiency versus Input Voltage



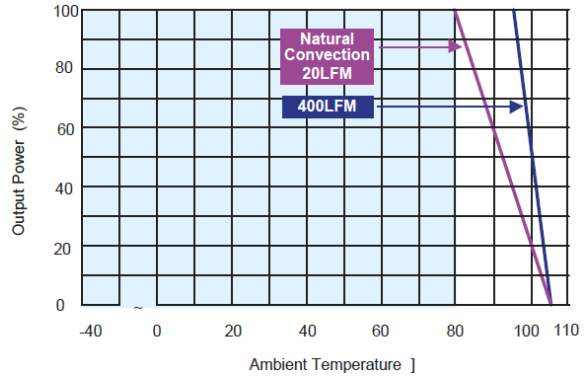
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



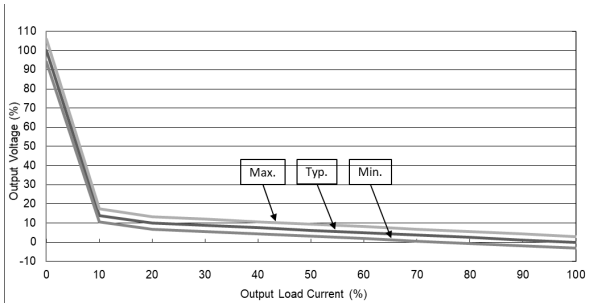
Typical Input Start-Up and Output Rise Characteristic



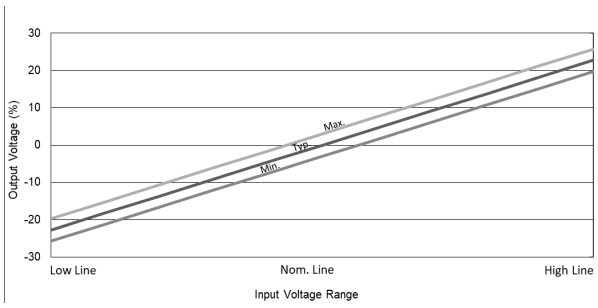
Derating Output versus Ambient Temperature



Load Variation versus Output Voltage

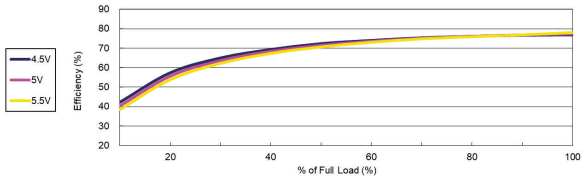


Input Variation versus Output Voltage

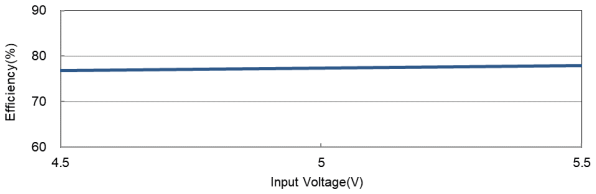


TSM 0512S

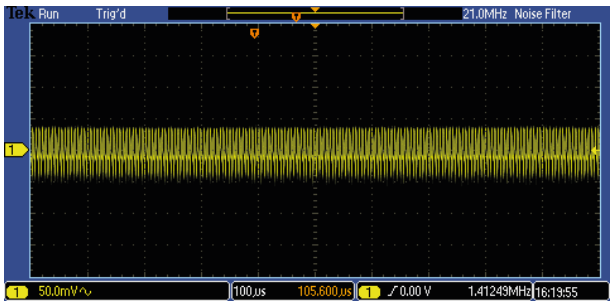
Efficiency versus Output Load



Efficiency versus Input Voltage

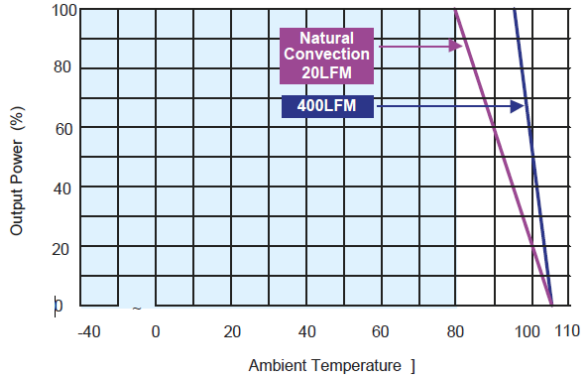


Typical Output Ripple and Noise
(with external capacitor; see datasheet)

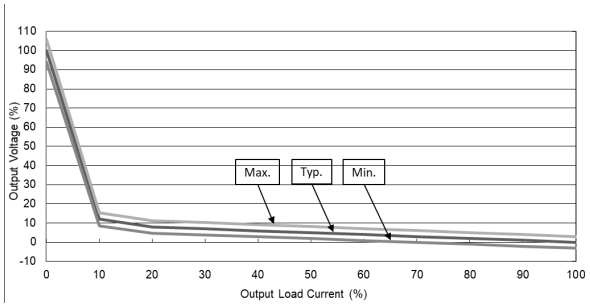


Typical Input Start-Up and Output Rise Characteristic

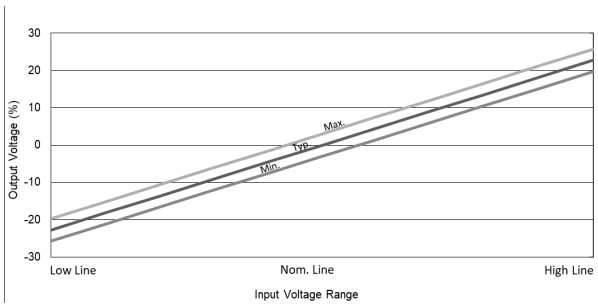
Derating Output versus Ambient Temperature



Load Variation versus Output Voltage

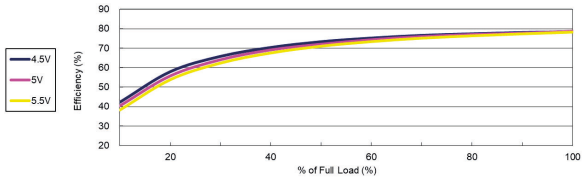


Input Variation versus Output Voltage

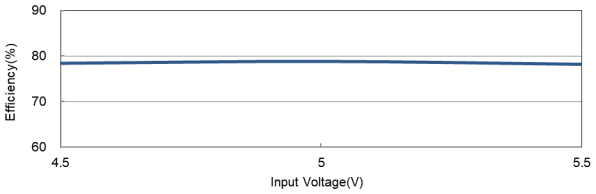


TSM 0515S

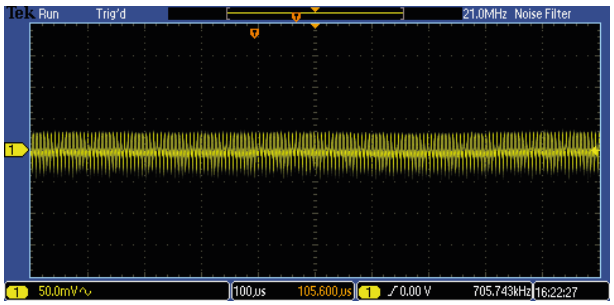
Efficiency versus Output Load



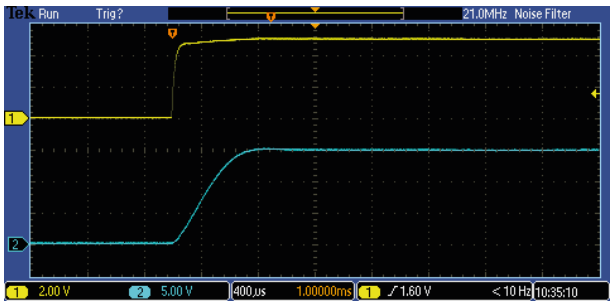
Efficiency versus Input Voltage



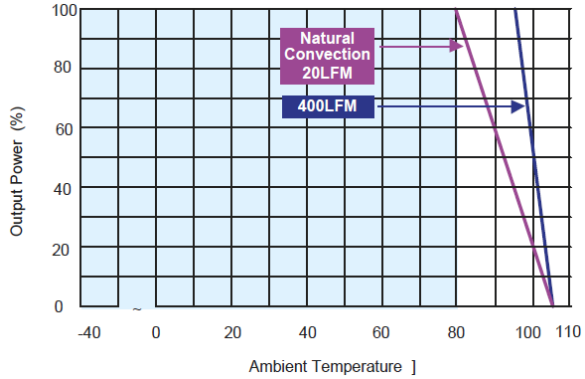
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



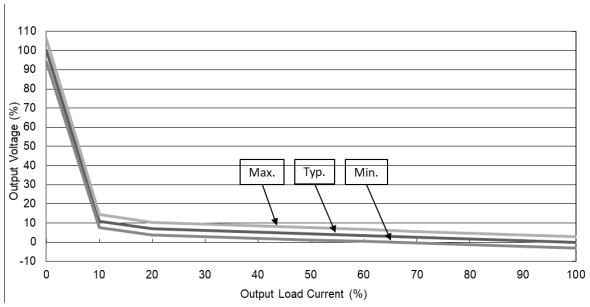
Typical Input Start-Up and Output Rise Characteristic



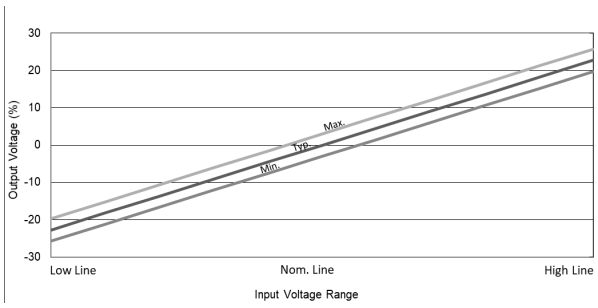
Derating Output versus Ambient Temperature



Load Variation versus Output Voltage

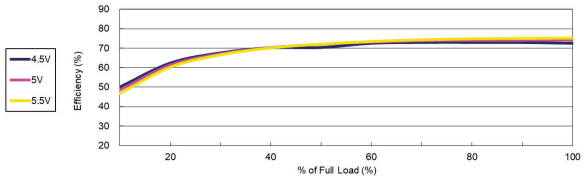


Input Variation versus Output Voltage

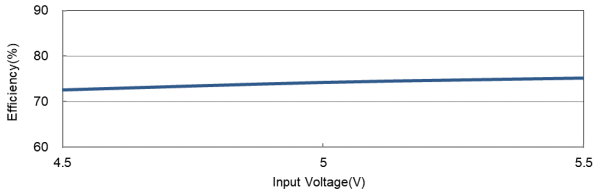


TSM 0505D

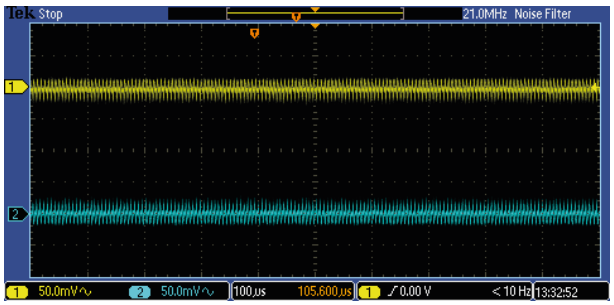
Efficiency versus Output Load



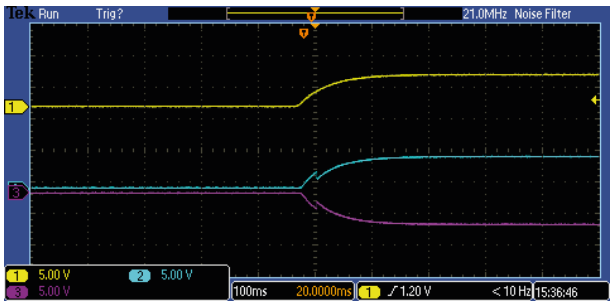
Efficiency versus Input Voltage



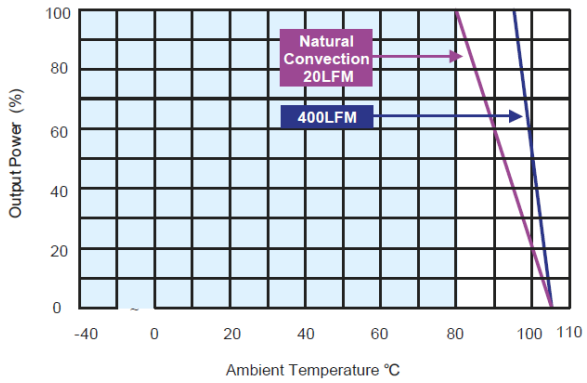
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



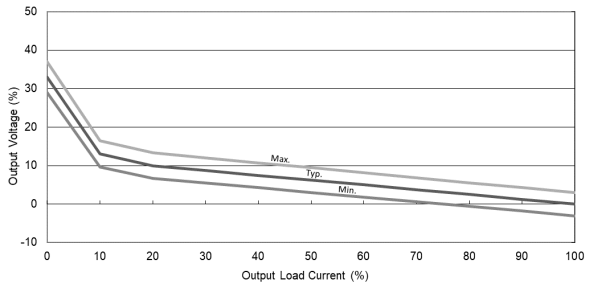
Typical Input Start-Up and Output Rise Characteristic



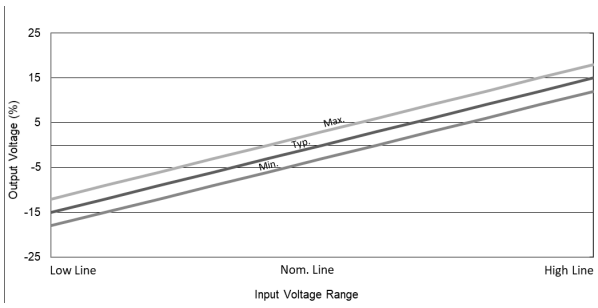
Derating Output versus Ambient Temperature



Load Variation versus Output Voltage

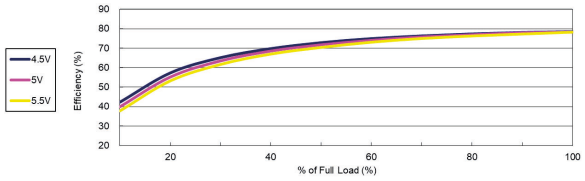


Input Variation versus Output Voltage

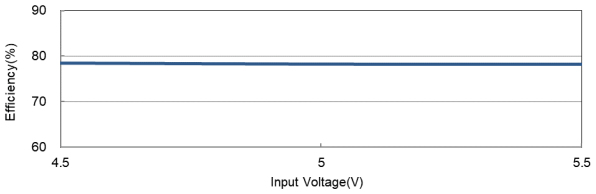


TSM 0512D

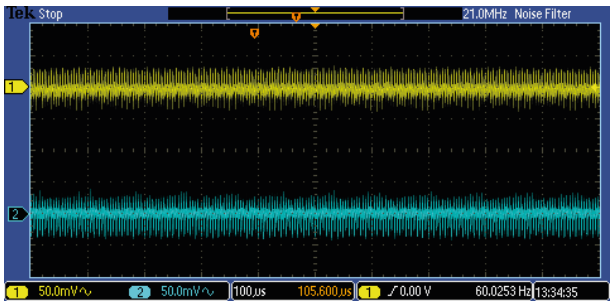
Efficiency versus Output Load



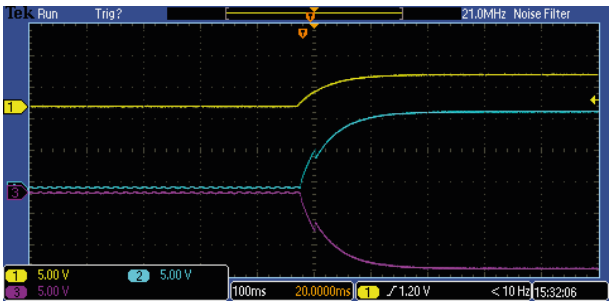
Efficiency versus Input Voltage



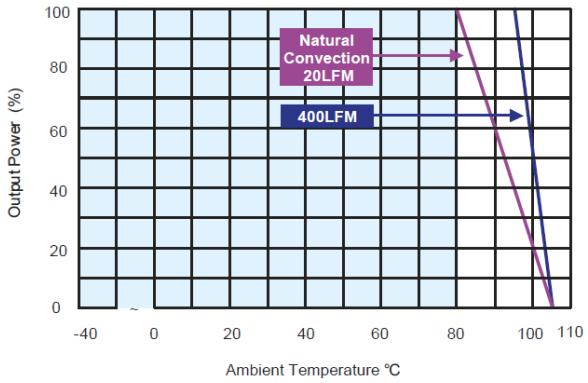
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



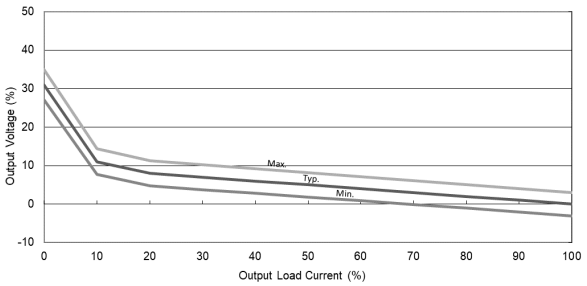
Typical Input Start-Up and Output Rise Characteristic



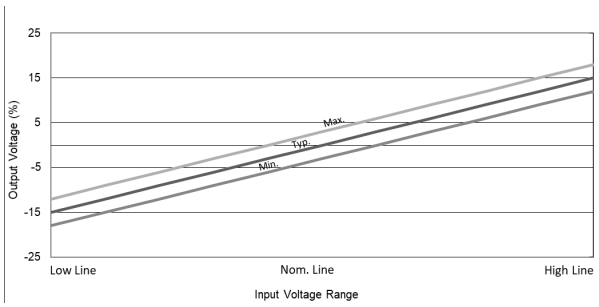
Derating Output versus Ambient Temperature



Load Variation versus Output Voltage

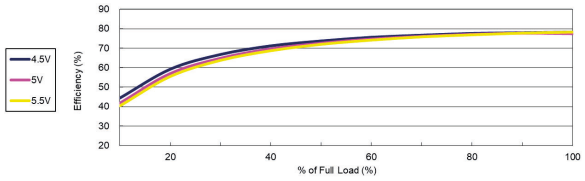


Input Variation versus Output Voltage

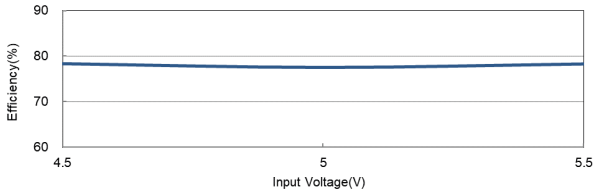


TSM 0515D

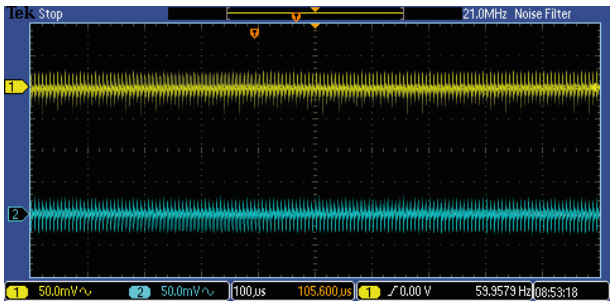
Efficiency versus Output Load



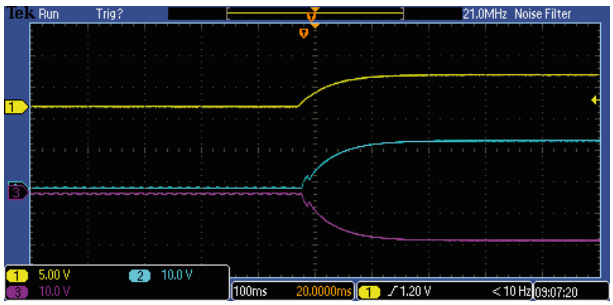
Efficiency versus Input Voltage



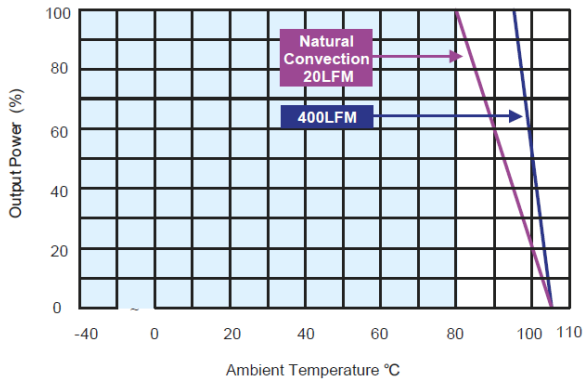
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



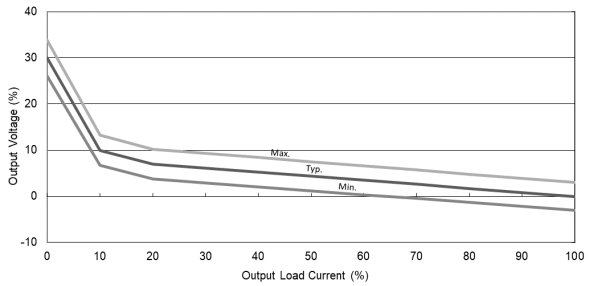
Typical Input Start-Up and Output Rise Characteristic



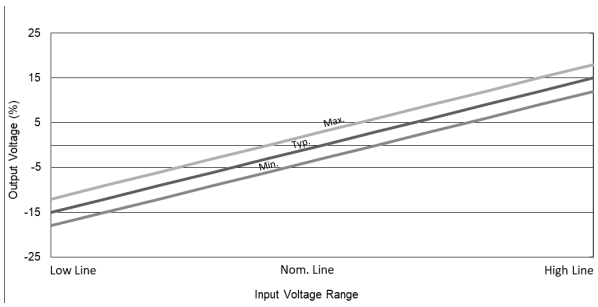
Derating Output versus Ambient Temperature



Load Variation versus Output Voltage

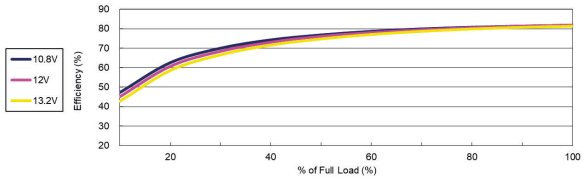


Input Variation versus Output Voltage

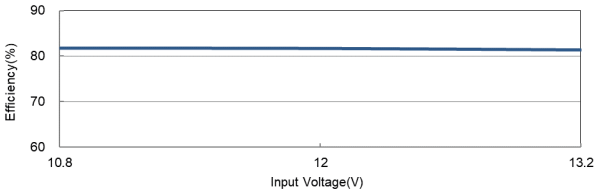


TSM 1205S

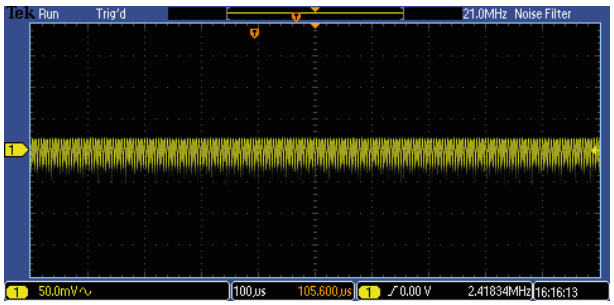
Efficiency versus Output Load



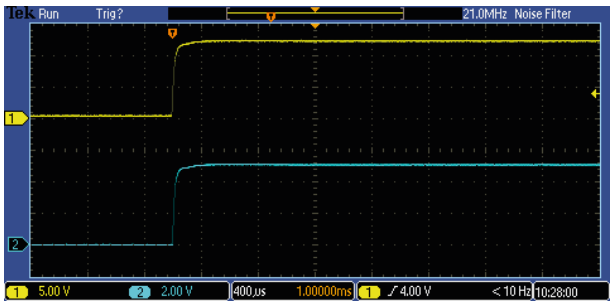
Efficiency versus Input Voltage



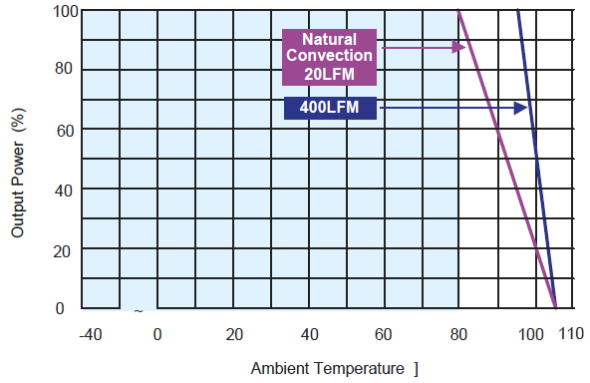
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



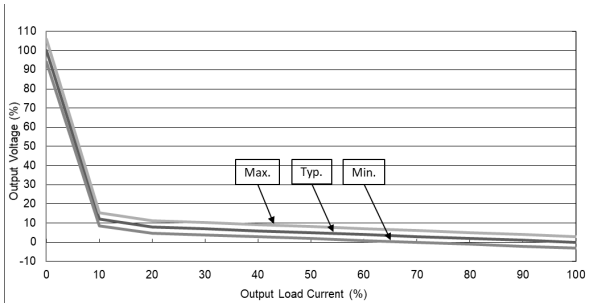
Typical Input Start-Up and Output Rise Characteristic



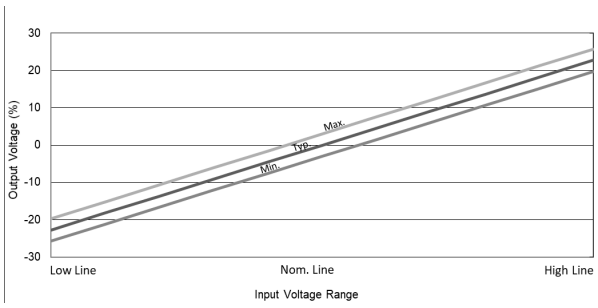
Derating Output versus Ambient Temperature



Load Variation versus Output Voltage

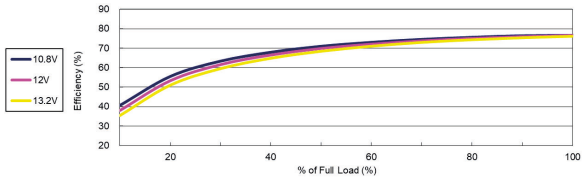


Input Variation versus Output Voltage

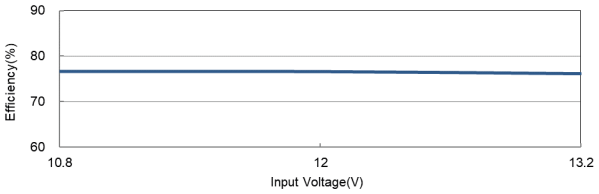


TSM 1209S

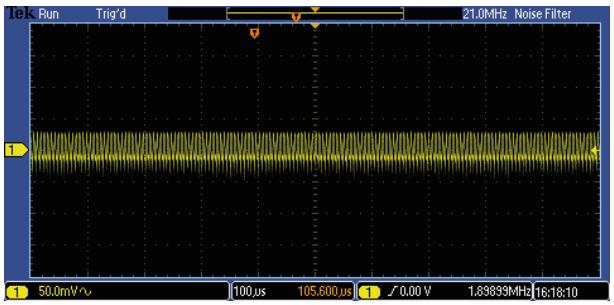
Efficiency versus Output Load



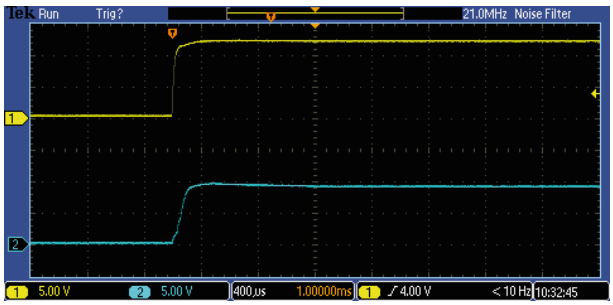
Efficiency versus Input Voltage



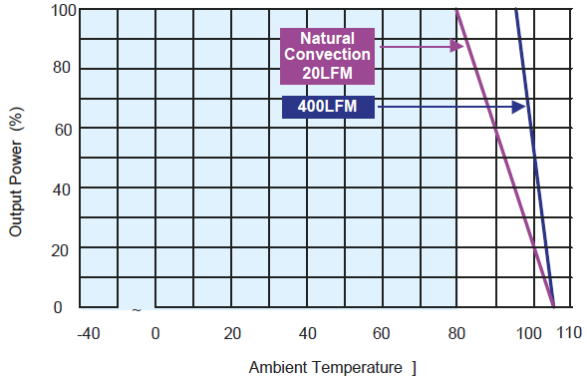
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



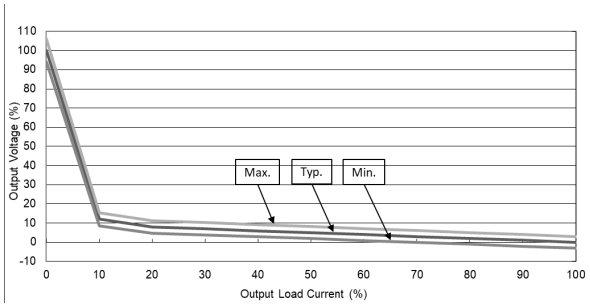
Typical Input Start-Up and Output Rise Characteristic



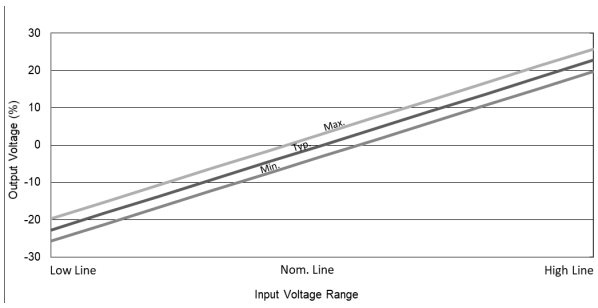
Derating Output versus Ambient Temperature



Load Variation versus Output Voltage

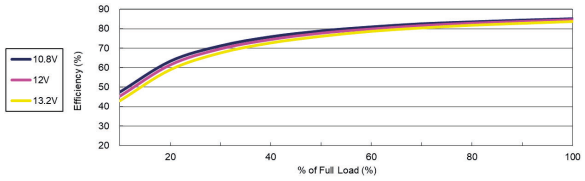


Input Variation versus Output Voltage

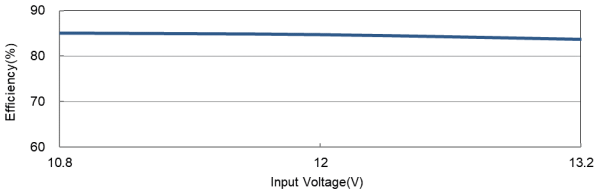


TSM 1212S

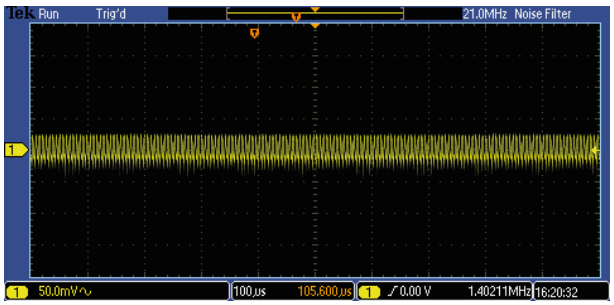
Efficiency versus Output Load



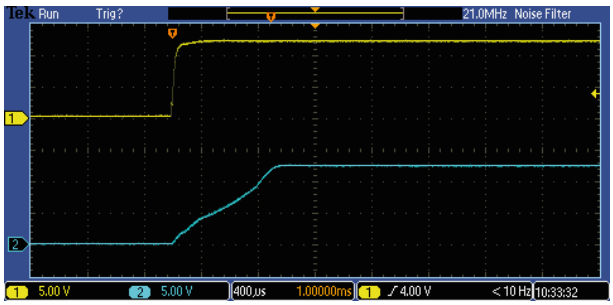
Efficiency versus Input Voltage



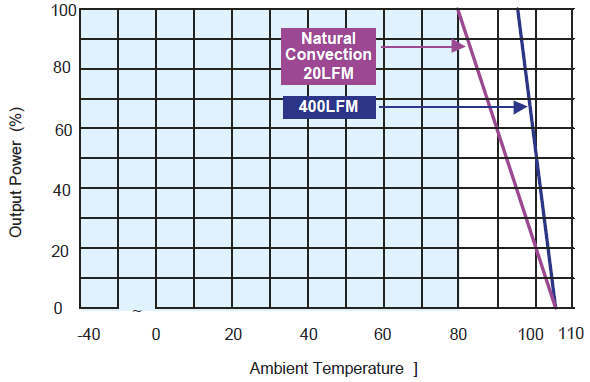
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



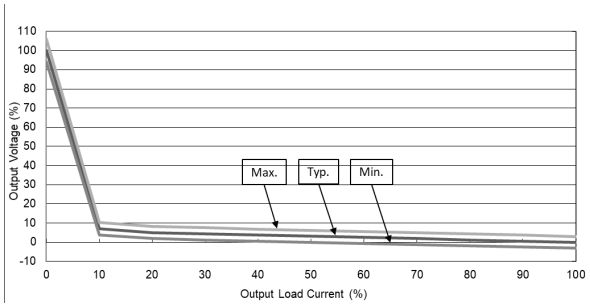
Typical Input Start-Up and Output Rise Characteristic



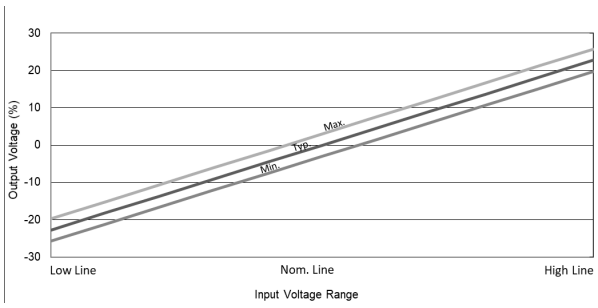
Derating Output versus Ambient Temperature



Load Variation versus Output Voltage

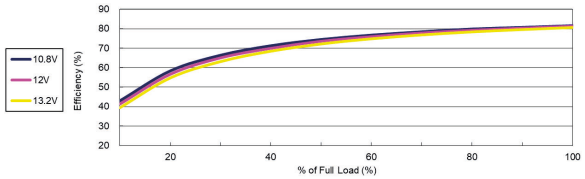


Input Variation versus Output Voltage

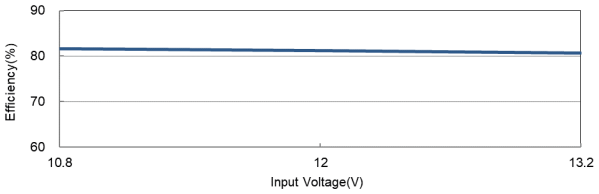


TSM 1215S

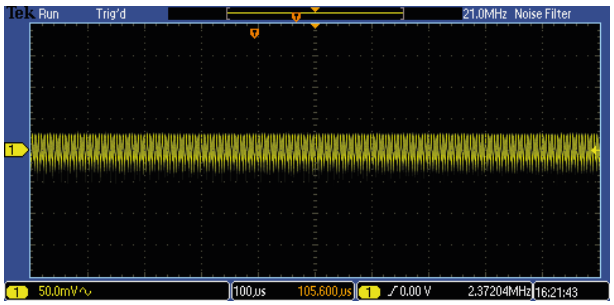
Efficiency versus Output Load



Efficiency versus Input Voltage

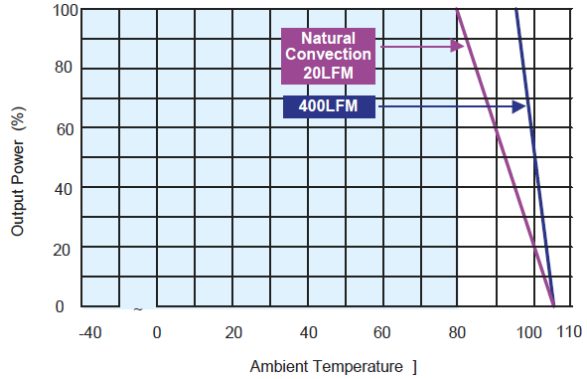


Typical Output Ripple and Noise
(with external capacitor; see datasheet)

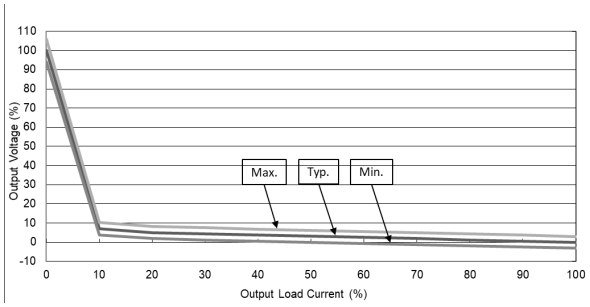


Typical Input Start-Up and Output Rise Characteristic

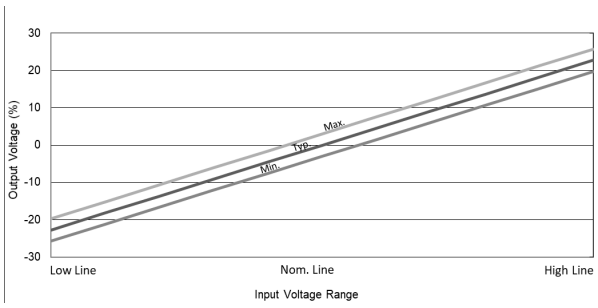
Derating Output versus Ambient Temperature



Load Variation versus Output Voltage

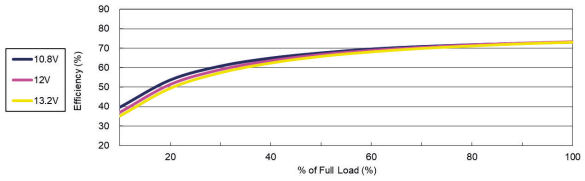


Input Variation versus Output Voltage

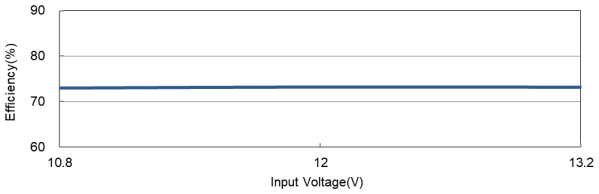


TSM 1205D

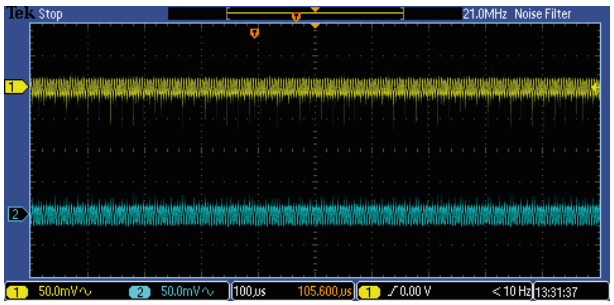
Efficiency versus Output Load



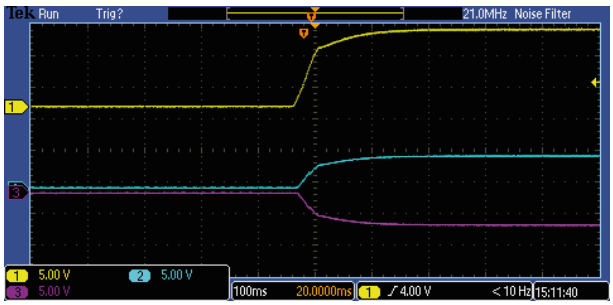
Efficiency versus Input Voltage



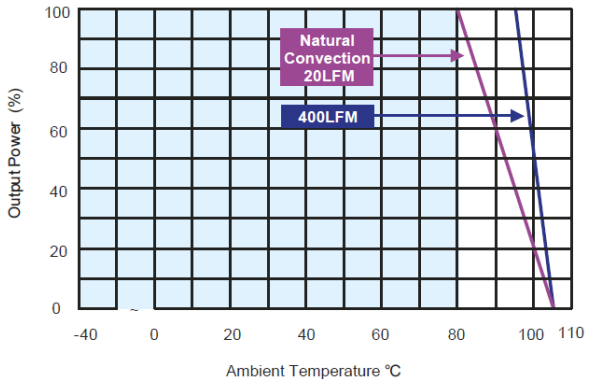
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



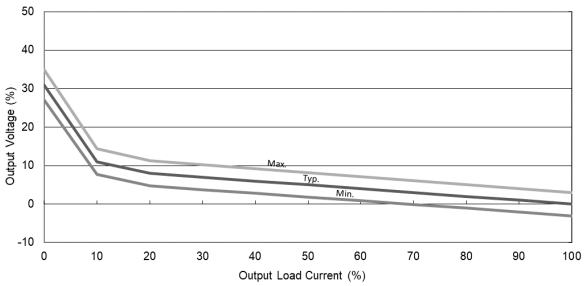
Typical Input Start-Up and Output Rise Characteristic



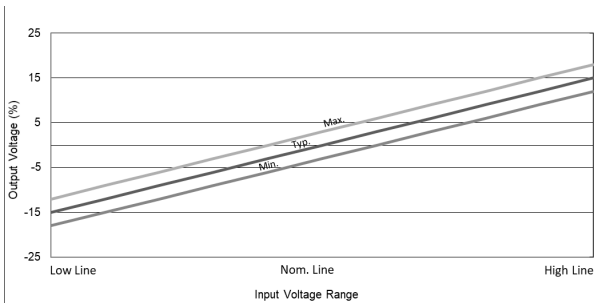
Derating Output versus Ambient Temperature



Load Variation versus Output Voltage

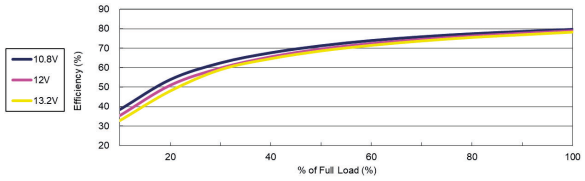


Input Variation versus Output Voltage

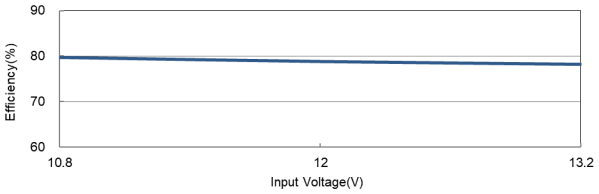


TSM 1212D

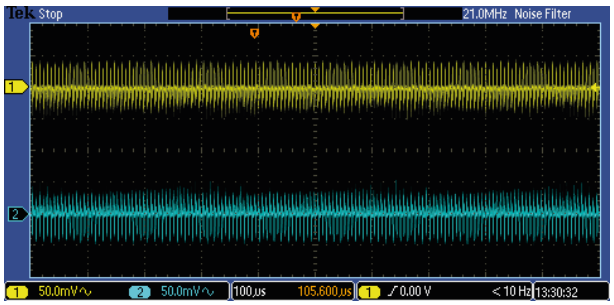
Efficiency versus Output Load



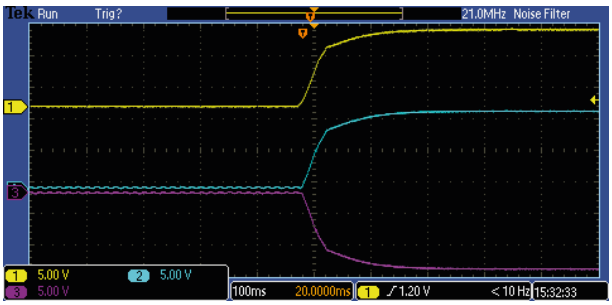
Efficiency versus Input Voltage



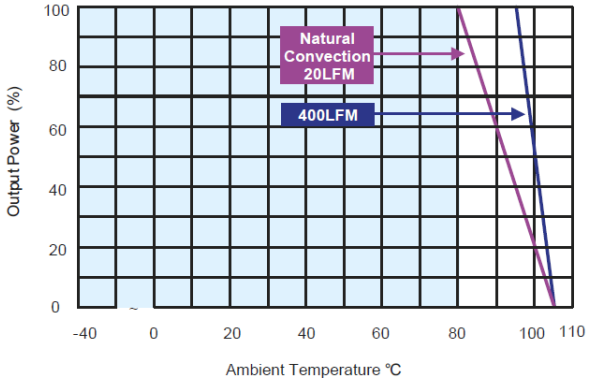
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



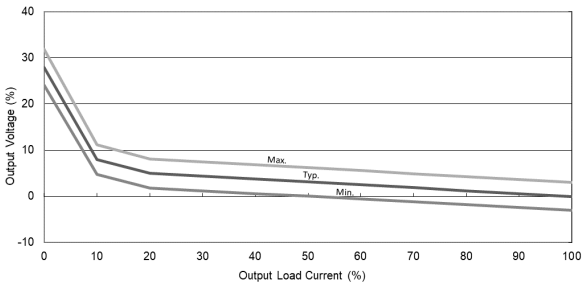
Typical Input Start-Up and Output Rise Characteristic



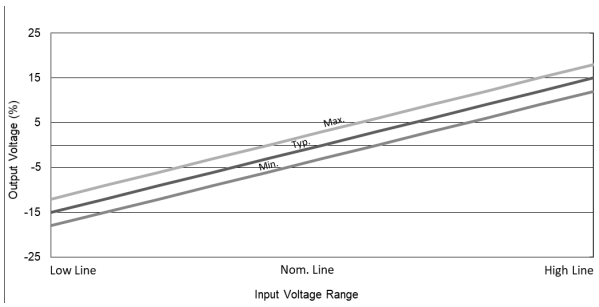
Derating Output versus Ambient Temperature



Load Variation versus Output Voltage

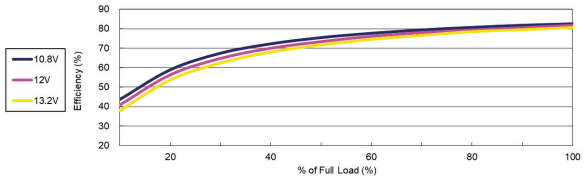


Input Variation versus Output Voltage

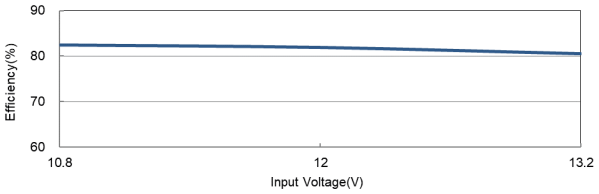


TSM 1215D

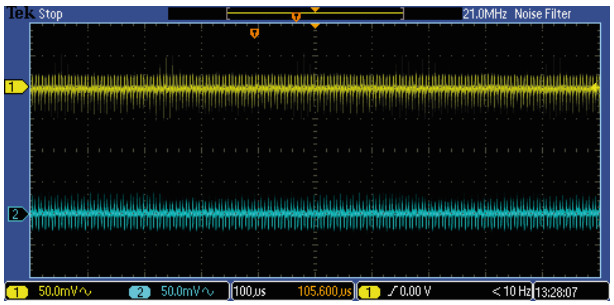
Efficiency versus Output Load



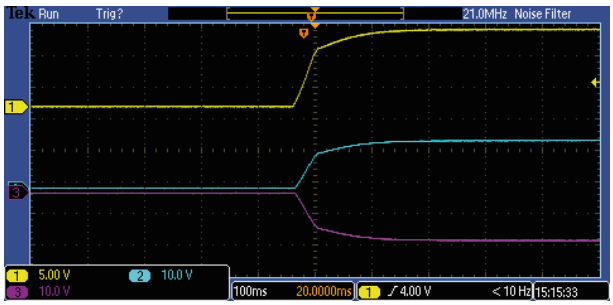
Efficiency versus Input Voltage



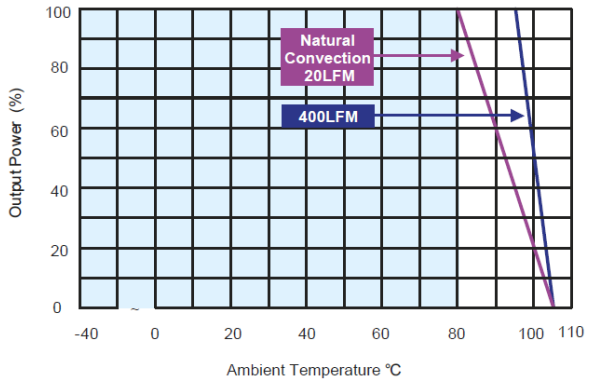
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



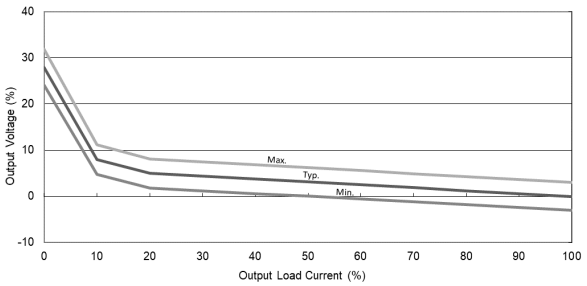
Typical Input Start-Up and Output Rise Characteristic



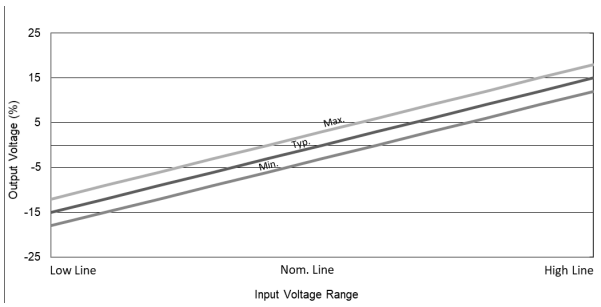
Derating Output versus Ambient Temperature



Load Variation versus Output Voltage

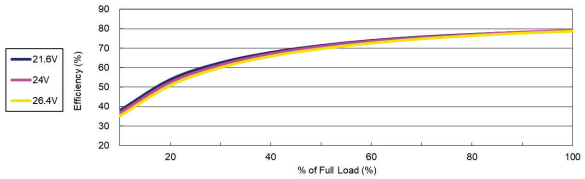


Input Variation versus Output Voltage

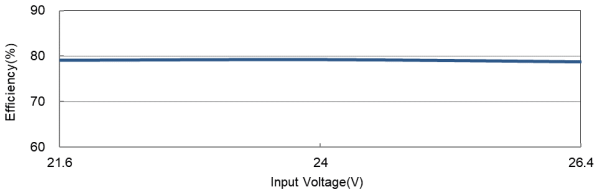


TSM 2405S

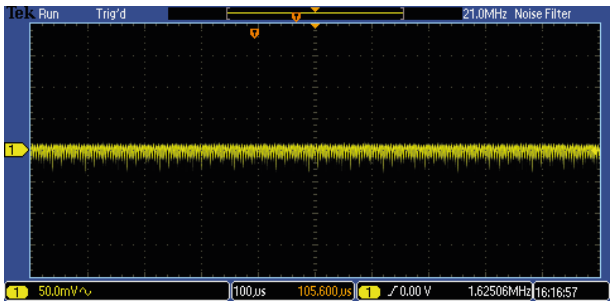
Efficiency versus Output Load



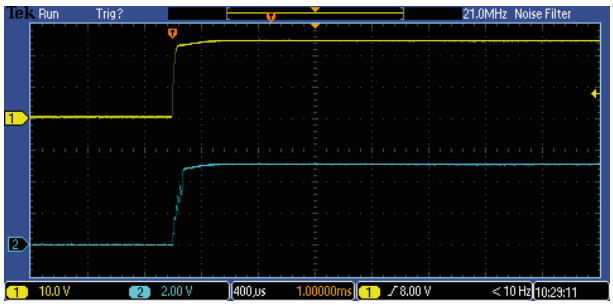
Efficiency versus Input Voltage



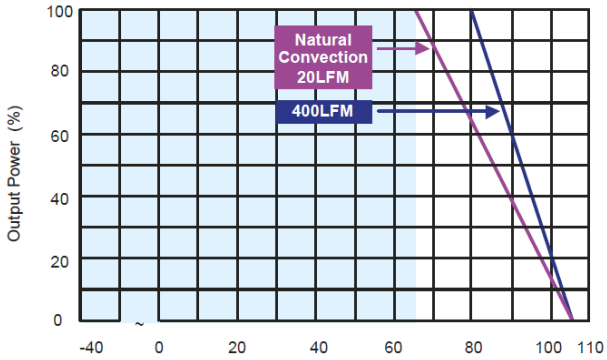
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



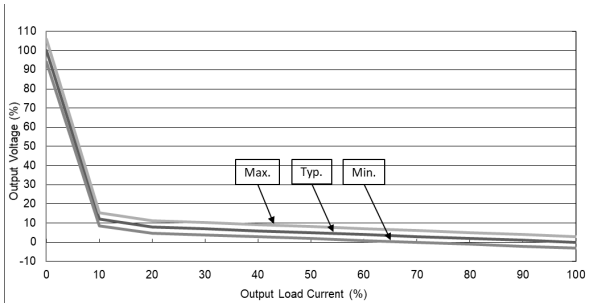
Typical Input Start-Up and Output Rise Characteristic



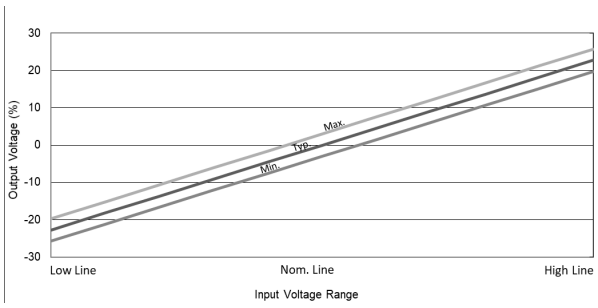
Derating Output versus Ambient Temperature



Load Variation versus Output Voltage

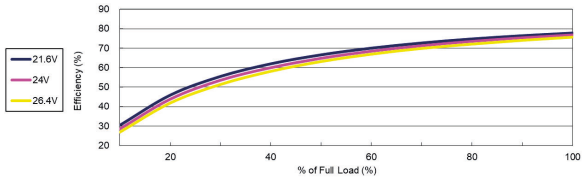


Input Variation versus Output Voltage

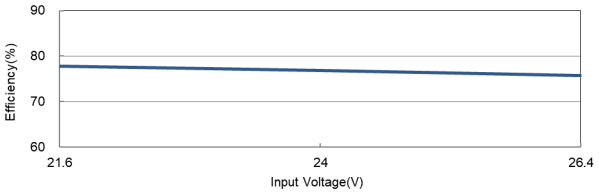


TSM 2409S

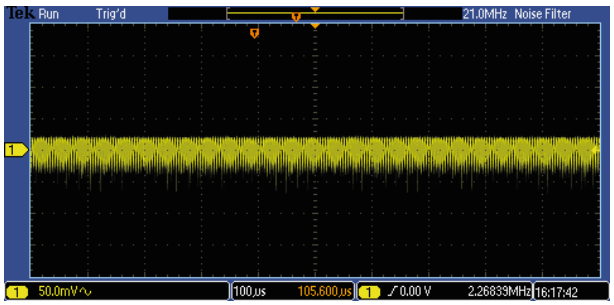
Efficiency versus Output Load



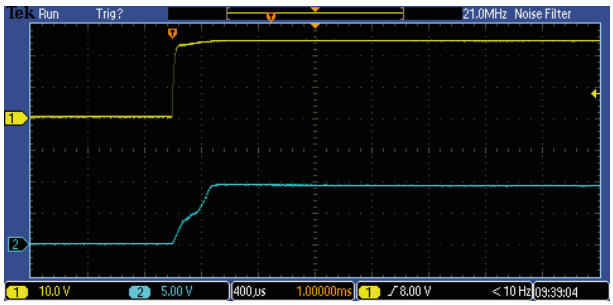
Efficiency versus Input Voltage



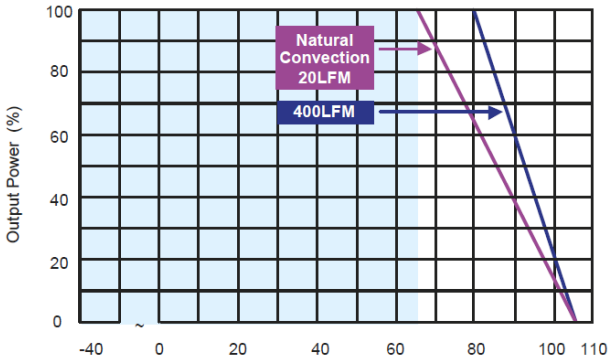
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



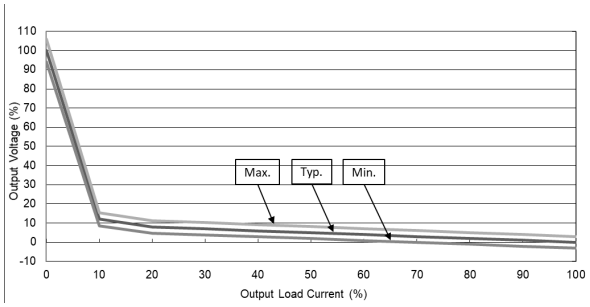
Typical Input Start-Up and Output Rise Characteristic



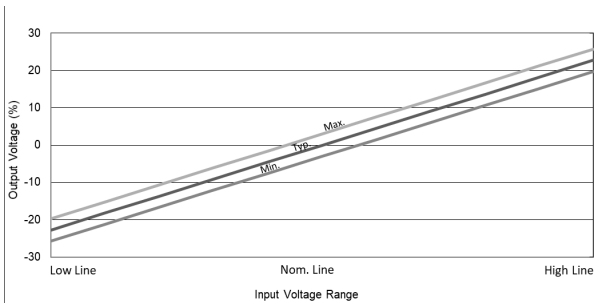
Derating Output versus Ambient Temperature



Load Variation versus Output Voltage

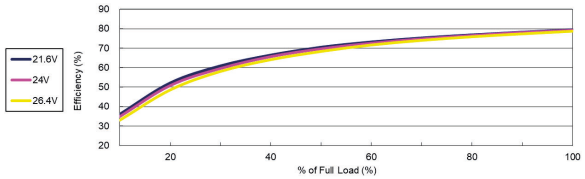


Input Variation versus Output Voltage

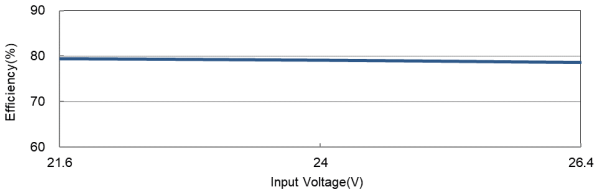


TSM 2412S

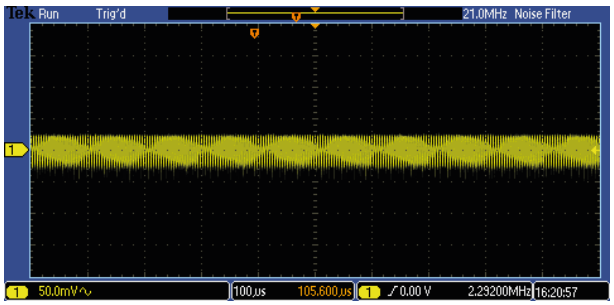
Efficiency versus Output Load



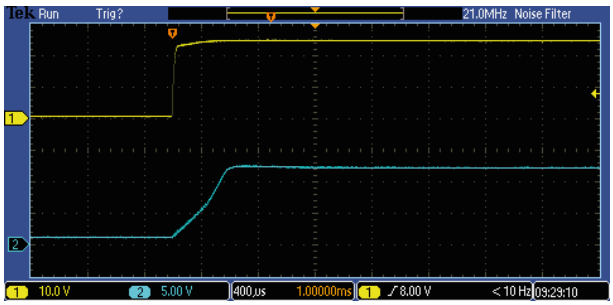
Efficiency versus Input Voltage



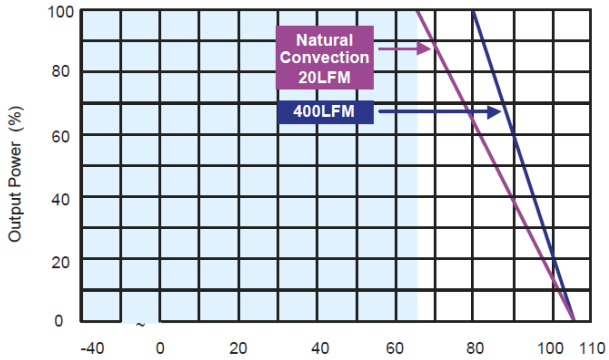
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



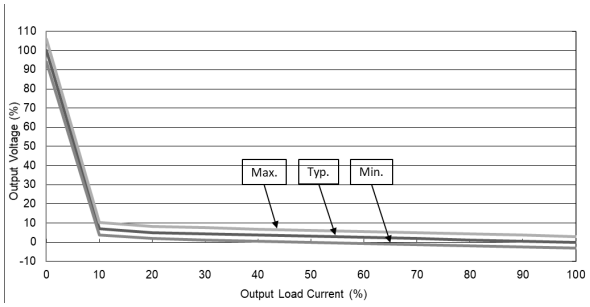
Typical Input Start-Up and Output Rise Characteristic



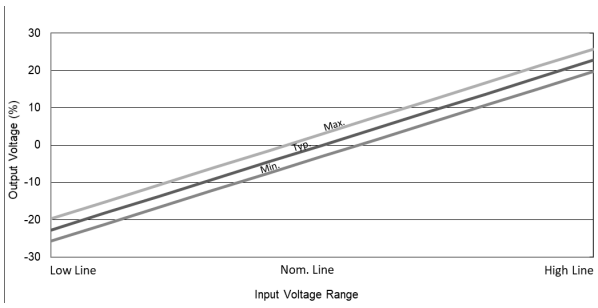
Derating Output versus Ambient Temperature



Load Variation versus Output Voltage

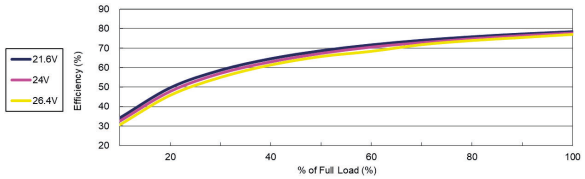


Input Variation versus Output Voltage

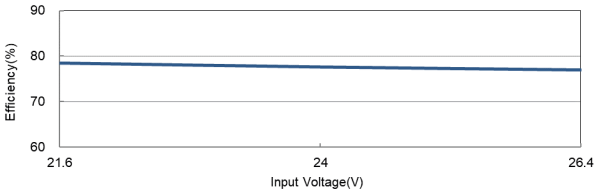


TSM 2415S

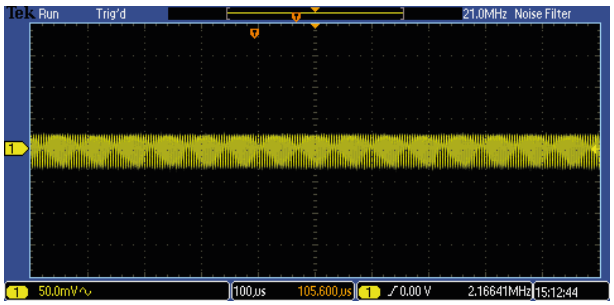
Efficiency versus Output Load



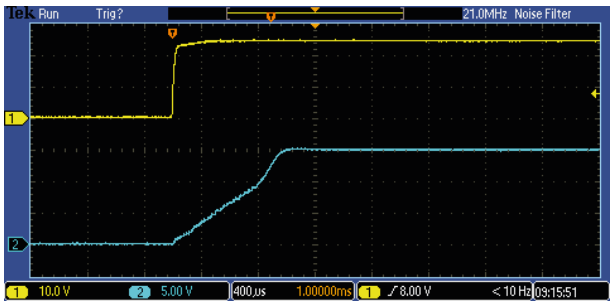
Efficiency versus Input Voltage



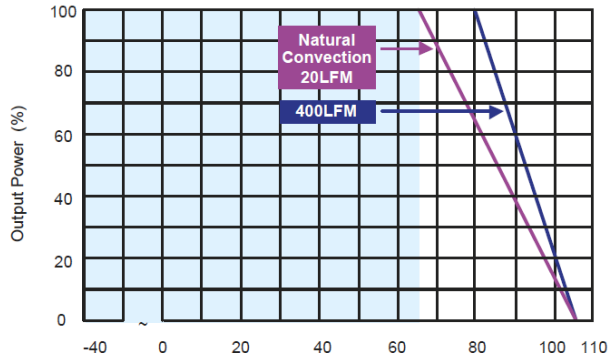
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



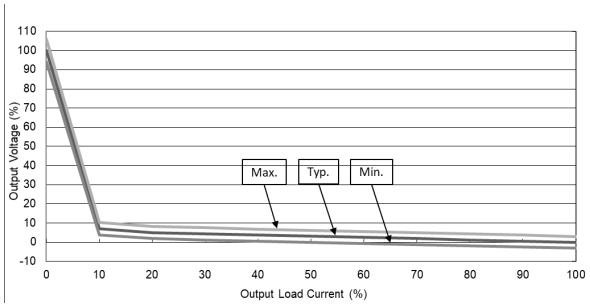
Typical Input Start-Up and Output Rise Characteristic



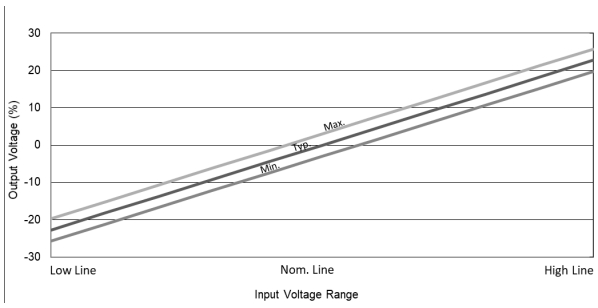
Derating Output versus Ambient Temperature



Load Variation versus Output Voltage

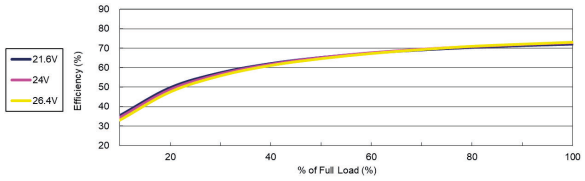


Input Variation versus Output Voltage

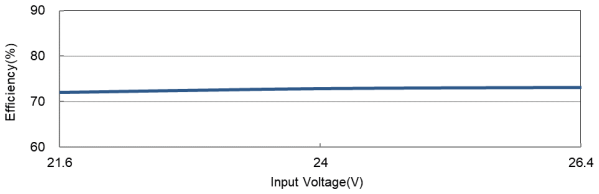


TSM 2405D

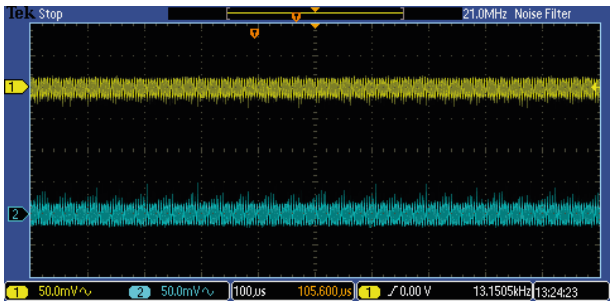
Efficiency versus Output Load



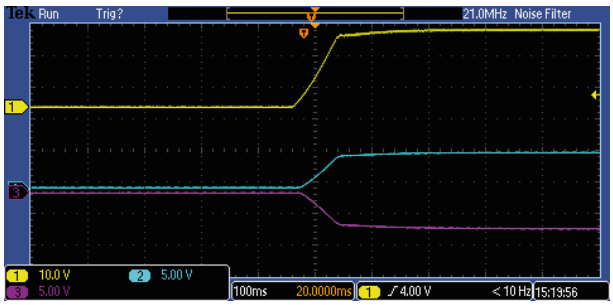
Efficiency versus Input Voltage



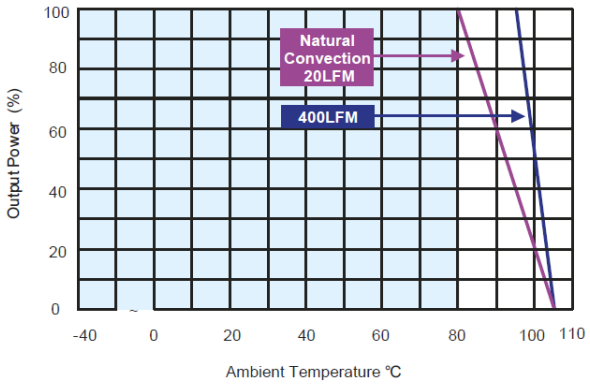
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



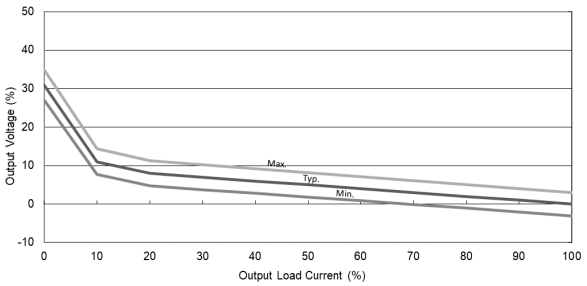
Typical Input Start-Up and Output Rise Characteristic



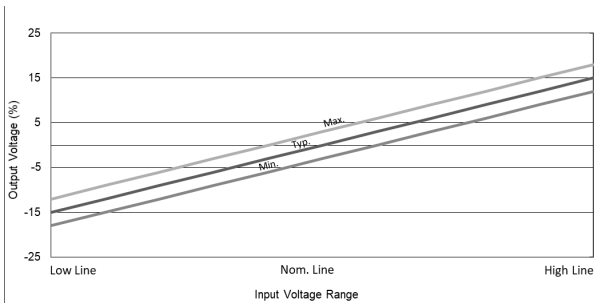
Derating Output versus Ambient Temperature



Load Variation versus Output Voltage

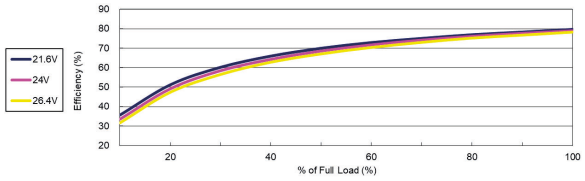


Input Variation versus Output Voltage

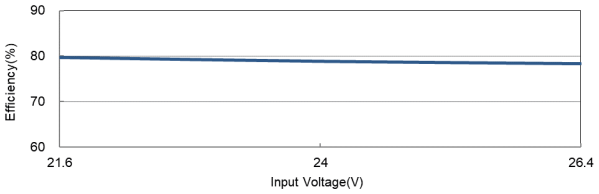


TSM 2412D

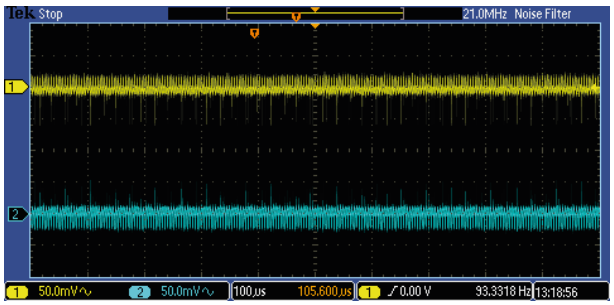
Efficiency versus Output Load



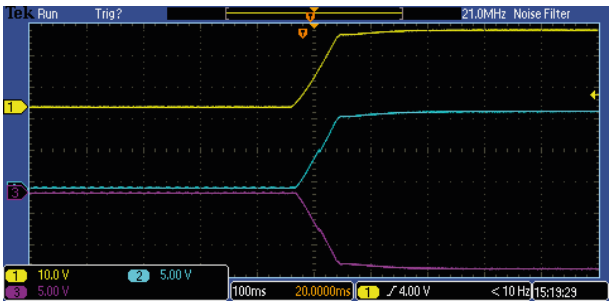
Efficiency versus Input Voltage



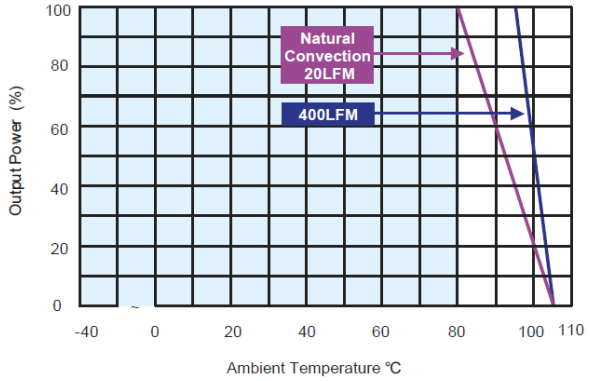
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



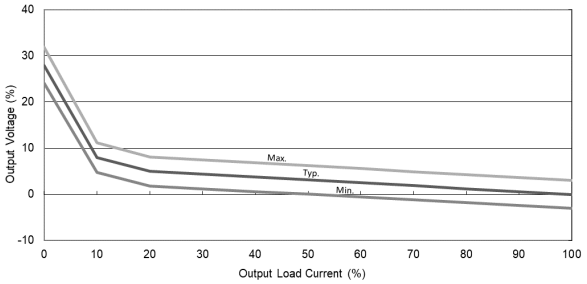
Typical Input Start-Up and Output Rise Characteristic



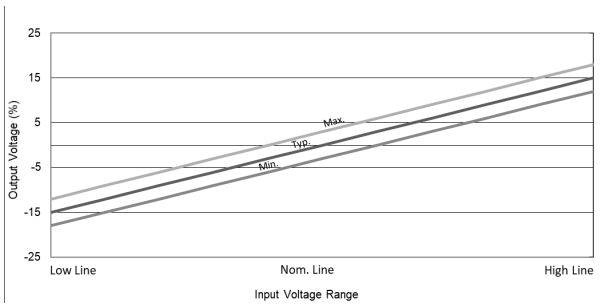
Derating Output versus Ambient Temperature



Load Variation versus Output Voltage

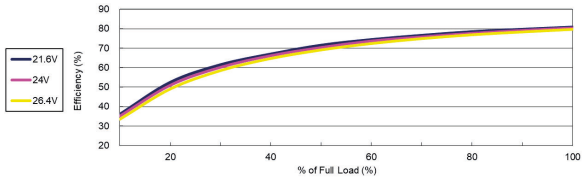


Input Variation versus Output Voltage

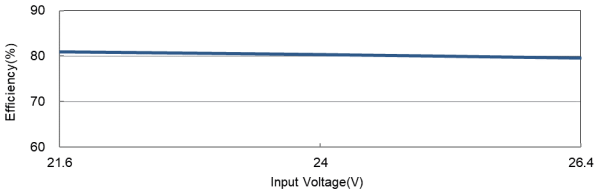


TSM 2415D

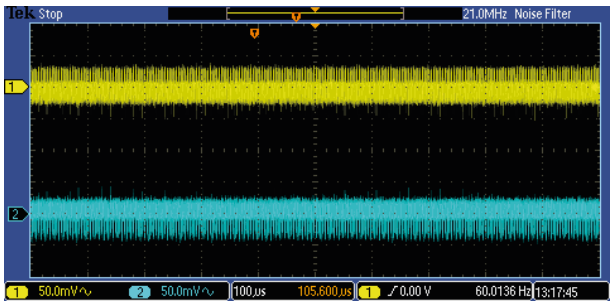
Efficiency versus Output Load



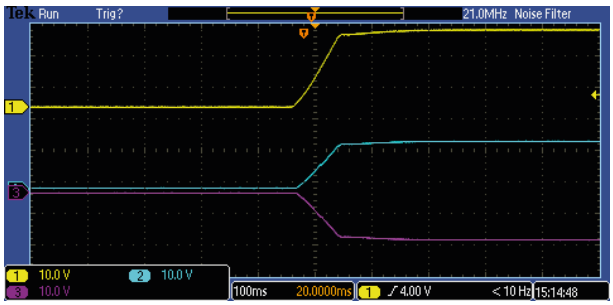
Efficiency versus Input Voltage



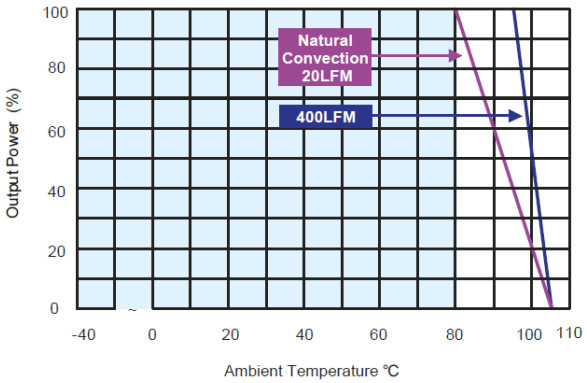
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



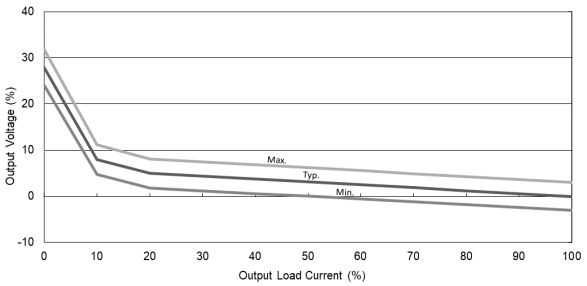
Typical Input Start-Up and Output Rise Characteristic



Derating Output versus Ambient Temperature



Load Variation versus Output Voltage



Input Variation versus Output Voltage