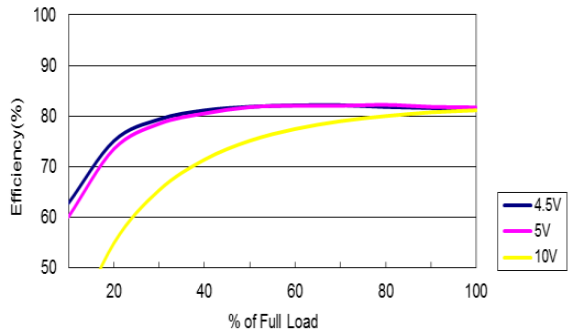


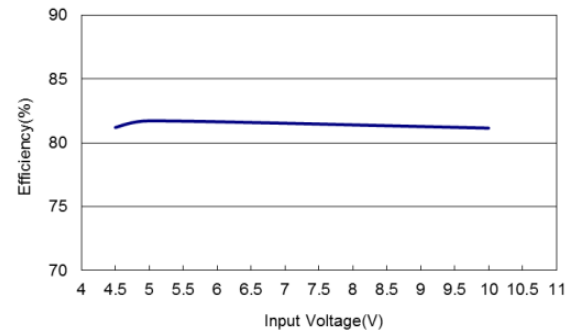
Characteristic Curves

TDL 2-0510

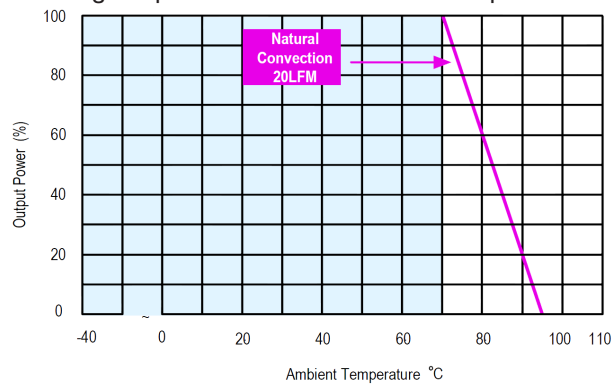
Efficiency versus Output Load



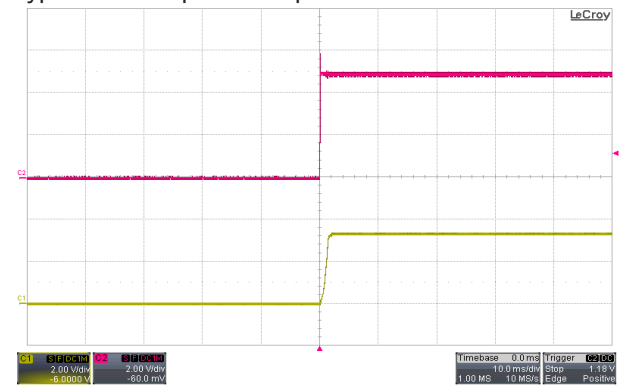
Efficiency versus Input Voltage



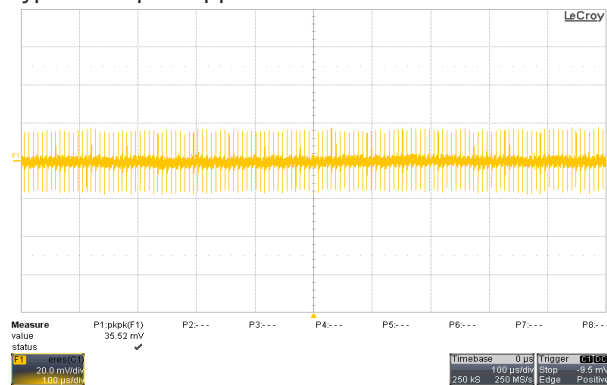
Derating Output Load versus Ambient Temperature



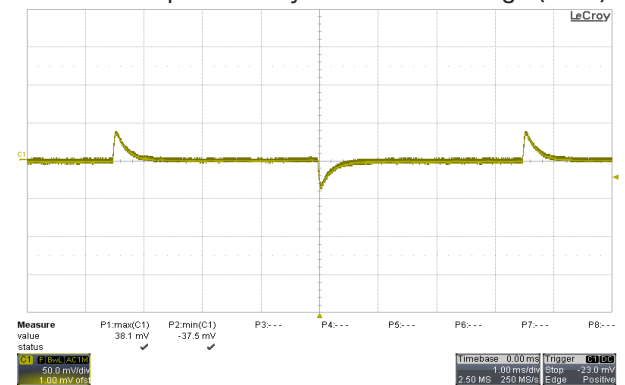
Typical Start-Up and Output Rise Characteristic



Typical Output Ripple and Noise

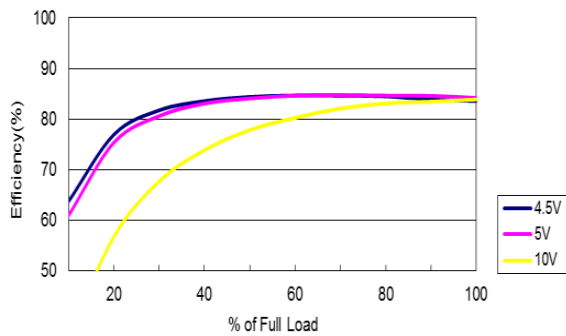


Transient Response to Dynamic Load Change (25%)

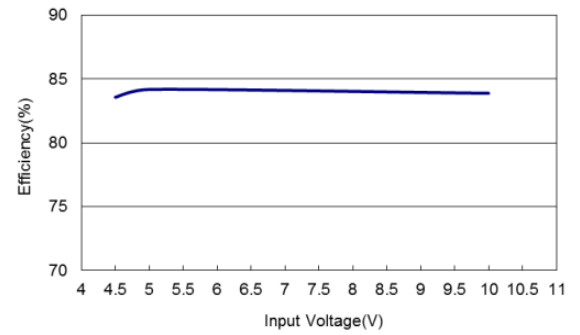


TDL 2-0511

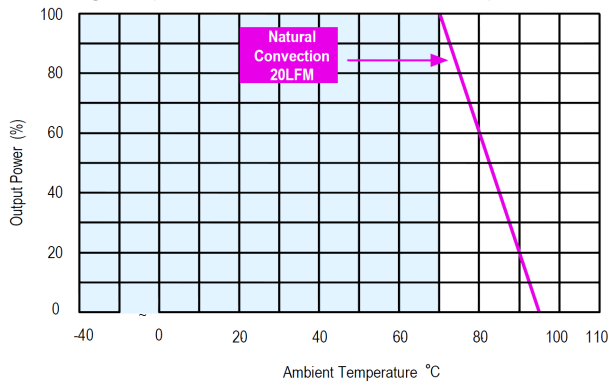
Efficiency versus Output Load



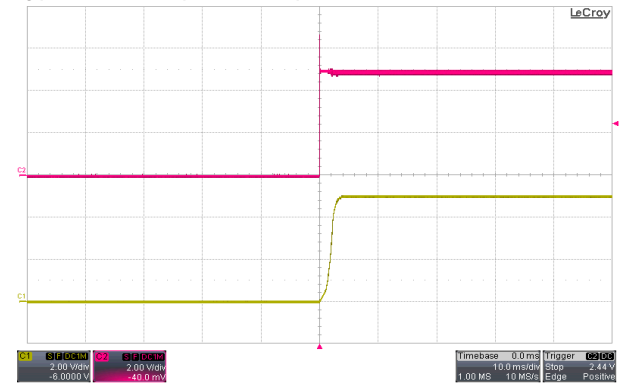
Efficiency versus Input Voltage



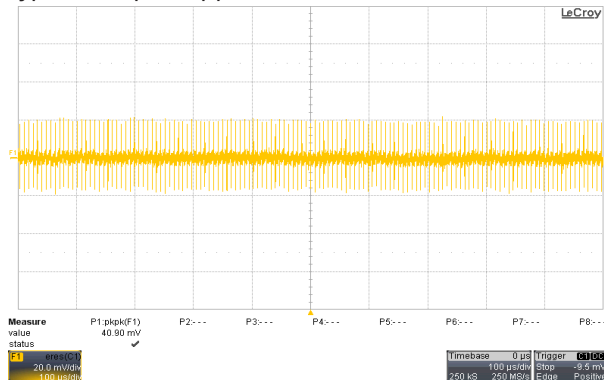
Derating Output Load versus Ambient Temperature



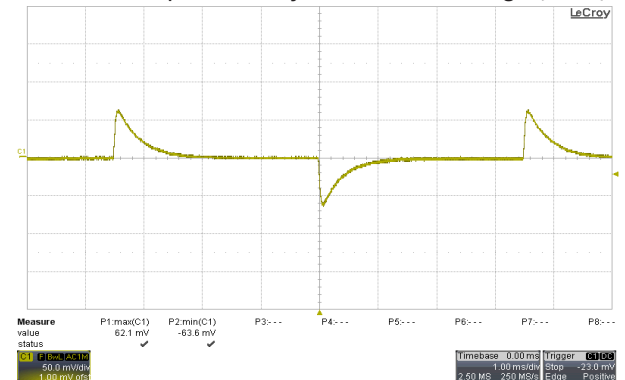
Typical Start-Up and Output Rise Characteristic



Typical Output Ripple and Noise



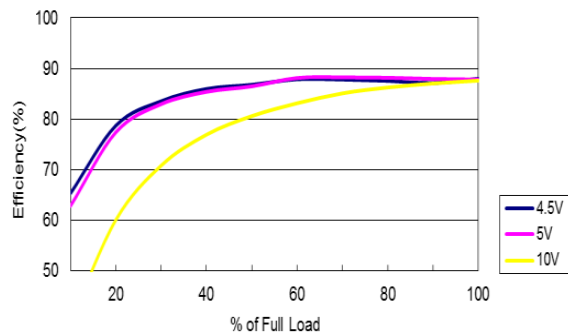
Transient Response to Dynamic Load Change (25%)



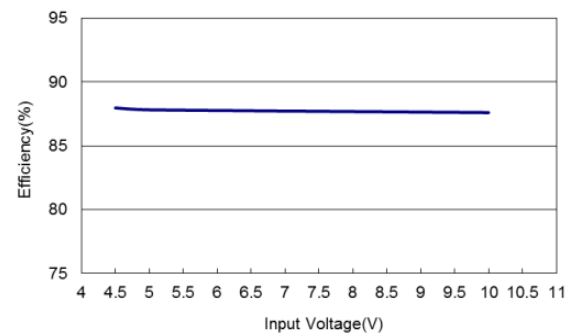
All specifications valid at nominal input voltage, full load and +25°C after warm-up time unless otherwise stated.

TDL 2-0512

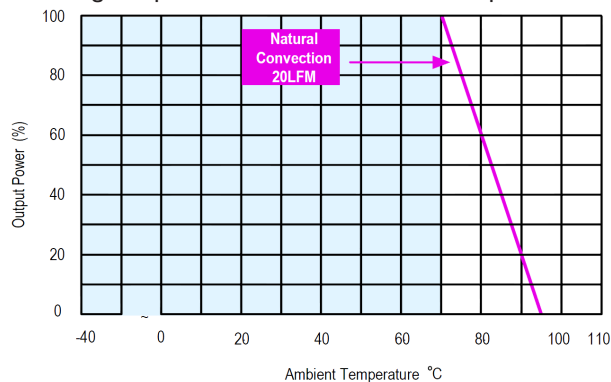
Efficiency versus Output Load



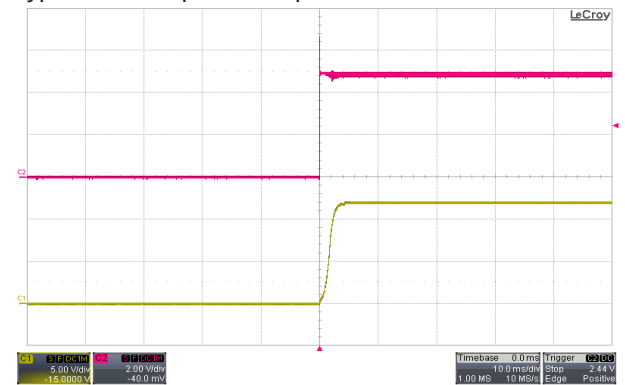
Efficiency versus Input Voltage



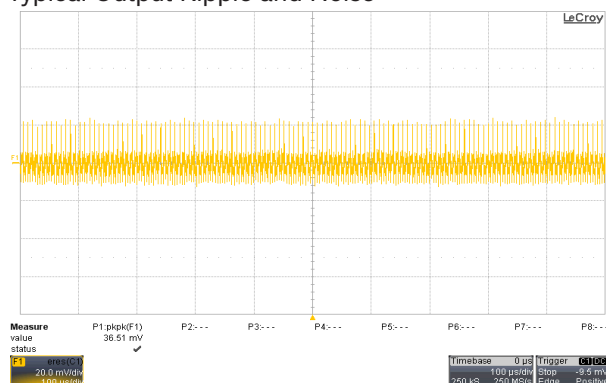
Derating Output Load versus Ambient Temperature



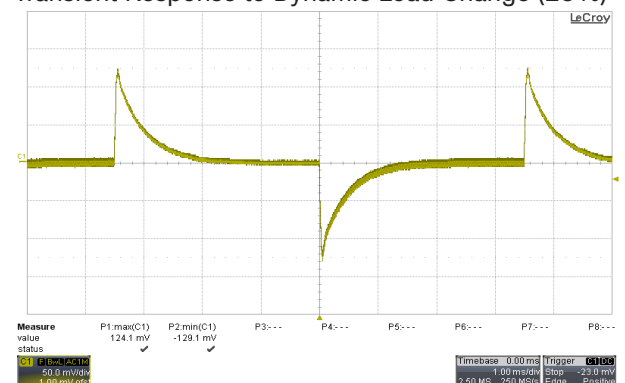
Typical Start-Up and Output Rise Characteristic



Typical Output Ripple and Noise



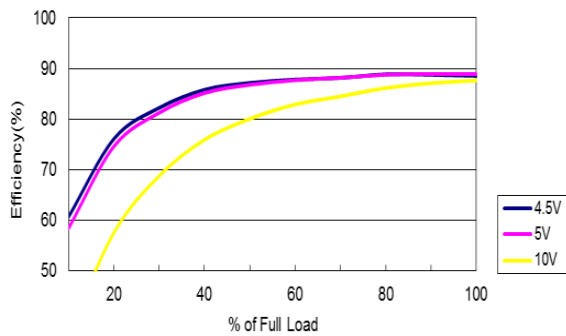
Transient Response to Dynamic Load Change (25%)



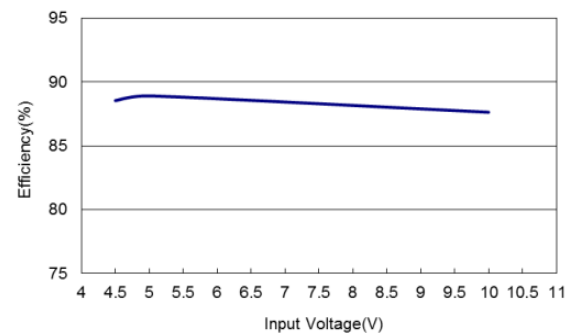
All specifications valid at nominal input voltage, full load and +25°C after warm-up time unless otherwise stated.

TDL 2-0513

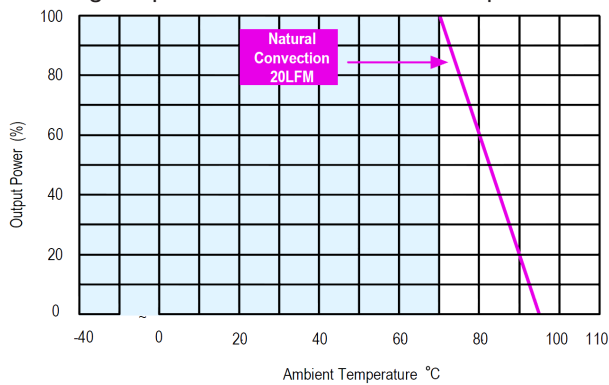
Efficiency versus Output Load



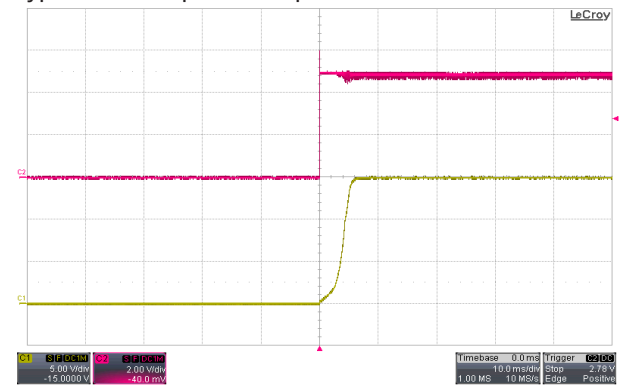
Efficiency versus Input Voltage



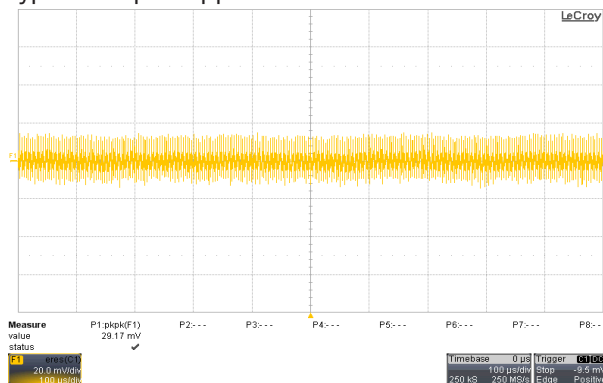
Derating Output Load versus Ambient Temperature



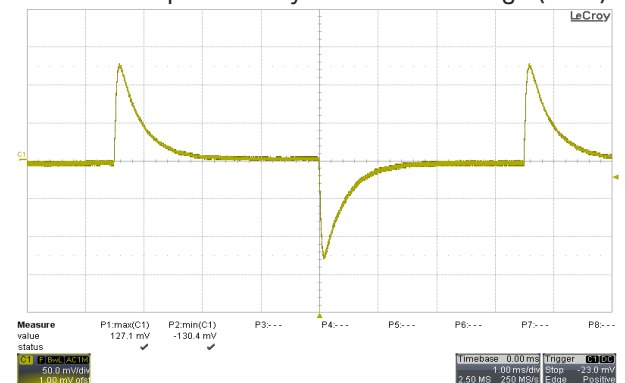
Typical Start-Up and Output Rise Characteristic



Typical Output Ripple and Noise



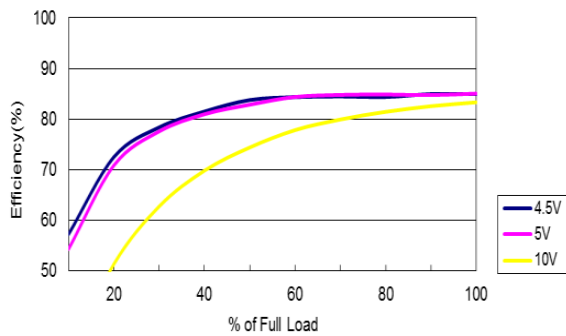
Transient Response to Dynamic Load Change (25%)



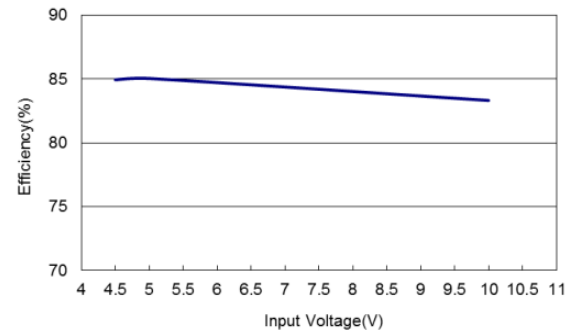
All specifications valid at nominal input voltage, full load and +25°C after warm-up time unless otherwise stated.

TDL 2-0521

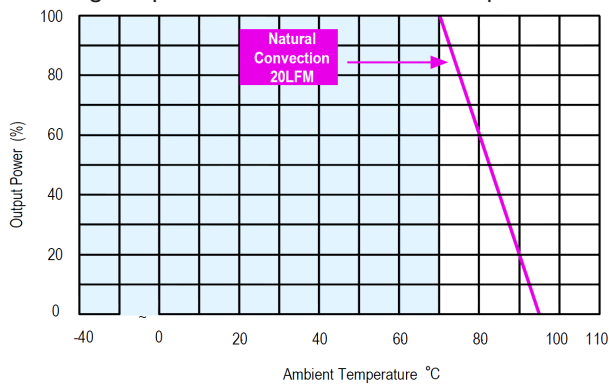
Efficiency versus Output Load



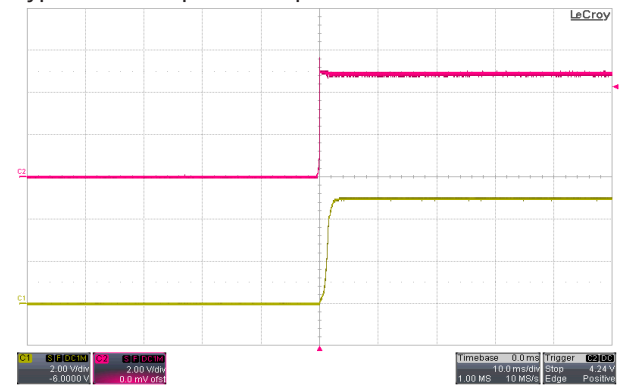
Efficiency versus Input Voltage



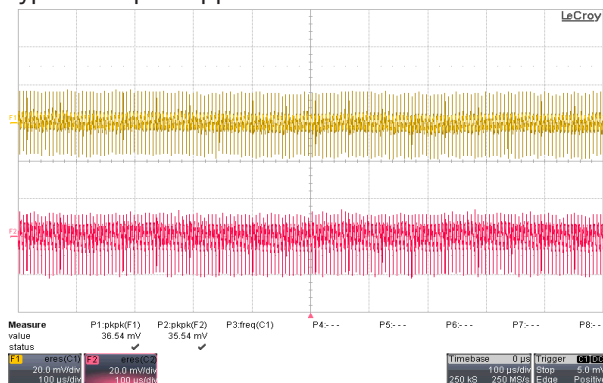
Derating Output Load versus Ambient Temperature



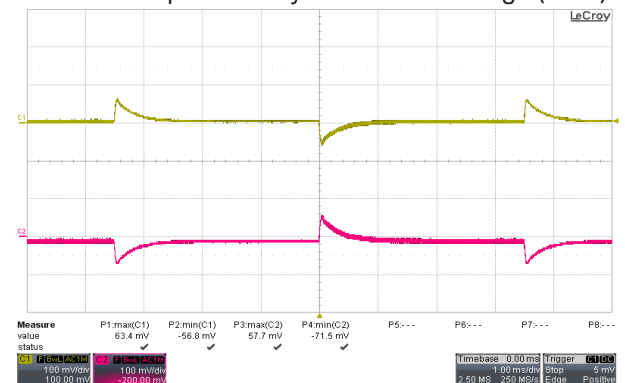
Typical Start-Up and Output Rise Characteristic



Typical Output Ripple and Noise



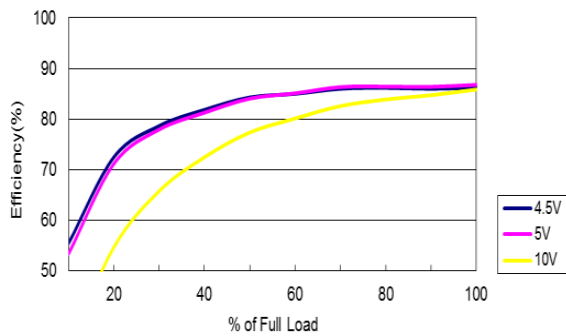
Transient Response to Dynamic Load Change (25%)



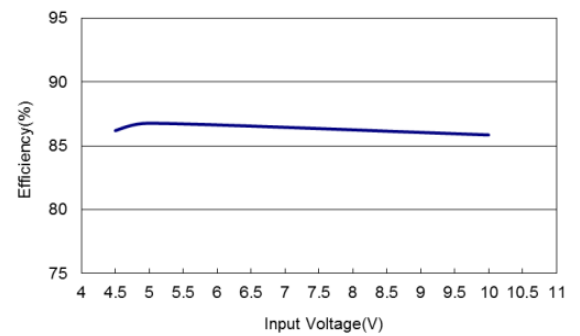
All specifications valid at nominal input voltage, full load and +25°C after warm-up time unless otherwise stated.

TDL 2-0522

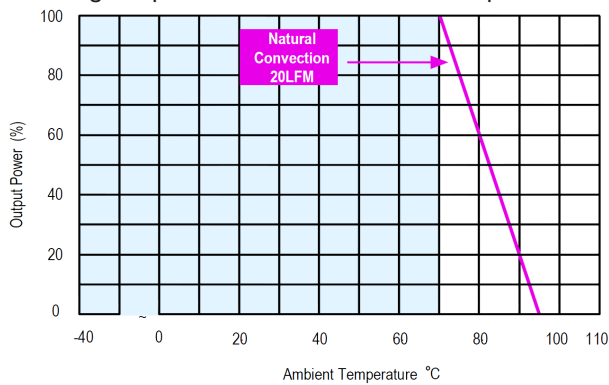
Efficiency versus Output Load



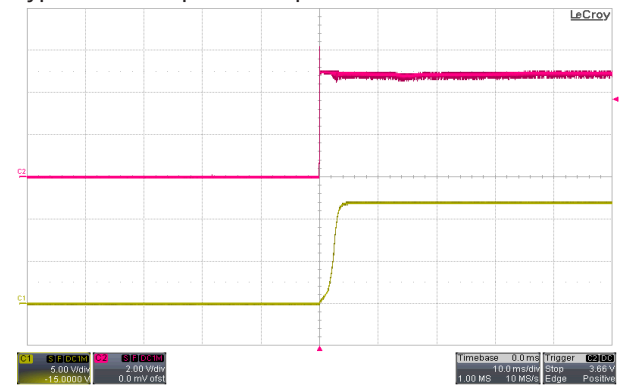
Efficiency versus Input Voltage



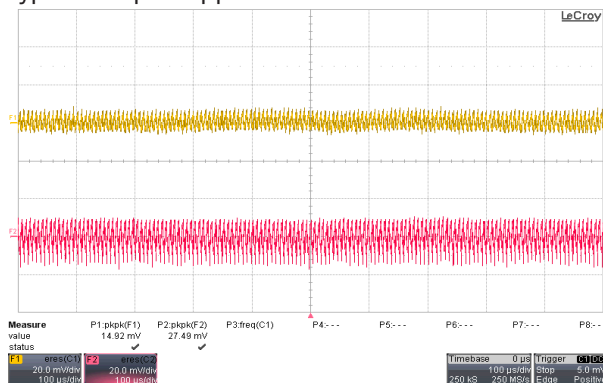
Derating Output Load versus Ambient Temperature



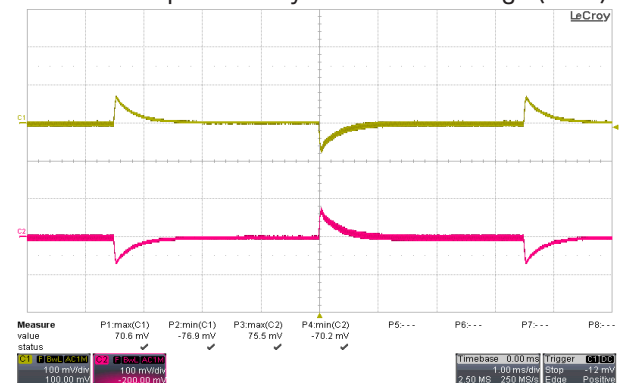
Typical Start-Up and Output Rise Characteristic



Typical Output Ripple and Noise



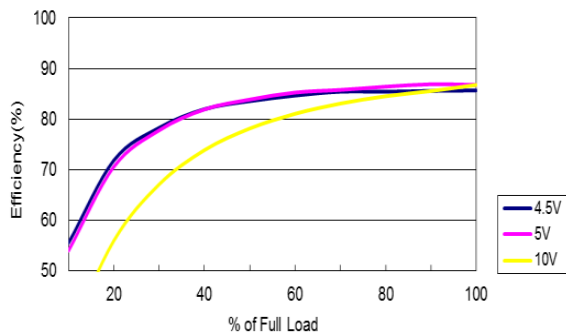
Transient Response to Dynamic Load Change (25%)



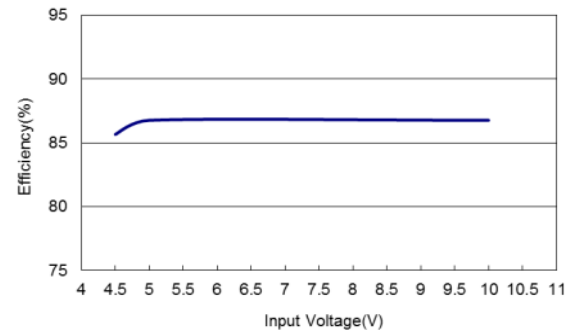
All specifications valid at nominal input voltage, full load and +25°C after warm-up time unless otherwise stated.

TDL 2-0523

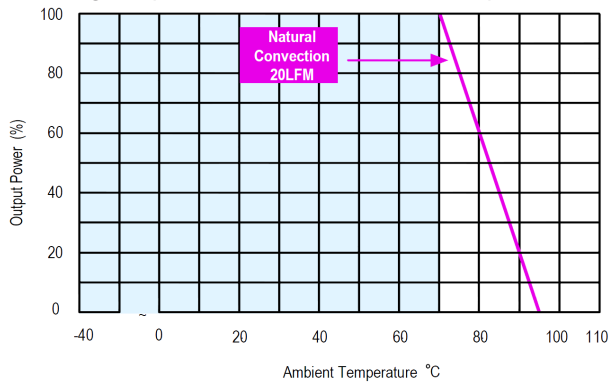
Efficiency versus Output Load



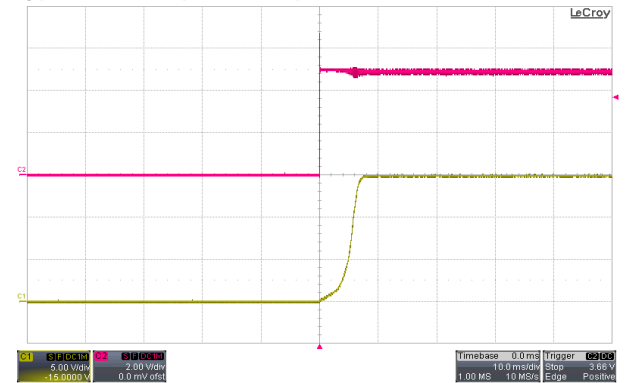
Efficiency versus Input Voltage



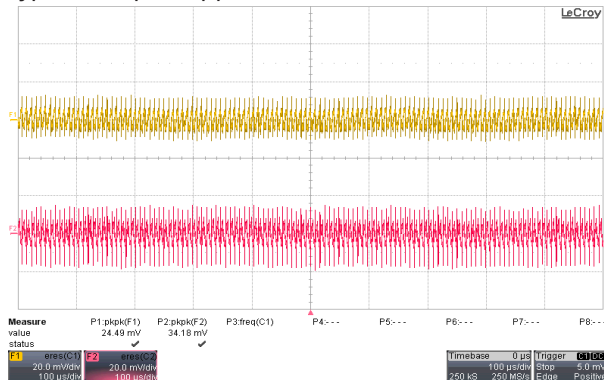
Derating Output Load versus Ambient Temperature



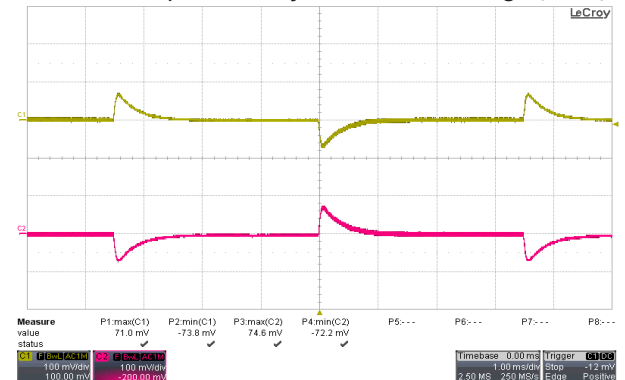
Typical Start-Up and Output Rise Characteristic



Typical Output Ripple and Noise



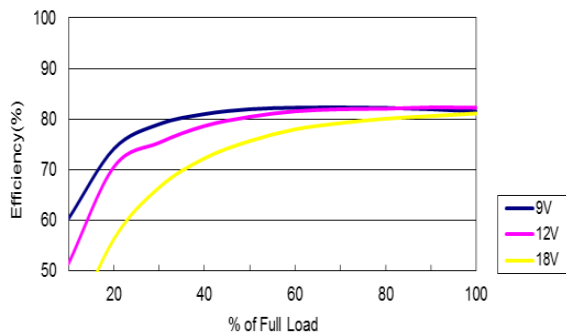
Transient Response to Dynamic Load Change (25%)



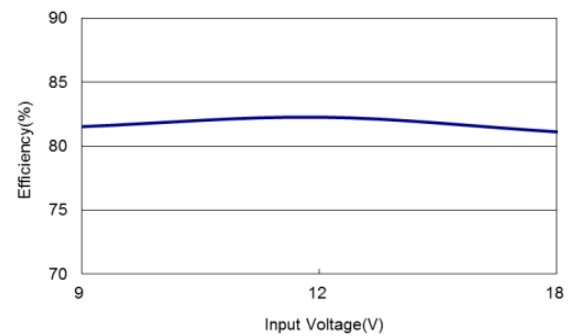
All specifications valid at nominal input voltage, full load and +25°C after warm-up time unless otherwise stated.

TDL 2-1210

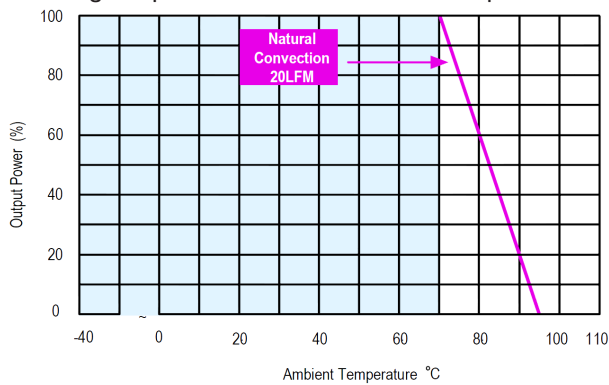
Efficiency versus Output Load



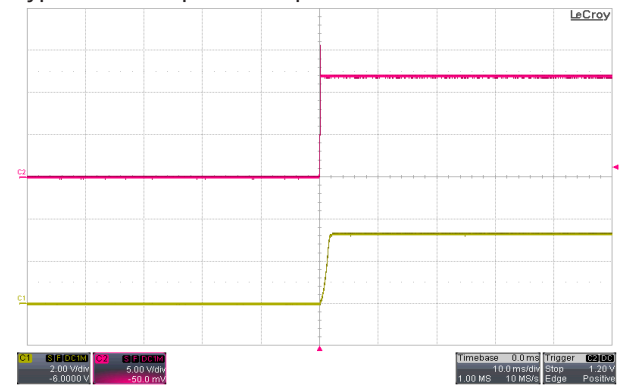
Efficiency versus Input Voltage



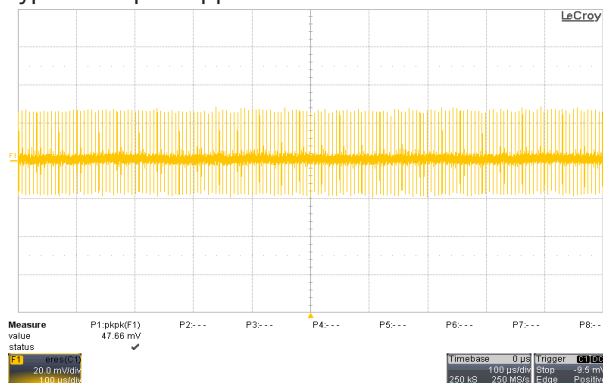
Derating Output Load versus Ambient Temperature



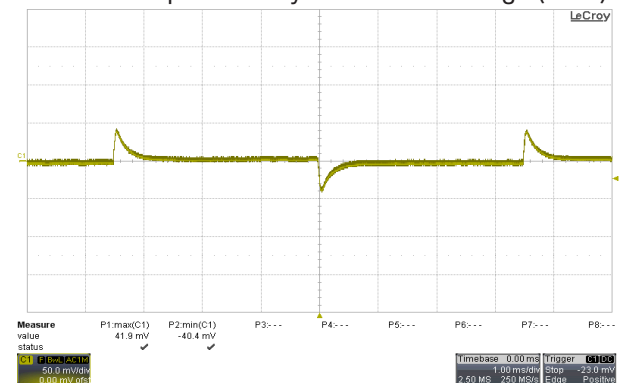
Typical Start-Up and Output Rise Characteristic



Typical Output Ripple and Noise



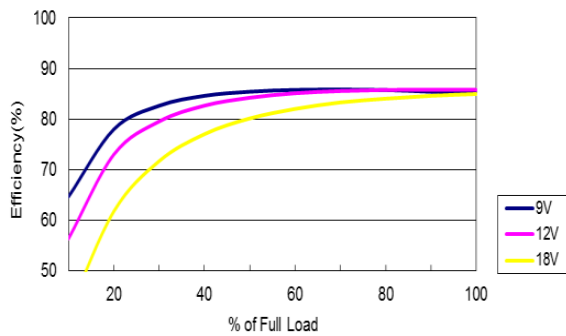
Transient Response to Dynamic Load Change (25%)



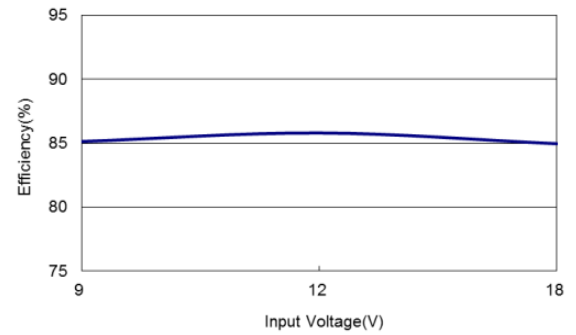
All specifications valid at nominal input voltage, full load and +25°C after warm-up time unless otherwise stated.

TDL 2-1211

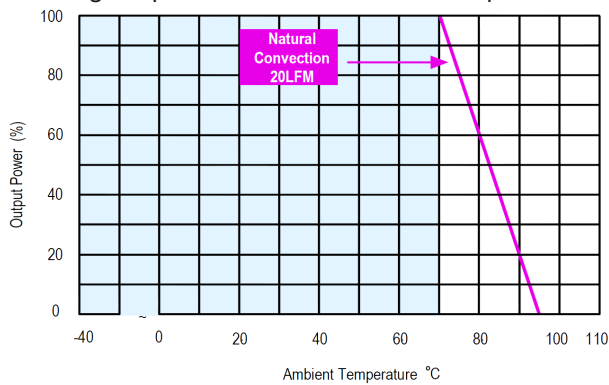
Efficiency versus Output Load



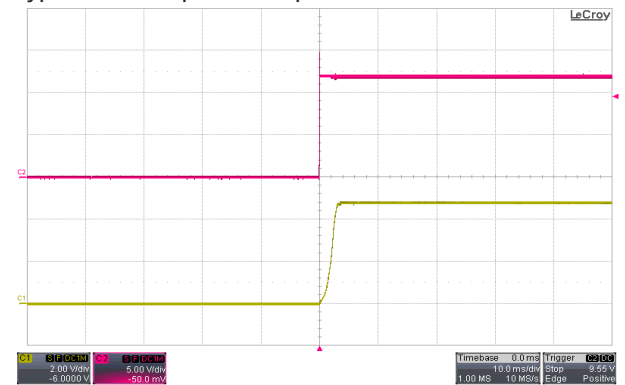
Efficiency versus Input Voltage



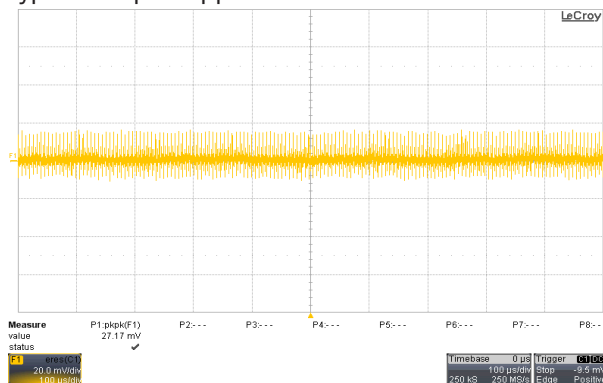
Derating Output Load versus Ambient Temperature



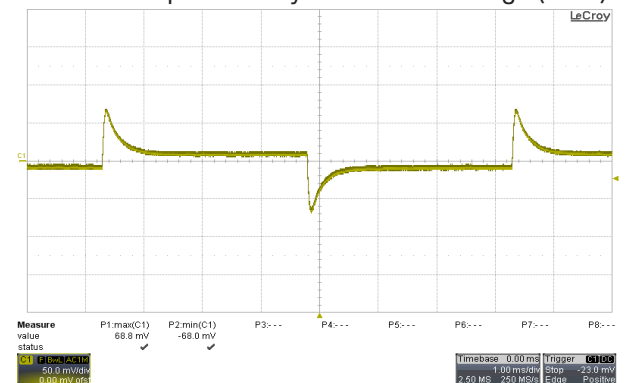
Typical Start-Up and Output Rise Characteristic



Typical Output Ripple and Noise



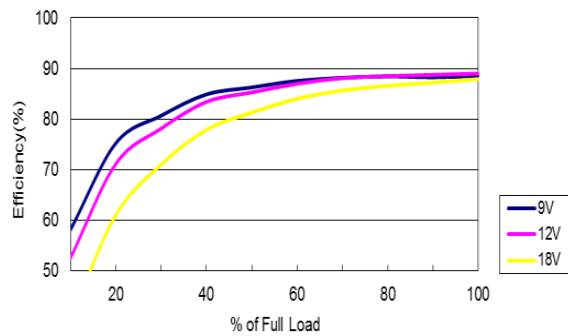
Transient Response to Dynamic Load Change (25%)



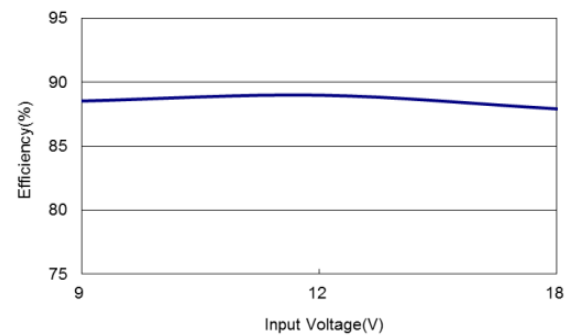
All specifications valid at nominal input voltage, full load and +25°C after warm-up time unless otherwise stated.

TDL 2-1212

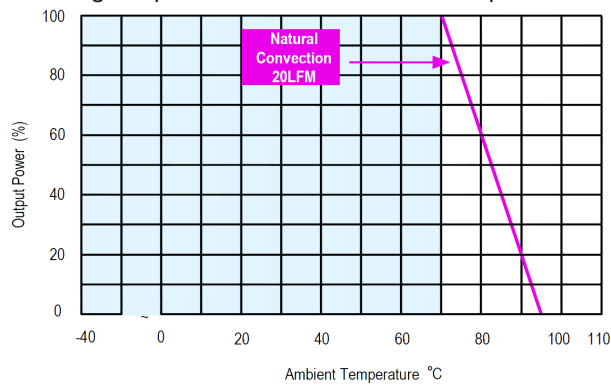
Efficiency versus Output Load



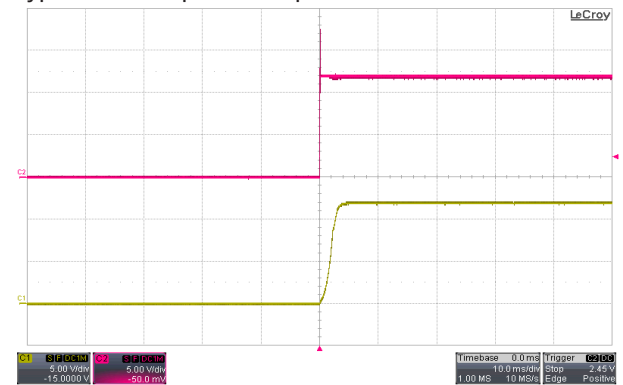
Efficiency versus Input Voltage



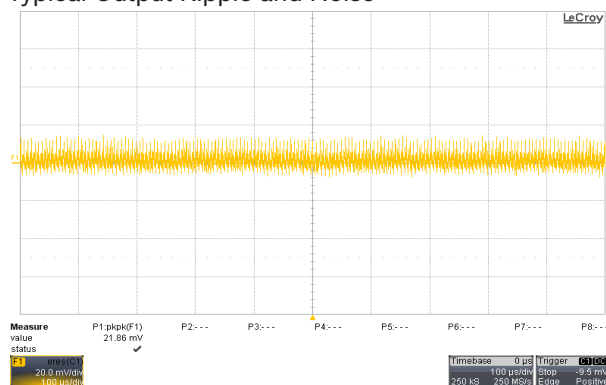
Derating Output Load versus Ambient Temperature



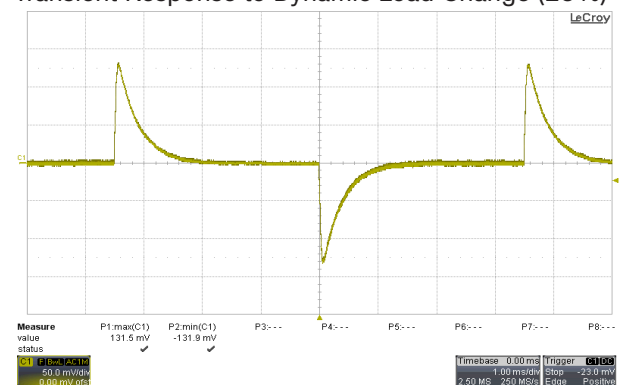
Typical Start-Up and Output Rise Characteristic



Typical Output Ripple and Noise



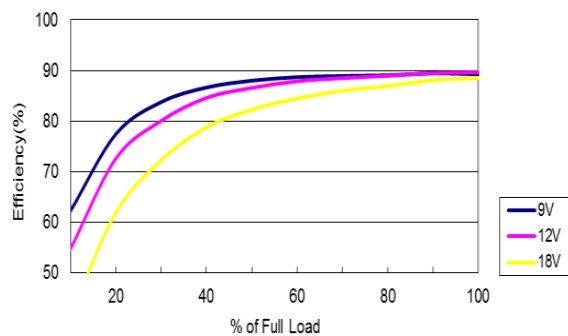
Transient Response to Dynamic Load Change (25%)



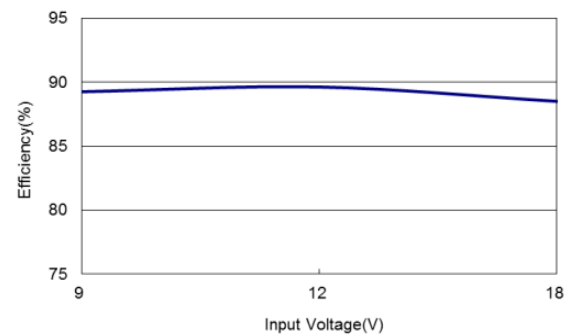
All specifications valid at nominal input voltage, full load and +25°C after warm-up time unless otherwise stated.

TDL 2-1213

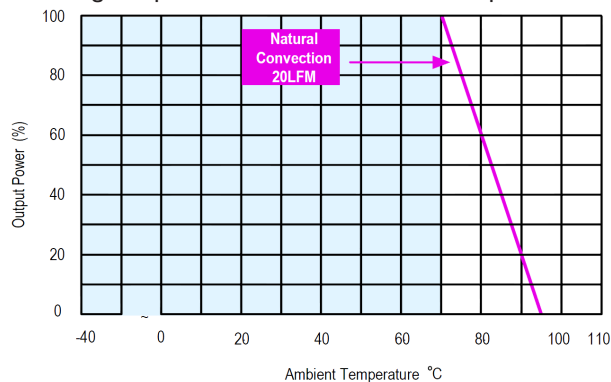
Efficiency versus Output Load



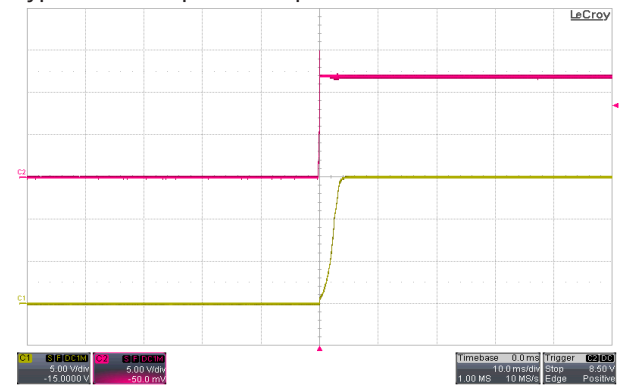
Efficiency versus Input Voltage



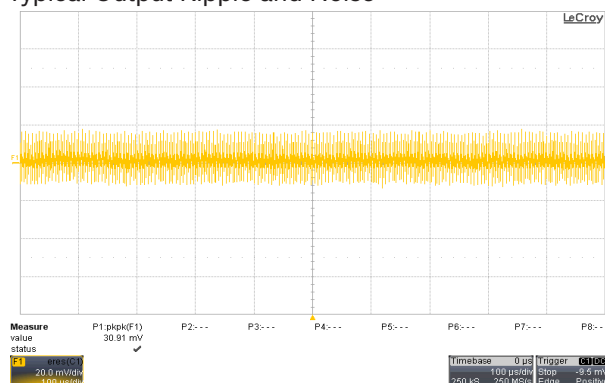
Derating Output Load versus Ambient Temperature



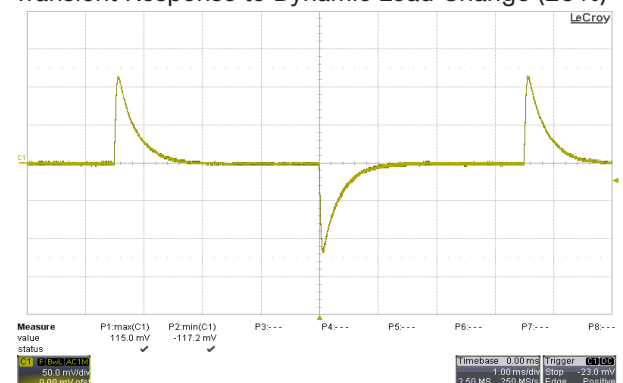
Typical Start-Up and Output Rise Characteristic



Typical Output Ripple and Noise



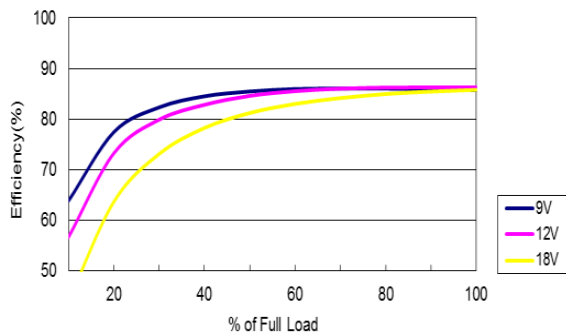
Transient Response to Dynamic Load Change (25%)



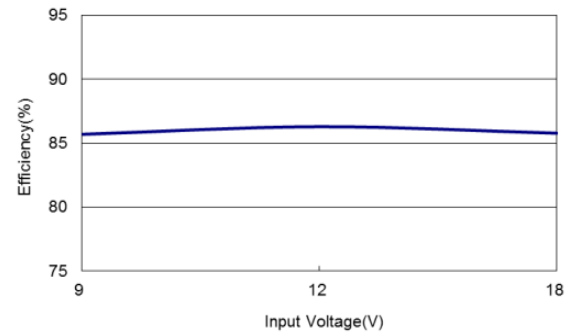
All specifications valid at nominal input voltage, full load and +25°C after warm-up time unless otherwise stated.

TDL 2-1221

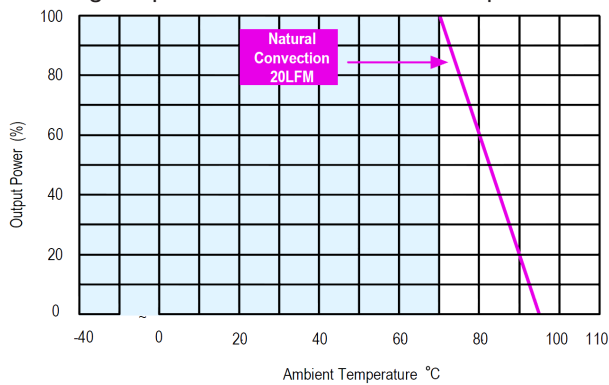
Efficiency versus Output Load



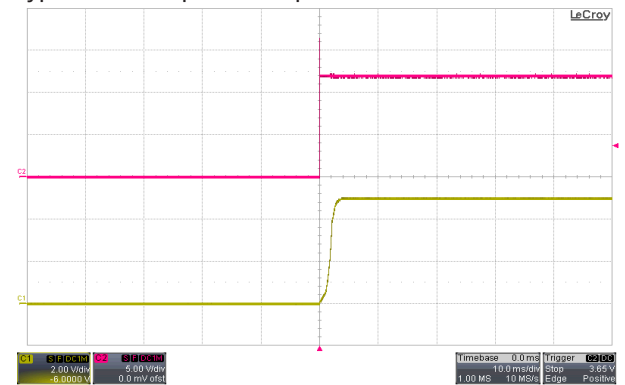
Efficiency versus Input Voltage



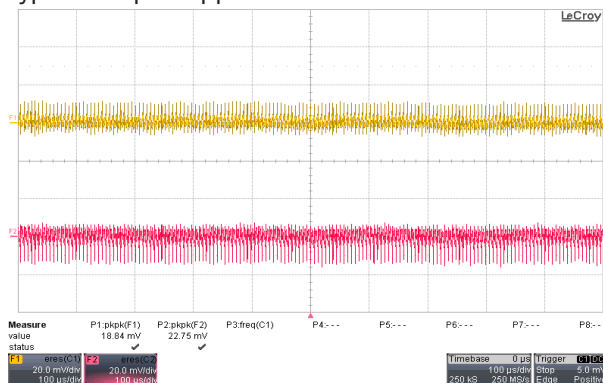
Derating Output Load versus Ambient Temperature



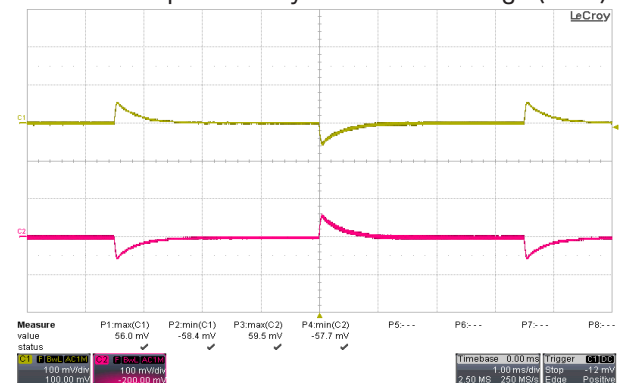
Typical Start-Up and Output Rise Characteristic



Typical Output Ripple and Noise



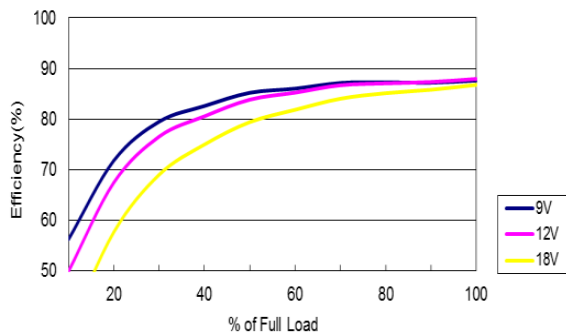
Transient Response to Dynamic Load Change (25%)



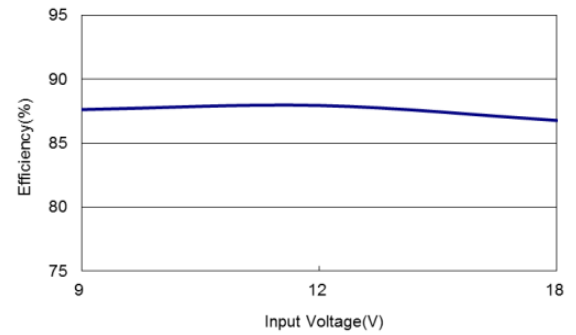
All specifications valid at nominal input voltage, full load and +25°C after warm-up time unless otherwise stated.

TDL 2-1222

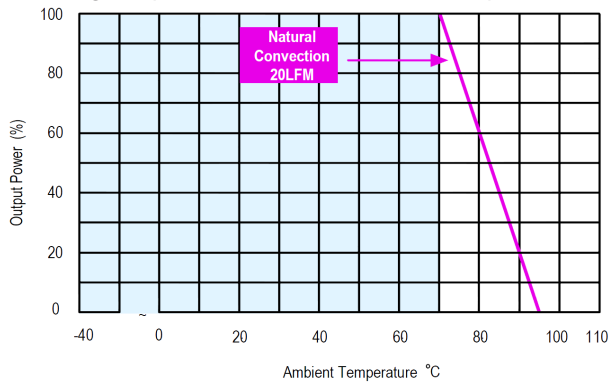
Efficiency versus Output Load



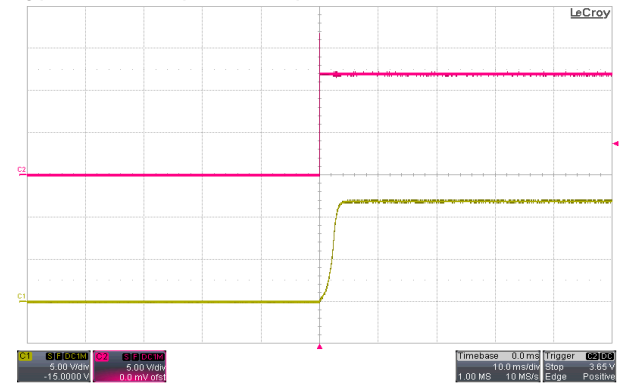
Efficiency versus Input Voltage



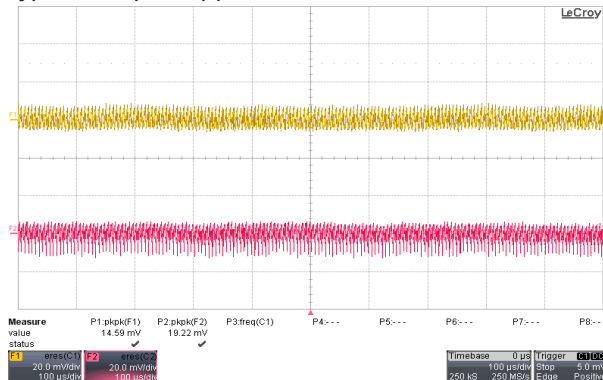
Derating Output Load versus Ambient Temperature



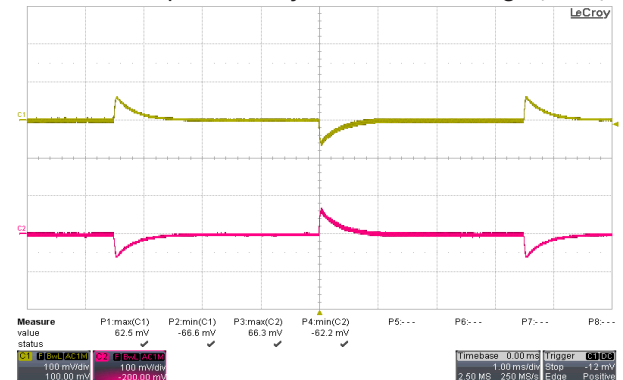
Typical Start-Up and Output Rise Characteristic



Typical Output Ripple and Noise



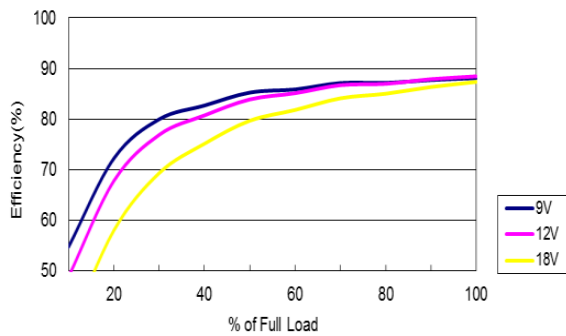
Transient Response to Dynamic Load Change (25%)



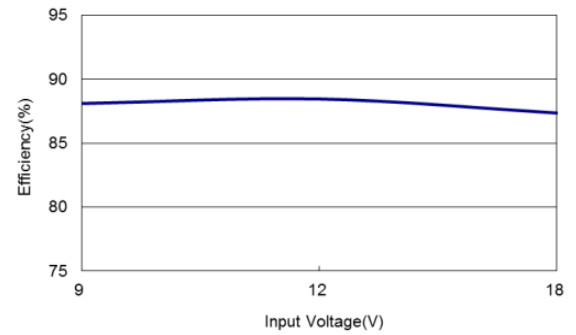
All specifications valid at nominal input voltage, full load and +25°C after warm-up time unless otherwise stated.

TDL 2-1223

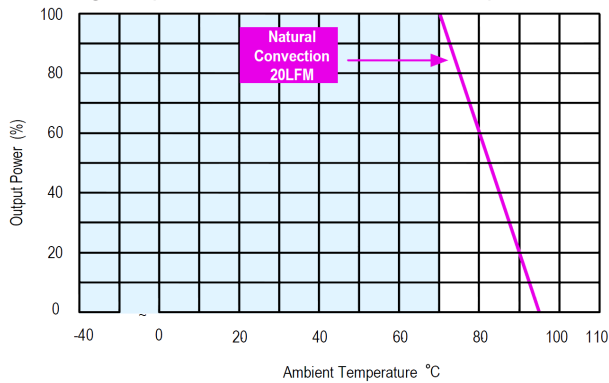
Efficiency versus Output Load



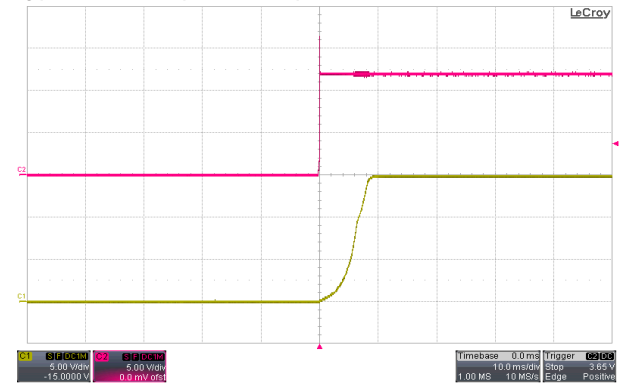
Efficiency versus Input Voltage



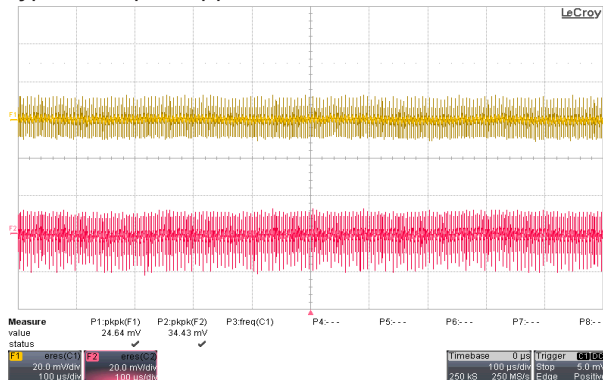
Derating Output Load versus Ambient Temperature



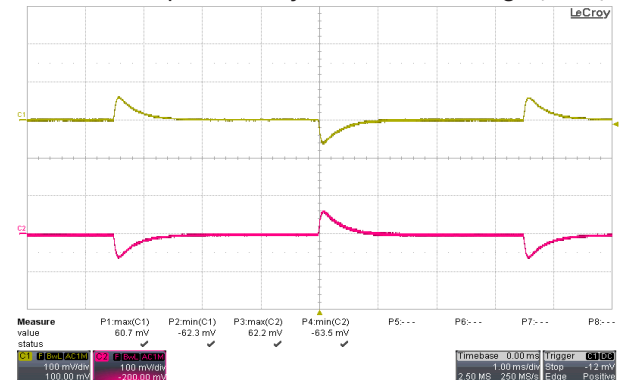
Typical Start-Up and Output Rise Characteristic



Typical Output Ripple and Noise



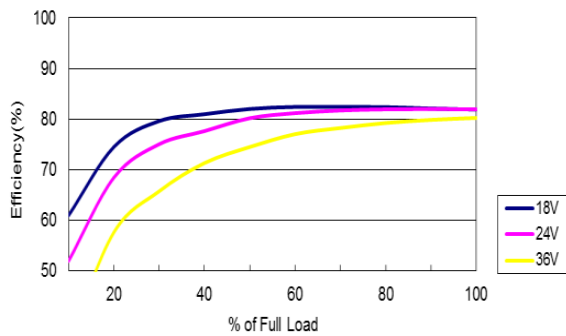
Transient Response to Dynamic Load Change (25%)



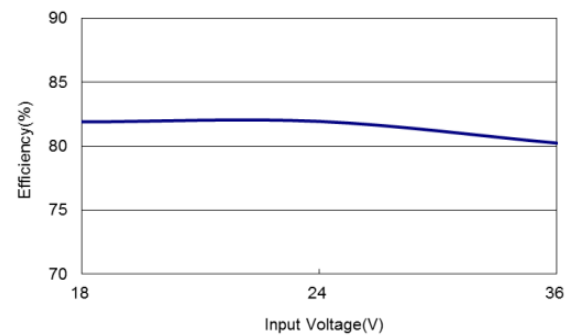
All specifications valid at nominal input voltage, full load and +25°C after warm-up time unless otherwise stated.

TDL 2-2410

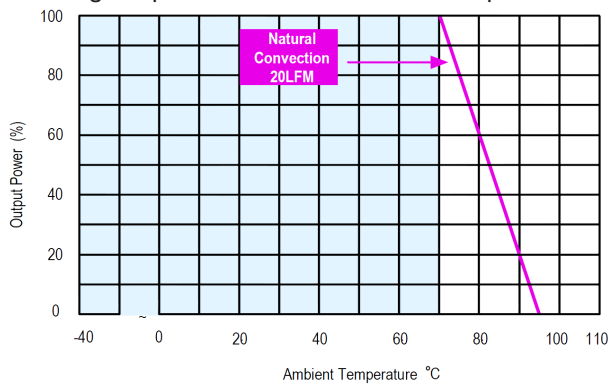
Efficiency versus Output Load



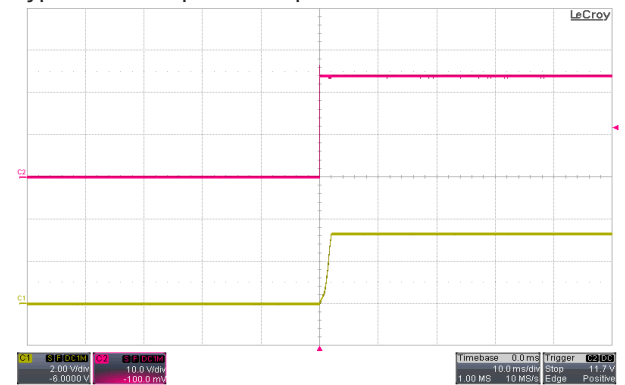
Efficiency versus Input Voltage



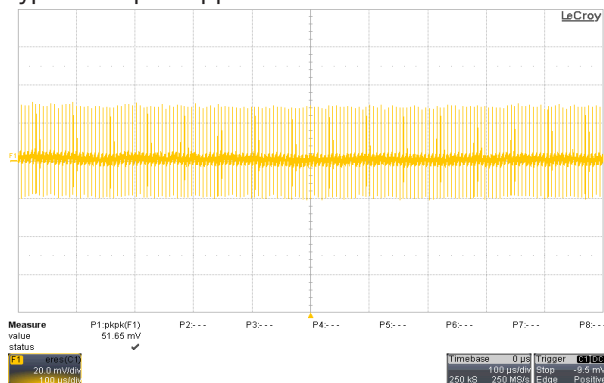
Derating Output Load versus Ambient Temperature



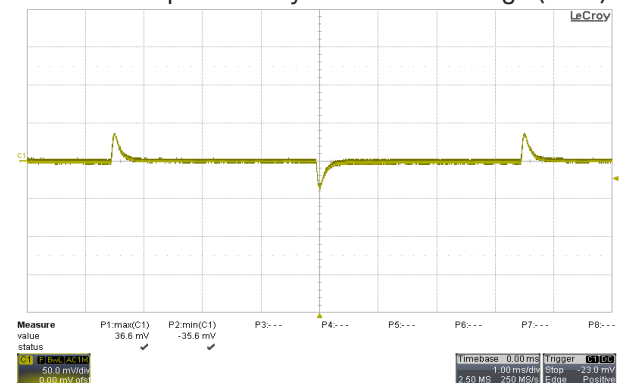
Typical Start-Up and Output Rise Characteristic



Typical Output Ripple and Noise



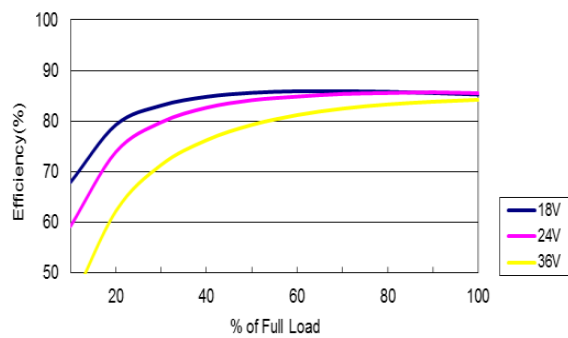
Transient Response to Dynamic Load Change (25%)



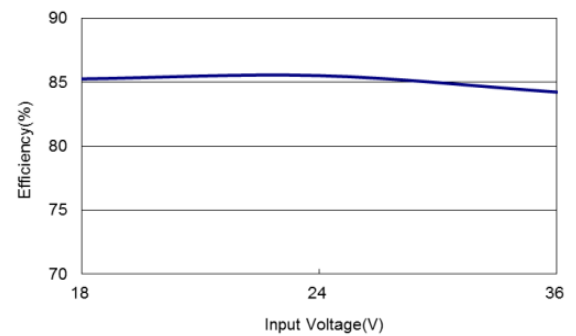
All specifications valid at nominal input voltage, full load and +25°C after warm-up time unless otherwise stated.

TDL 2-2411

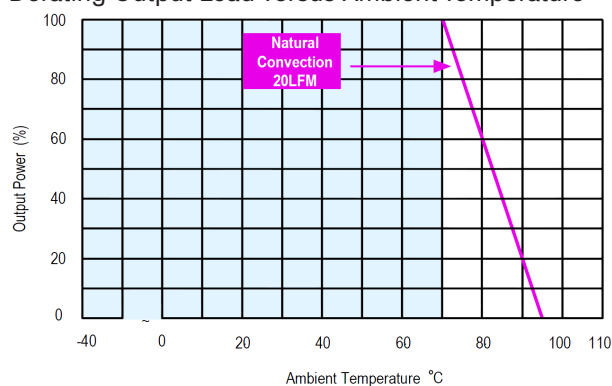
Efficiency versus Output Load



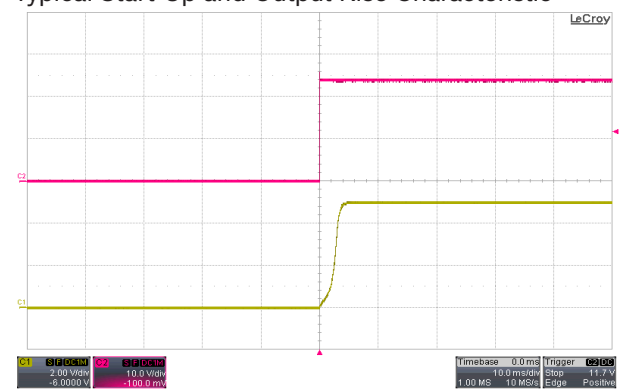
Efficiency versus Input Voltage



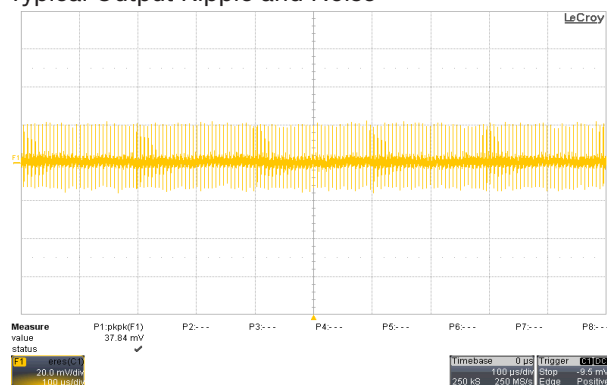
Derating Output Load versus Ambient Temperature



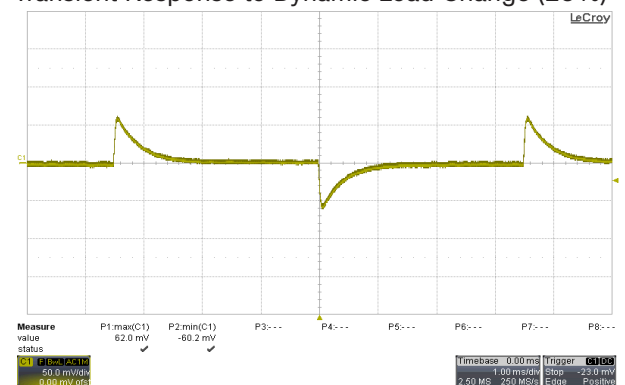
Typical Start-Up and Output Rise Characteristic



Typical Output Ripple and Noise



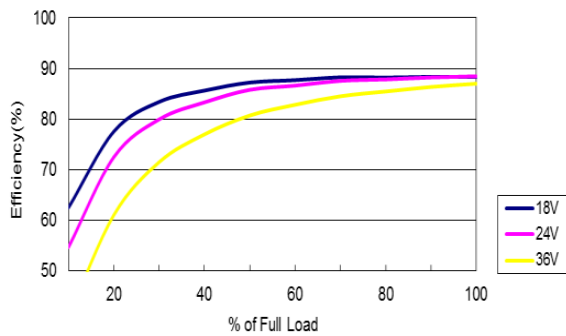
Transient Response to Dynamic Load Change (25%)



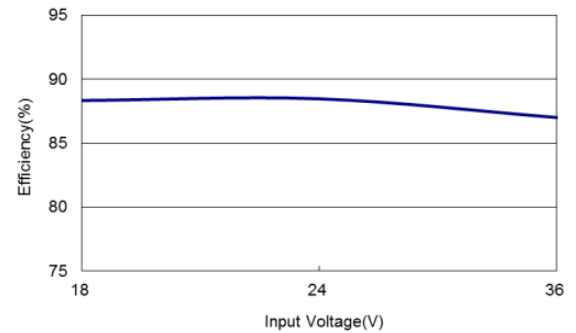
All specifications valid at nominal input voltage, full load and +25°C after warm-up time unless otherwise stated.

TDL 2-2412

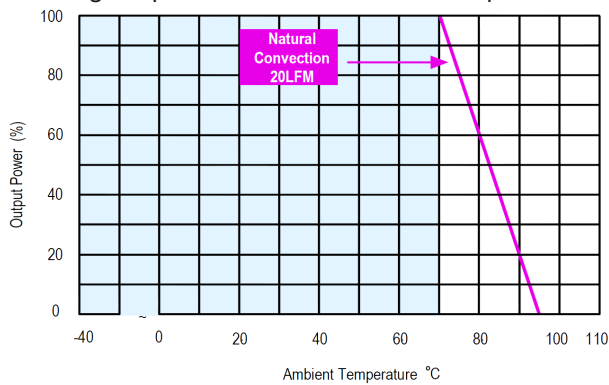
Efficiency versus Output Load



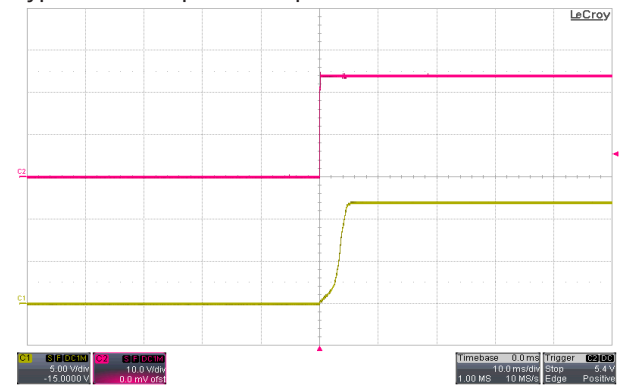
Efficiency versus Input Voltage



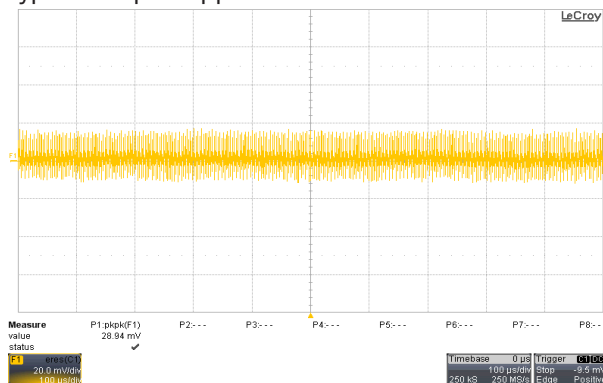
Derating Output Load versus Ambient Temperature



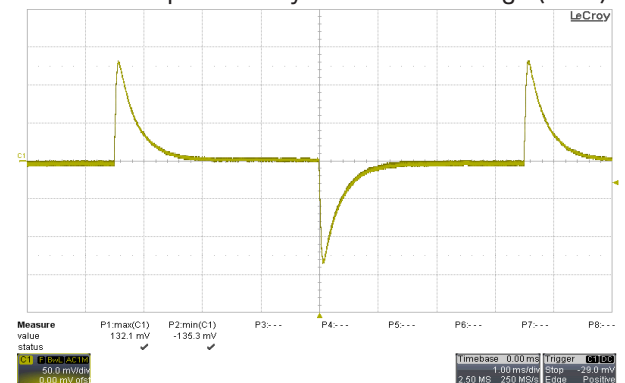
Typical Start-Up and Output Rise Characteristic



Typical Output Ripple and Noise



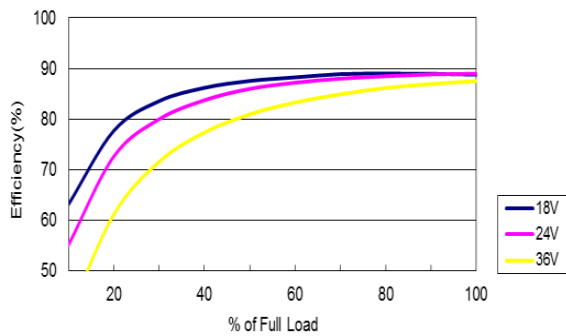
Transient Response to Dynamic Load Change (25%)



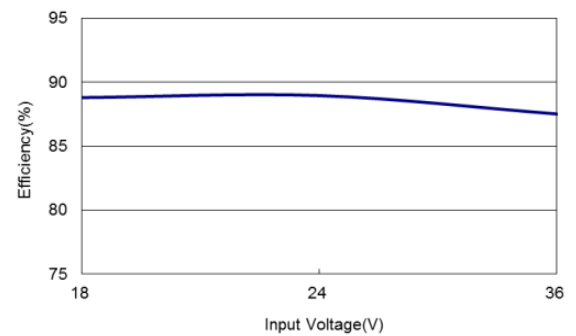
All specifications valid at nominal input voltage, full load and +25°C after warm-up time unless otherwise stated.

TDL 2-2413

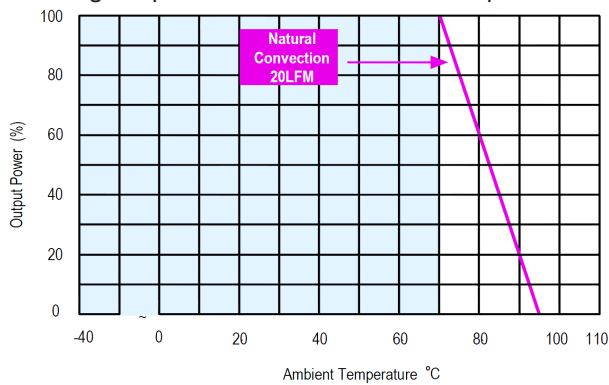
Efficiency versus Output Load



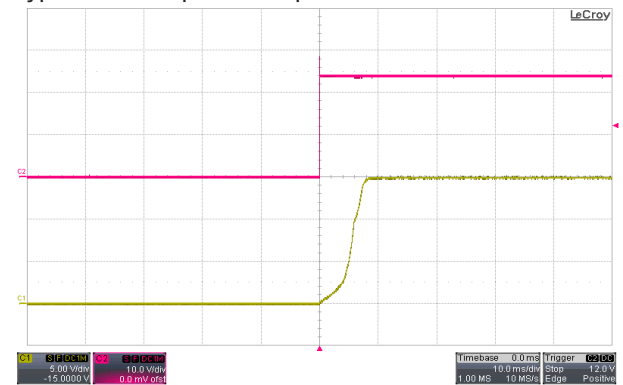
Efficiency versus Input Voltage



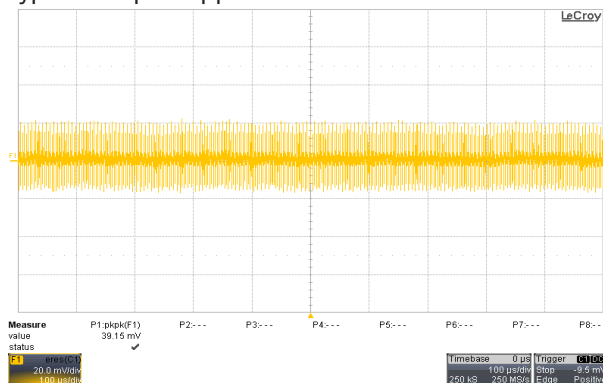
Derating Output Load versus Ambient Temperature



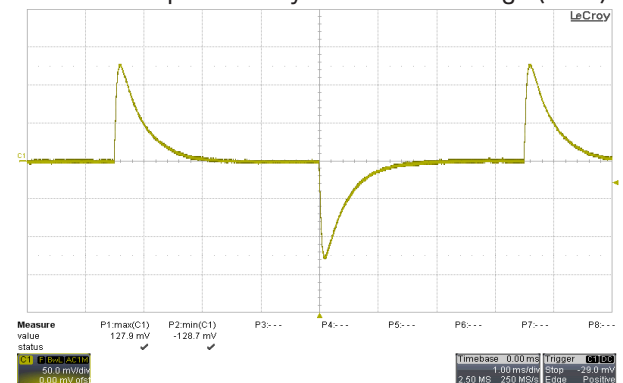
Typical Start-Up and Output Rise Characteristic



Typical Output Ripple and Noise



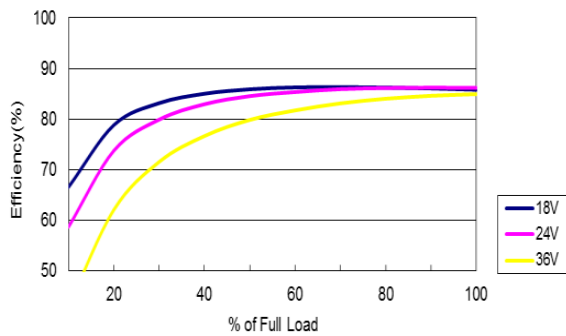
Transient Response to Dynamic Load Change (25%)



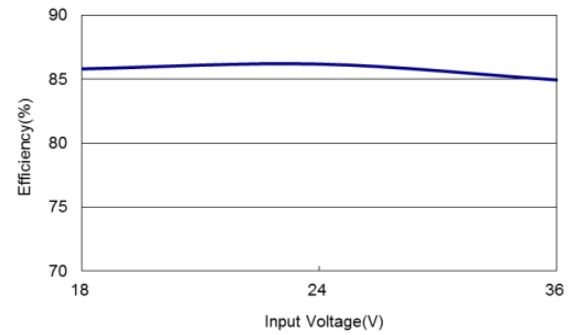
All specifications valid at nominal input voltage, full load and +25°C after warm-up time unless otherwise stated.

TDL 2-2421

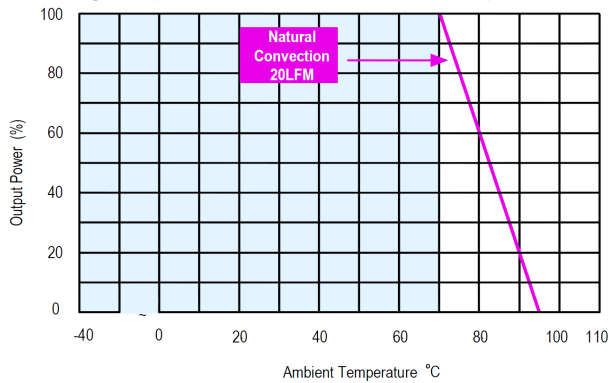
Efficiency versus Output Load



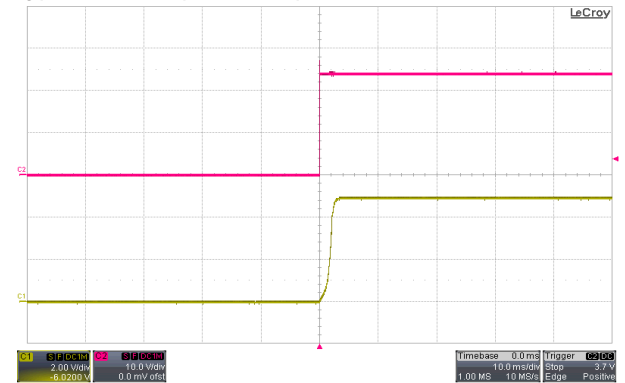
Efficiency versus Input Voltage



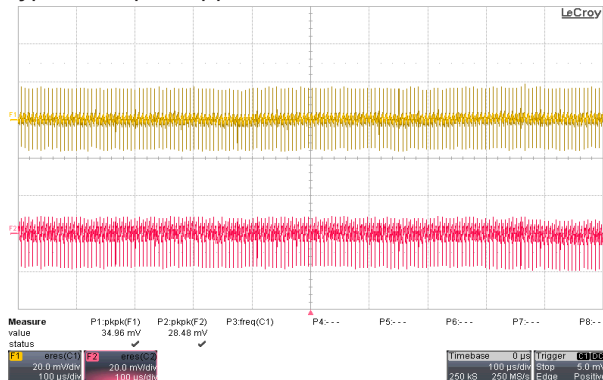
Derating Output Load versus Ambient Temperature



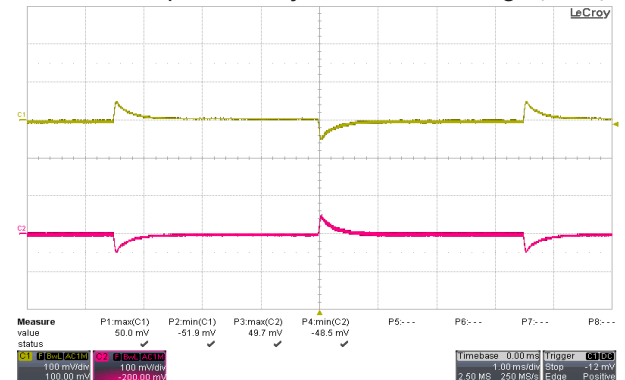
Typical Start-Up and Output Rise Characteristic



Typical Output Ripple and Noise



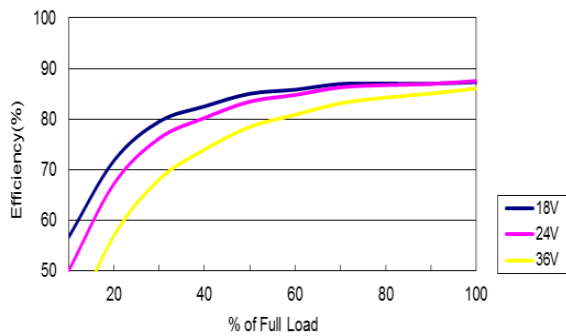
Transient Response to Dynamic Load Change (25%)



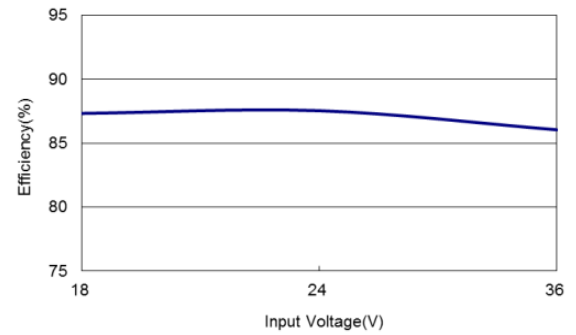
All specifications valid at nominal input voltage, full load and +25°C after warm-up time unless otherwise stated.

TDL 2-2422

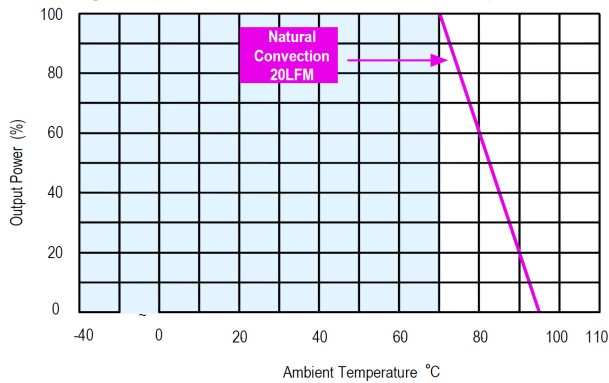
Efficiency versus Output Load



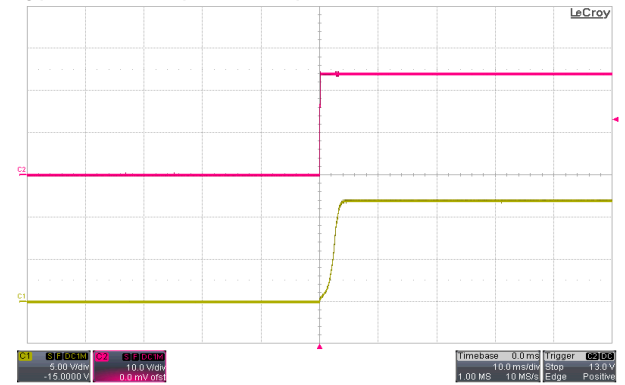
Efficiency versus Input Voltage



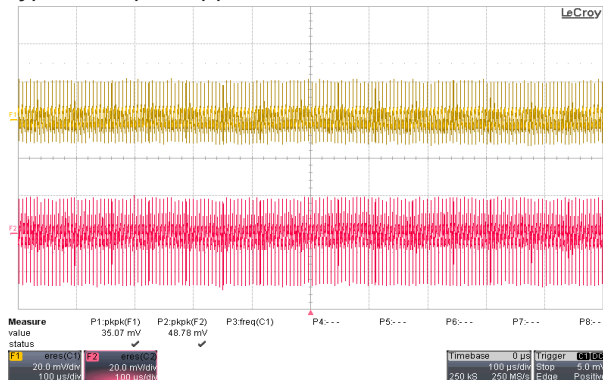
Derating Output Load versus Ambient Temperature



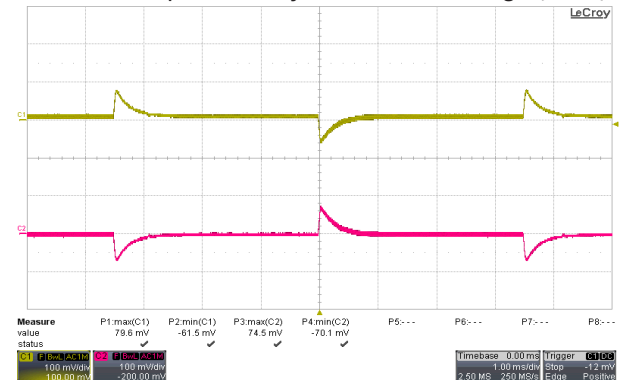
Typical Start-Up and Output Rise Characteristic



Typical Output Ripple and Noise



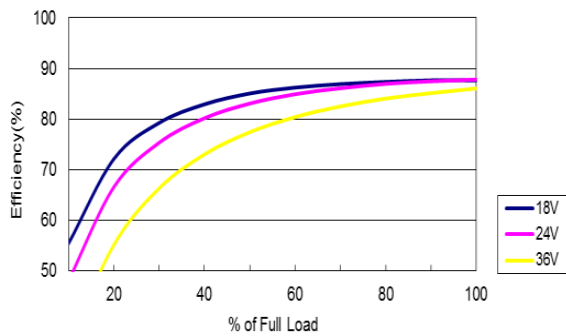
Transient Response to Dynamic Load Change (25%)



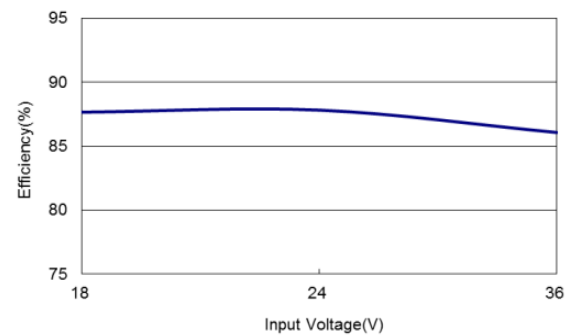
All specifications valid at nominal input voltage, full load and +25°C after warm-up time unless otherwise stated.

TDL 2-2423

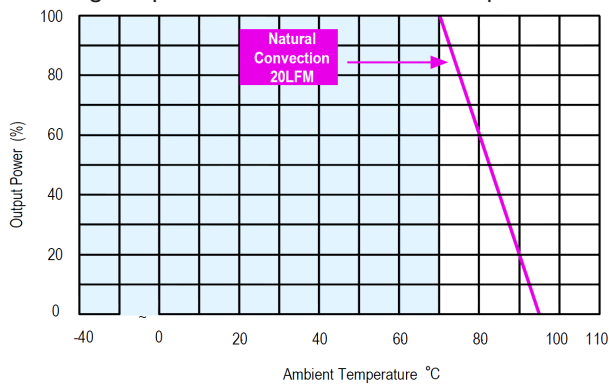
Efficiency versus Output Load



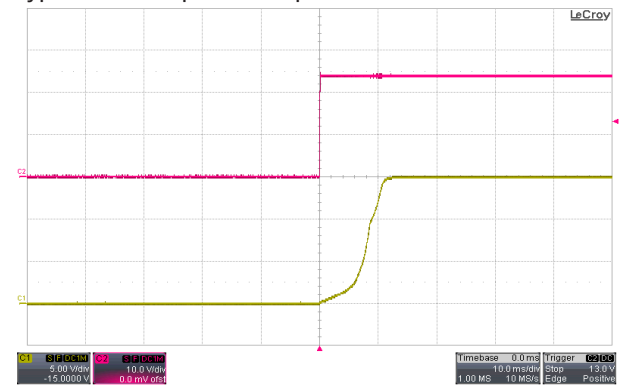
Efficiency versus Input Voltage



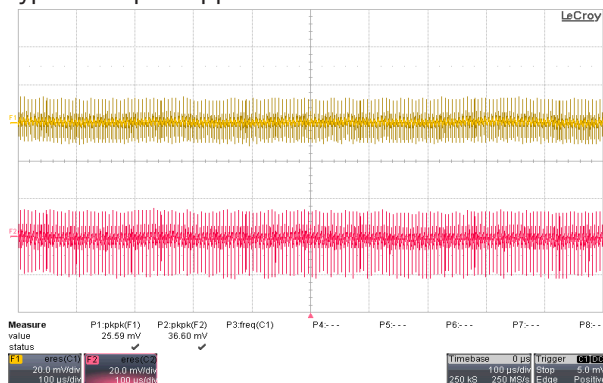
Derating Output Load versus Ambient Temperature



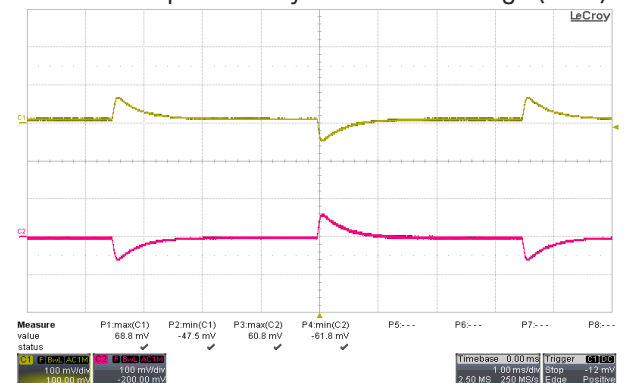
Typical Start-Up and Output Rise Characteristic



Typical Output Ripple and Noise



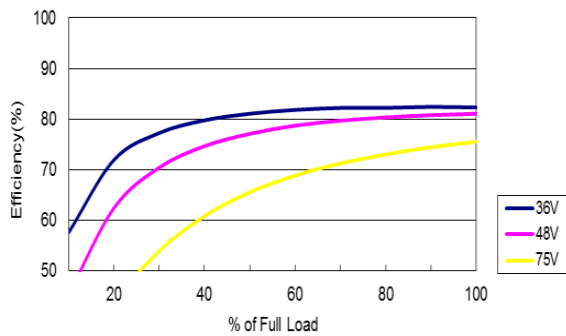
Transient Response to Dynamic Load Change (25%)



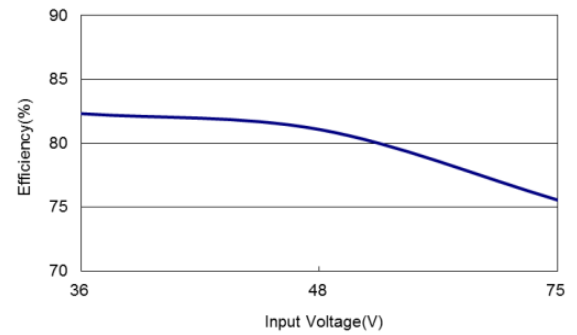
All specifications valid at nominal input voltage, full load and +25°C after warm-up time unless otherwise stated.

TDL 2-4810

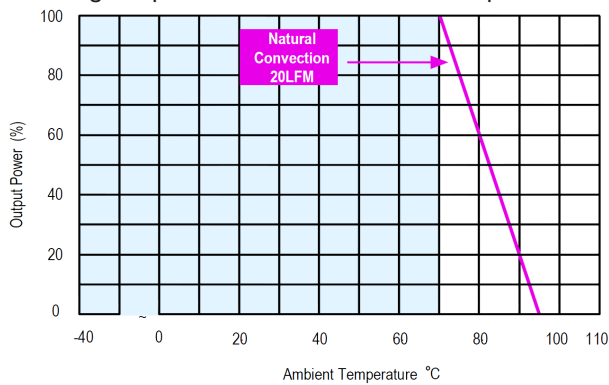
Efficiency versus Output Load



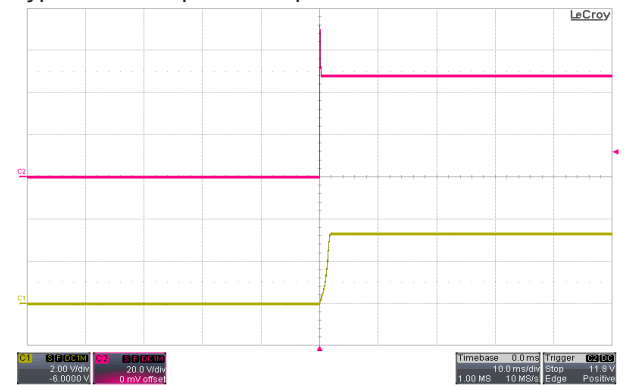
Efficiency versus Input Voltage



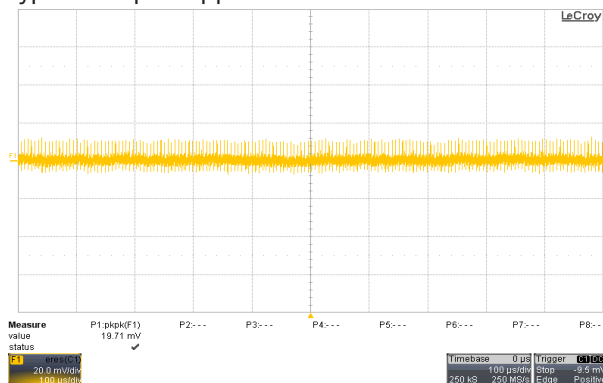
Derating Output Load versus Ambient Temperature



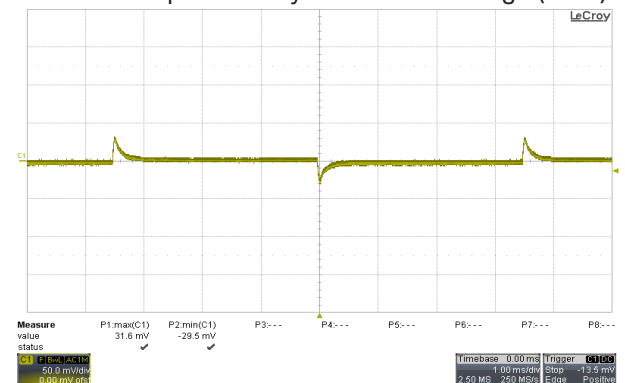
Typical Start-Up and Output Rise Characteristic



Typical Output Ripple and Noise



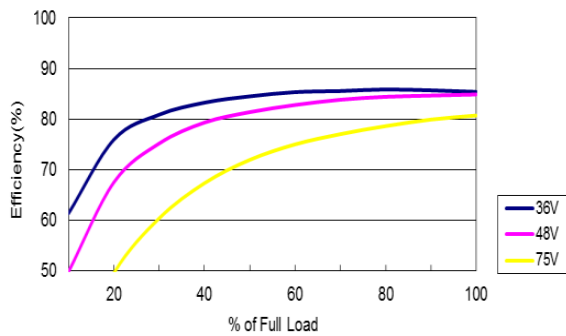
Transient Response to Dynamic Load Change (25%)



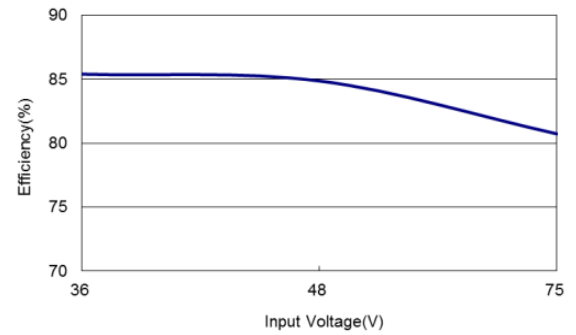
All specifications valid at nominal input voltage, full load and +25°C after warm-up time unless otherwise stated.

TDL 2-4811

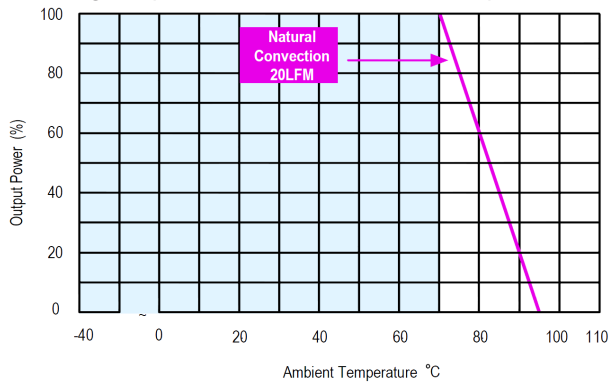
Efficiency versus Output Load



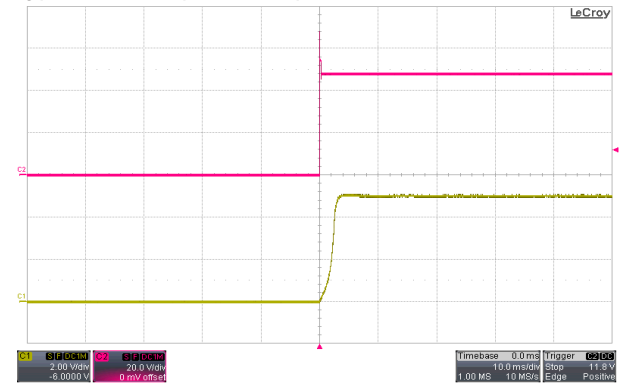
Efficiency versus Input Voltage



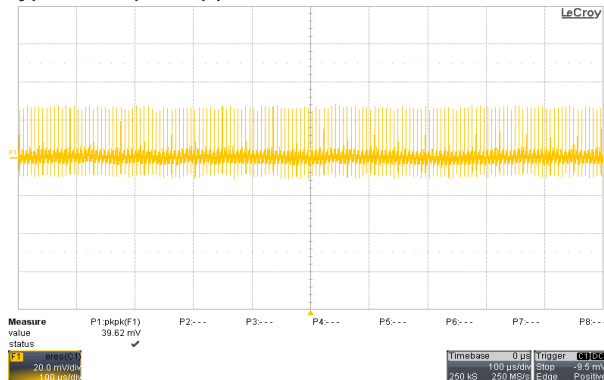
Derating Output Load versus Ambient Temperature



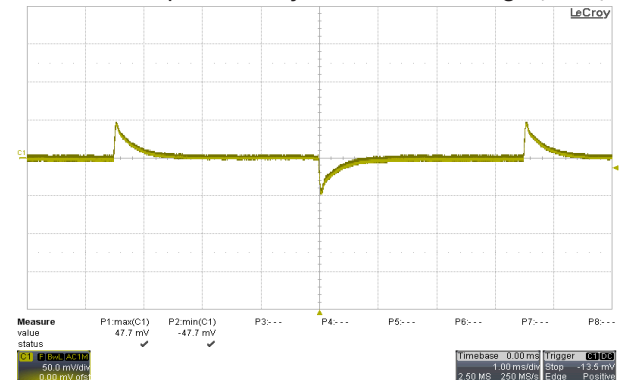
Typical Start-Up and Output Rise Characteristic



Typical Output Ripple and Noise



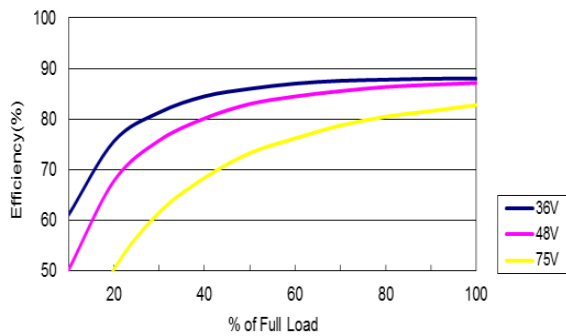
Transient Response to Dynamic Load Change (25%)



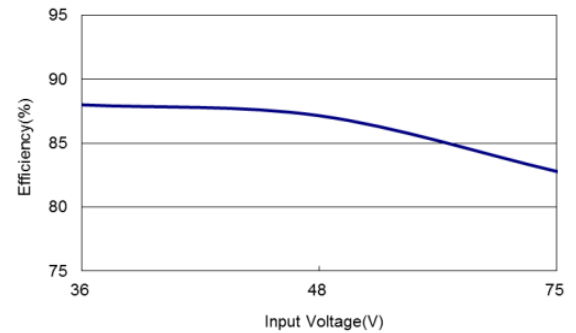
All specifications valid at nominal input voltage, full load and +25°C after warm-up time unless otherwise stated.

TDL 2-4812

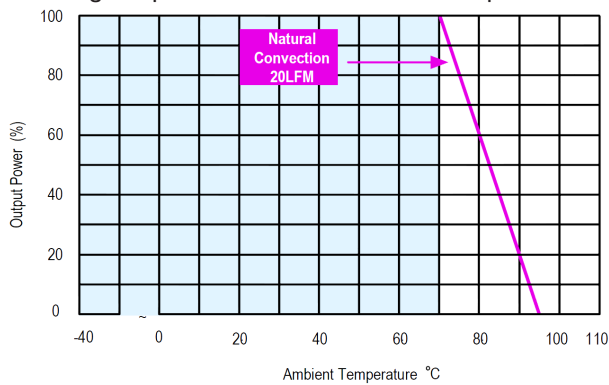
Efficiency versus Output Load



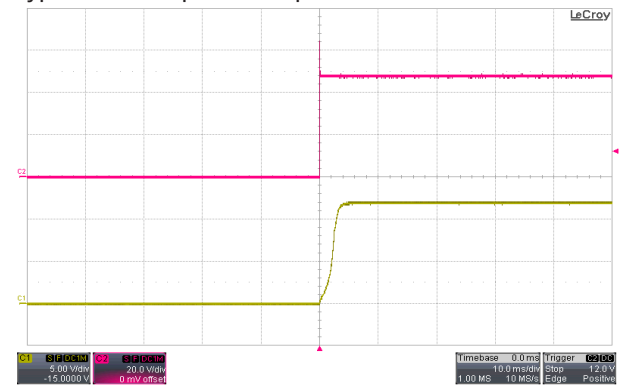
Efficiency versus Input Voltage



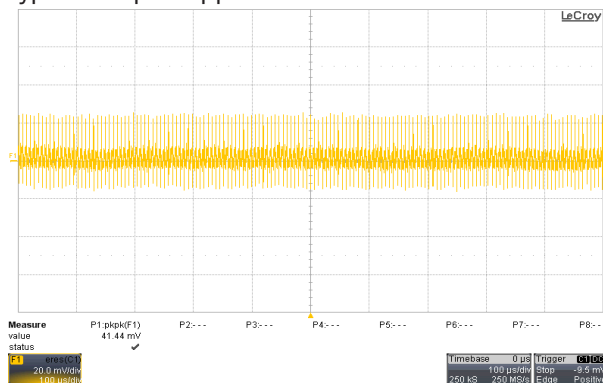
Derating Output Load versus Ambient Temperature



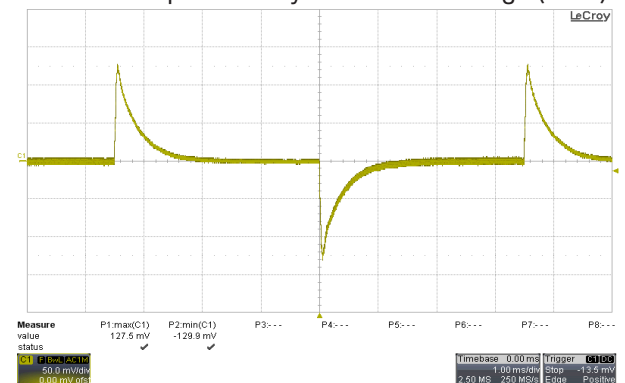
Typical Start-Up and Output Rise Characteristic



Typical Output Ripple and Noise



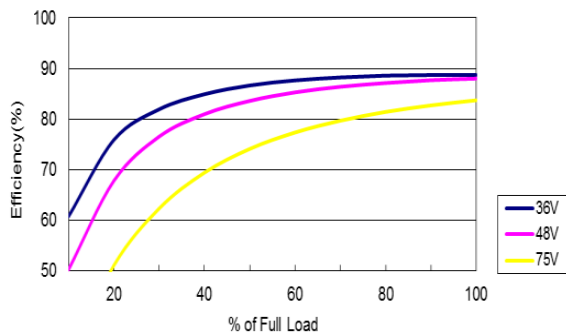
Transient Response to Dynamic Load Change (25%)



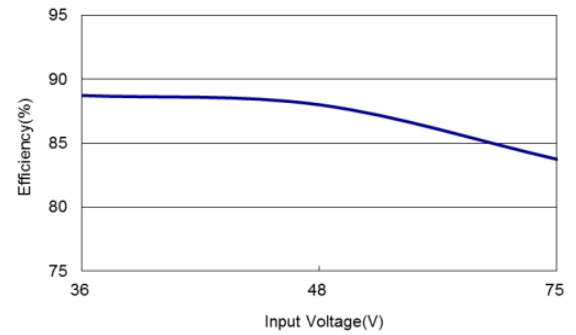
All specifications valid at nominal input voltage, full load and +25°C after warm-up time unless otherwise stated.

TDL 2-4813

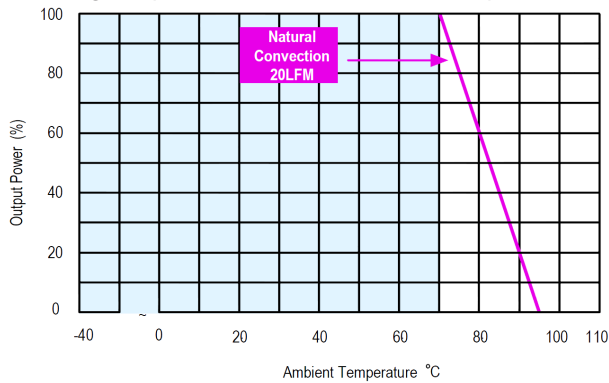
Efficiency versus Output Load



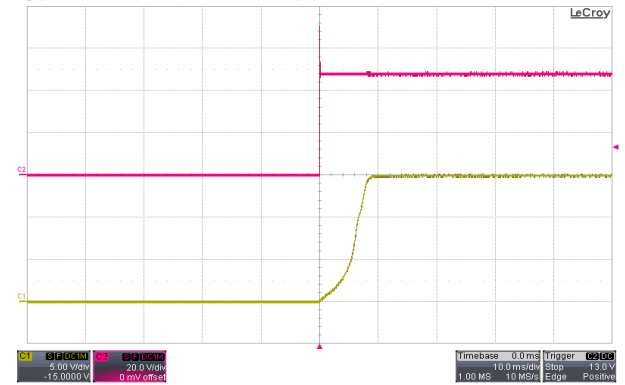
Efficiency versus Input Voltage



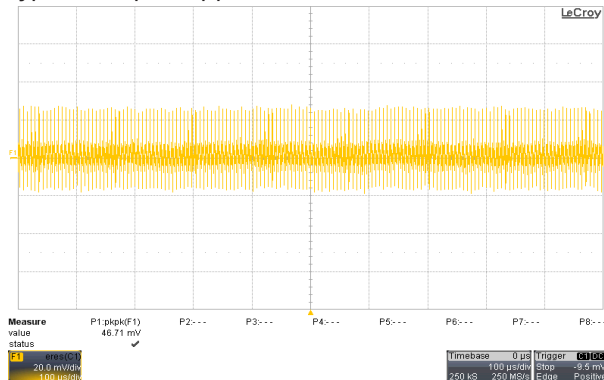
Derating Output Load versus Ambient Temperature



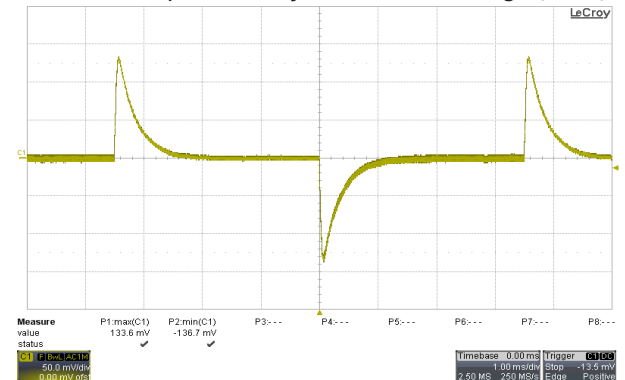
Typical Start-Up and Output Rise Characteristic



Typical Output Ripple and Noise



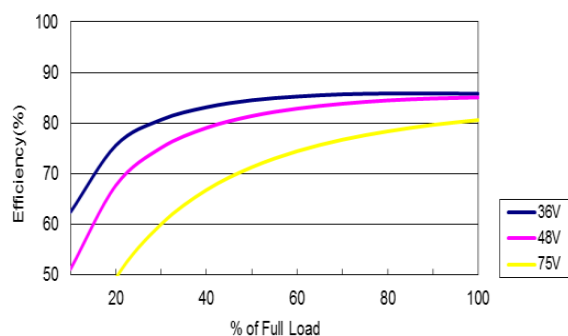
Transient Response to Dynamic Load Change (25%)



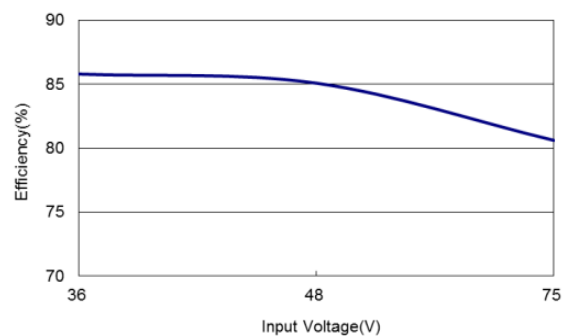
All specifications valid at nominal input voltage, full load and +25°C after warm-up time unless otherwise stated.

TDL 2-4821

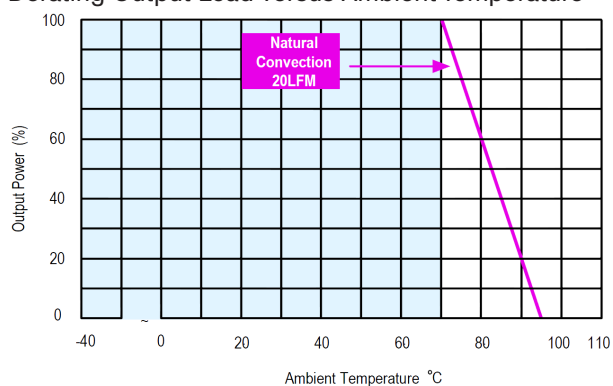
Efficiency versus Output Load



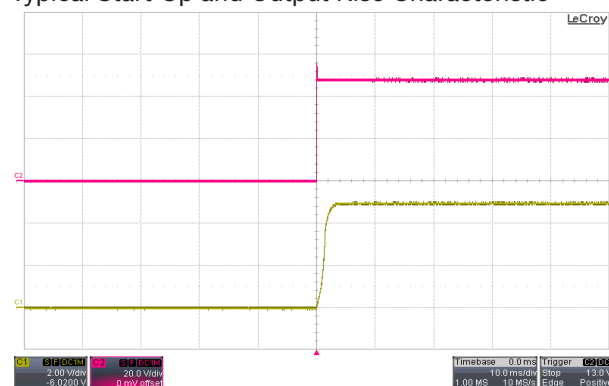
Efficiency versus Input Voltage



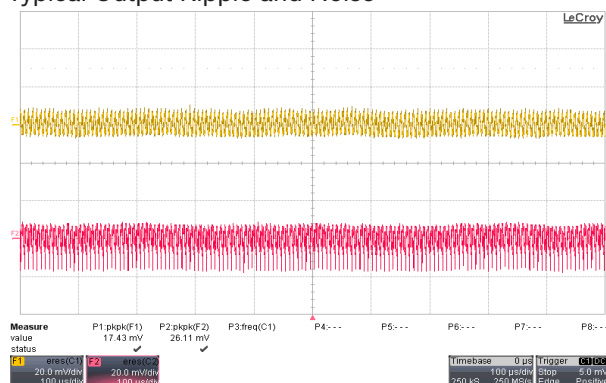
Derating Output Load versus Ambient Temperature



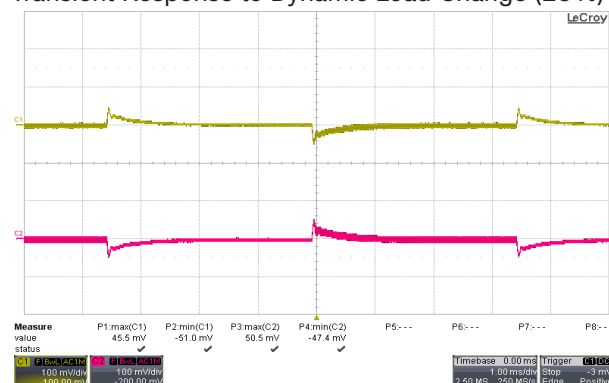
Typical Start-Up and Output Rise Characteristic



Typical Output Ripple and Noise



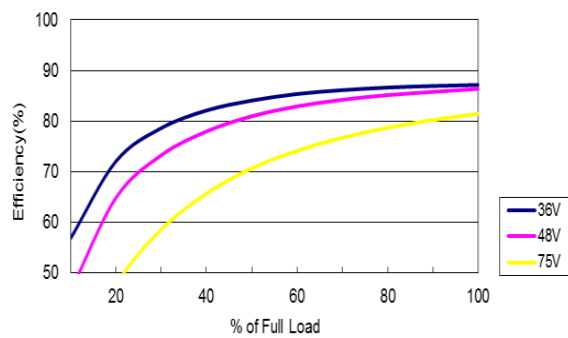
Transient Response to Dynamic Load Change (25%)



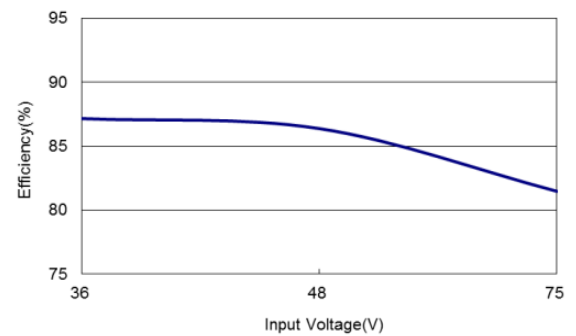
All specifications valid at nominal input voltage, full load and +25°C after warm-up time unless otherwise stated.

TDL 2-4822

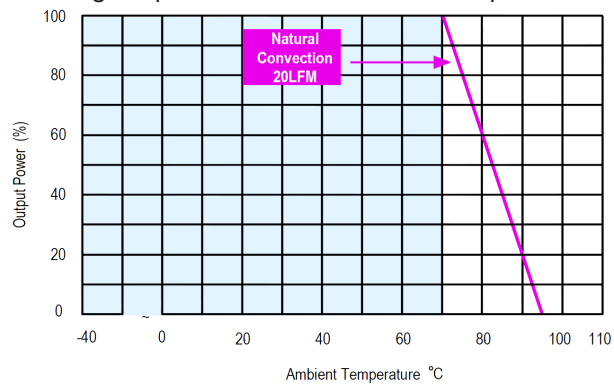
Efficiency versus Output Load



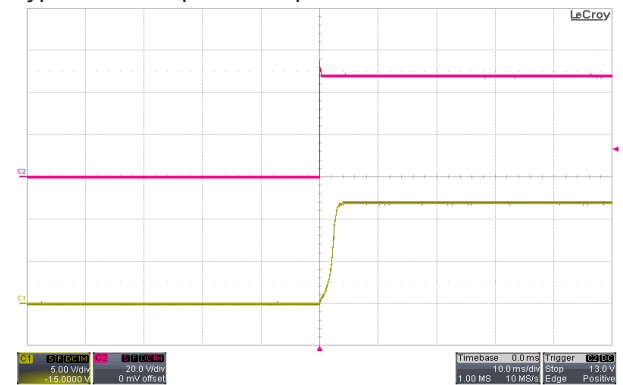
Efficiency versus Input Voltage



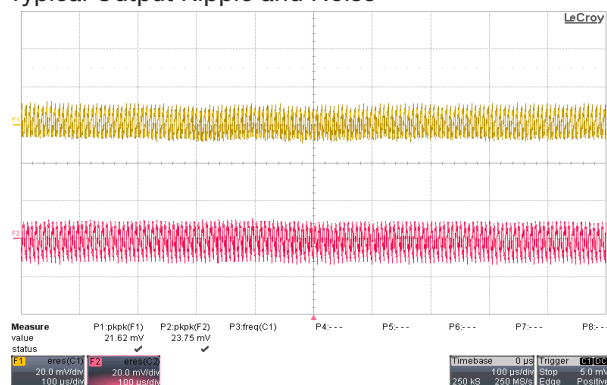
Derating Output Load versus Ambient Temperature



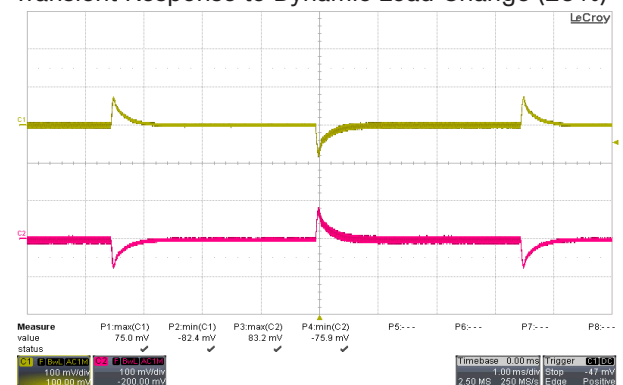
Typical Start-Up and Output Rise Characteristic



Typical Output Ripple and Noise



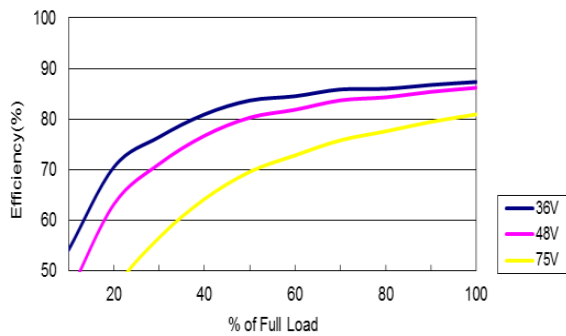
Transient Response to Dynamic Load Change (25%)



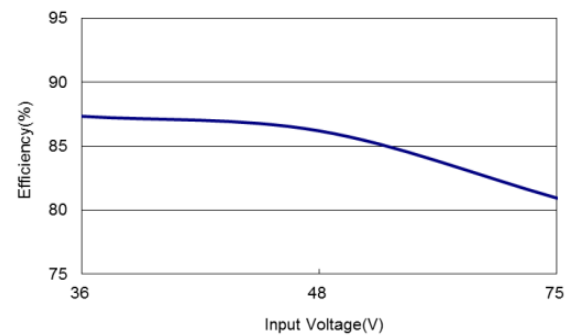
All specifications valid at nominal input voltage, full load and +25°C after warm-up time unless otherwise stated.

TDL 2-4823

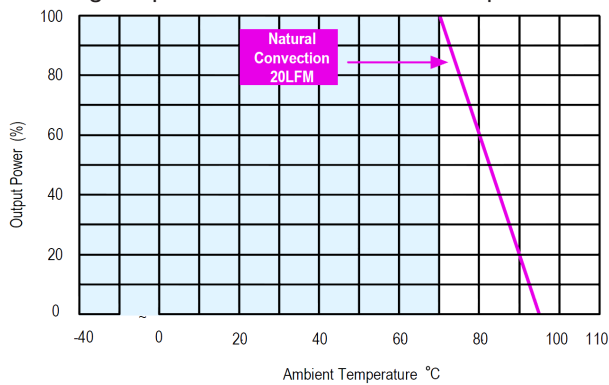
Efficiency versus Output Load



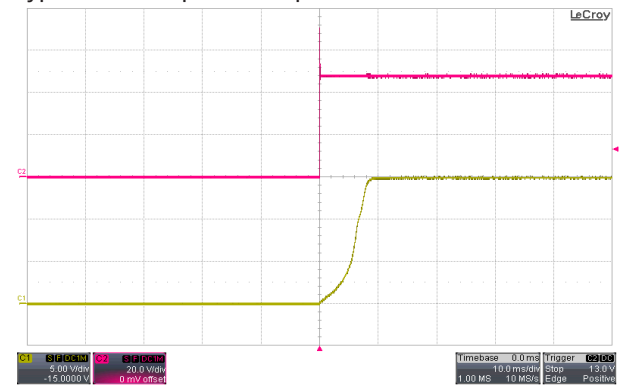
Efficiency versus Input Voltage



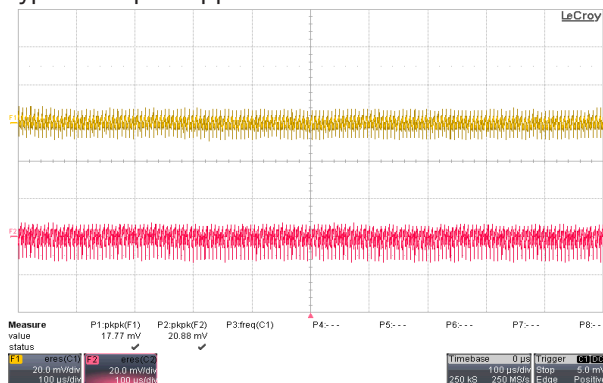
Derating Output Load versus Ambient Temperature



Typical Start-Up and Output Rise Characteristic



Typical Output Ripple and Noise



Transient Response to Dynamic Load Change (25%)

